

Part Number: 8060J2K50392KXB

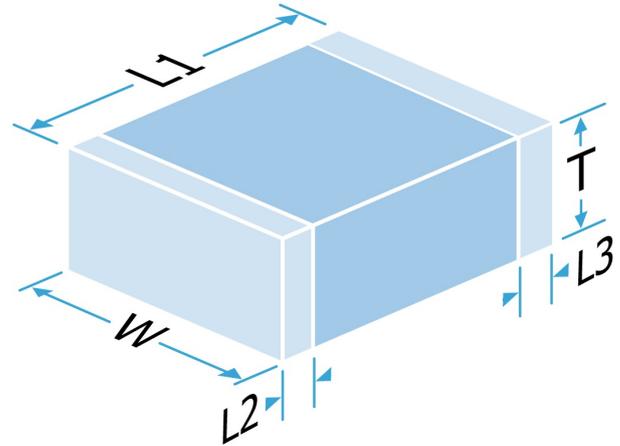
Description: 8060 2500Vdc 3.9nF ±10% X7R (2R1) (CTI ≥ 600)

A range of X7R MLC capacitors to suit a variety of applications. In a wide selection of chip sizes, rated voltages and terminations, including FlexiCap™, the world's first commercially available flexible termination.

WS2, WR2, WS3 and WR3 parts use StackiCap™ patented construction technology.

DR1, WR2 and WR3 parts have a voltage de-rating above 105°C.

Suffix code PXX or PX mandates the use of precious metal electrode (PME) materials. This may incur additional costs.



Mechanical Specification

Size Code	8060
Length (L1) in mm (")	20.3 ± 0.5 (0.80 ± 0.02)
Width (W) in mm (")	15.24 ± 0.50 (0.60 ± 0.02)
Thickness (T) in mm (")	2.5 Max (0.1 Max)
Minimum Termination Band (L2,L3) in mm (")	0.50 (0.020)
Maximum Termination Band (L2,L3) in mm (")	1.50 (0.060)
Termination Material	Nickel Barrier, Sn Plated Solder (RoHS compliant)
Solderability	IEC-60068-2-58
Packaging	Bulk
Conformal Coating	Not normally required

General Electrical Specification

Rated Voltage	2500Vdc
Nominal Capacitance Value	3.9nF
Capacitance Tolerance	±10%
Tangent of Loss Angle (Tan δ)	≤0.025
Capacitance and Tan δ Test Conditions	1.0Vrms @ 1kHz
Voltage Proof	3000Vdc
(Voltage applied for 5 secs max. @ 50mA max. charge current. 50% Max, RH)	
Min Insulation Resistance (IR)	100.00GOhm @ 100Vdc
Dielectric Classification	X7R (2R1) (CTI ≥ 600)
Rated Temperature Range	-55°C / +125°C
Maximum Capacitance Change over Temperature Range	No DC Voltage ±15%
Climatic Category (IEC)	Rated DC Voltage -
Ageing Characteristic	55/125/56
	<2% per decade (nominal capacitance is 1000 hour value)

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www.knowlescapacitors.com

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Data is correct to the best of our knowledge, errors and omissions excepted.

Date: Monday, July 21, 2025

20250721 103236073UTC

Multilayer Ceramic Chip Capacitor

Part Number: 8060J2K50392KXB

Description: 8060 2500Vdc 3.9nF ±10% X7R (2R1) (CTI ≥ 600)

Environmental

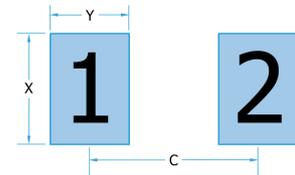
RoHS Compliant to 2011/65/EC as amended by 2015/863/EU	Compliant
REACH Compliant	247 compliant
California Proposition 65	No exposure risk

Board Layout

Knowles' conventional 2-terminal chip capacitors can generally be mounted using pad designs in accordance with international specification IPC-7351, Generic Requirements for Surface Mount Design and Land Pattern Standards, but there are some other factors that have been shown to reduce mechanical stress, such as reducing the pad width to less than the chip width. In addition, the position of the chip on the board should be considered.

IPC-7351 pad design

	8060	
C	19.50mm	0.768"
Y	2.05mm	0.081"
X	15.75mm	0.620"

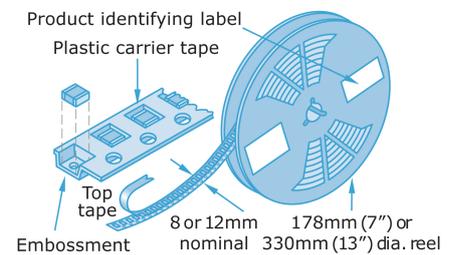


Some high voltage parts may require modifications to the board layout and/or the addition of a conformal coating to prevent flashover, especially under high humidity conditions. Board cleanliness and environmental conditions can also impact this. Refer to application note AN0043 for further information.

Packaging

Tape packaging information for tape-and-reel parts:

Tape and reel packing of surface mounting chip capacitors for automatic placement are in accordance with IEC60286-3.



Soldering

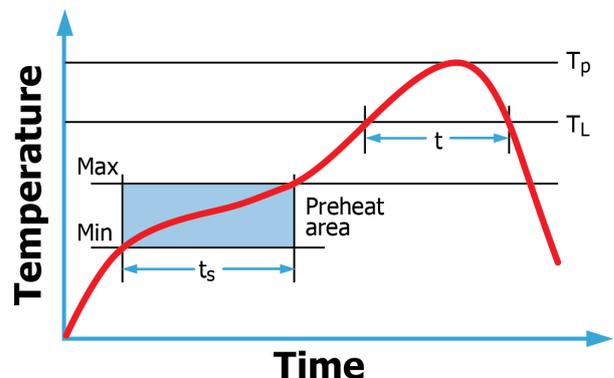
Reflow solder in accordance with IPC-A-610. Recommended reflow profile as laid down in IPC/JEDEC J-STD-020.

Wave soldering is also possible, but care must be taken for case sizes 1210 and larger and component thickness >1.0mm. Trials are encouraged.

Hand soldering is not recommended and can lead to component damage through thermal shock.

PdAg terminations are primarily intended for conductive epoxy attachment - they may be suitable for soldering but trials are recommended.

Application notes with mounting and handling guidance are available on request.



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DC Bias Characteristics

Insufficient data exists to automatically calculate dc bias data for this specific part number.

Please contact your local sales office and our engineering teams will be happy to look at requests for part specific data.

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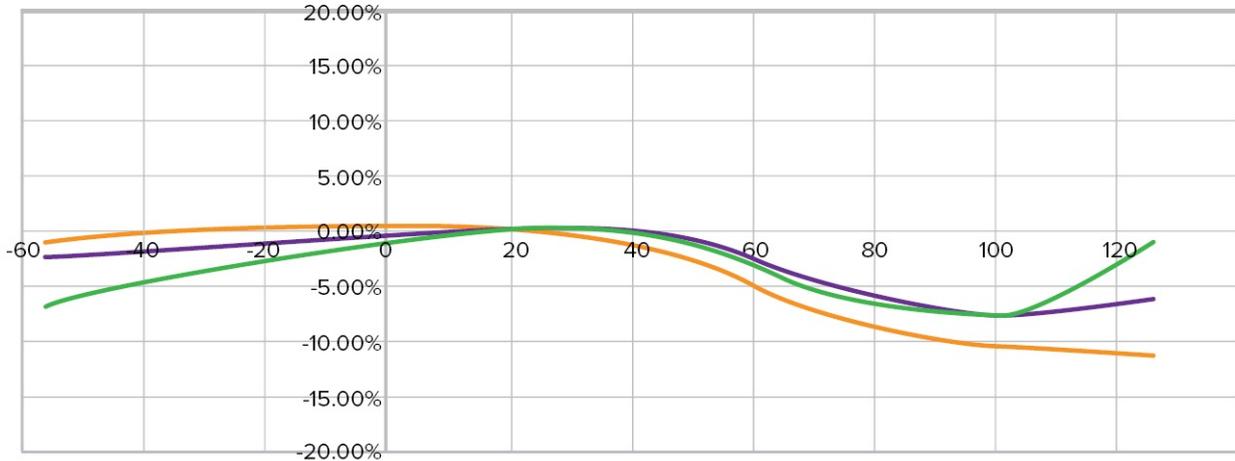
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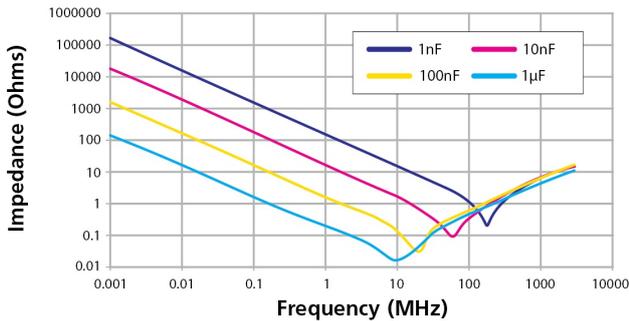
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Typical Capacitance Change vs Temperature

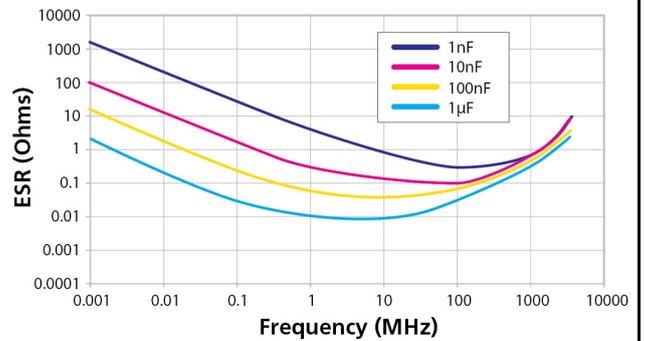


Typical TC Curves for X7R capacitors showing different dielectric types

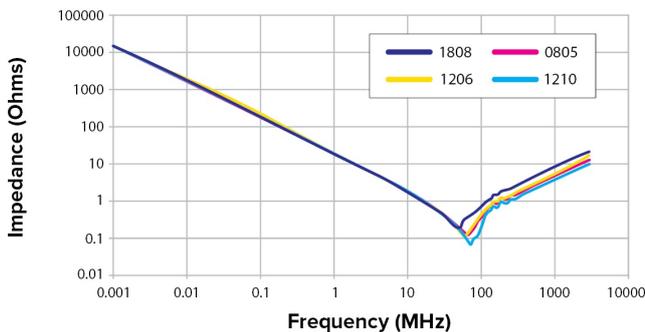
Stable X7R Dielectric



Stable X7R Dielectric



Stable X7R Dielectric — 10nF



Typical Performance Data - X7R

For part specific data, please contact your local sales office
This data is for reference only and does not constitute a specification.

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