

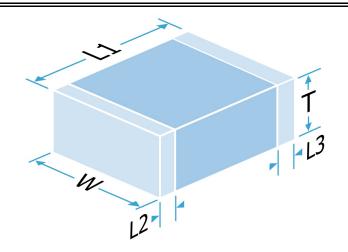
Multilayer Ceramic Chip Capacitor

Part Number: CDR31BX103AKZM

0805 50Vdc 10nF \pm 10% X7R (2X1) BX - Contains Lead **Description:**

The MIL-PRF-55681 qualified parts range refers to ceramic capacitors that meet the rigorous standards set forth by the qualifying activity (DLA). This range is designed for use in military and aerospace applications, where high reliability, performance, and environmental endurance are critical.

MIL-PRF-55681 qualified ceramic capacitors are manufactured using advanced ceramic materials and production techniques that meet or exceed military standards. The construction ensures minimal failure rates, even under the most challenging operational conditions.



Mechanical Specification

Size Code

Length (L1) in mm (")

Width (W) in mm (")

Thickness (T) in mm (")

Minimum Termination Band (L2,L3) in mm (")

Maximum Termination Band (L2,L3) in mm (")

Termination Material

Solderability Packaging

0805

 $2.0 \pm 0.20 (0.079 \pm 0.008)$

 $1.25 \pm 0.20 \ (0.049 \pm 0.008)$

1.3 Max (0.051 Max)

0.25 (0.010)

0.75 (0.030)

Nickel Barrier, Sn/Pb Plated Solder (Min 10% Lead, non RoHS)

Per MIL-STD-202, Method 208

Tape and Reel, 3000 per reel

General Electrical Specification

Rated Voltage

Nominal Capacitance Value

Capacitance Tolerance

Tangent of Loss Angle (Tan δ)

Capacitance and Tan δ Test Conditions

Voltage Proof

(Voltage applied for 5 secs max. @ 50mA max. charge current. 50% Max, RH)

Min Insulation Resistance (IR)

Dielectric Classification

Rated Temperature Range

Maximum Capacitance Change over Temperature Range

Climatic Category (IEC)

Ageing Characteristic

50Vdc

10nF

±10%

≤0.025

1.0Vrms @ 1kHz

125Vdc

66.67GOhm @ 50Vdc

X7R (2X1) BX - Contains Lead

-55°C / +125°C

No DC Voltage ±15%

Rated DC Voltage +15% / -25%

for this part may differ and is available at http://www.knowlescapacitors.com or by contacting us.

<2% per decade (nominal capacitance is 1000 hour value)

This datasheet is for a standard item and is confirmed valid on the date generated, the latest published data

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The information contained on this drawing is confidential and may not be copied in whole or part in any form or disclosed to a third party without the consenof Knowles and any customer mentioned within this

specification.

Data is correct to the best of our knowledge, errors and

Date: Friday, October 24, 2025



Multilayer Ceramic Chip Capacitor

Part Number: CDR31BX103AKZM

Description: 0805 50Vdc 10nF ±10% X7R (2X1) BX - Contains Lead

Environmental

RoHS Compliant to 2011/65/EC as amended by 2015/863/EU

Non Compliant

REACH Compliant

Contains 1 to 8% w/w Lead Titanium Oxide (PbTiO3, CAS 12060-00-3) and 0.1 to 1.0% w/w Lead (Pb, CAS 7439-92-1)

California Proposition 65

Risk of exposure to lead (CAS 7439-92-1)

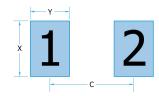
Board Layout

Knowles' conventional 2-terminal chip capacitors can generally be mounted using pad designs in accordance with international specification IPC-7351, Generic Requirements for Surface Mount Design and Land Pattern Standards, but there are some other factors that have been shown to reduce mechanical stress, such as reducing the pad width to less than the chip width. In addition, the position of the chip on the board should be considered.

Some high voltage parts may require modifications to the board layout and/or the addition of a conformal coating to prevent flashover, especially under high humidity conditions. Board cleanliness and environmental conditions can also impact this. Refer to application note AN0043 for further information.

IPC-7351 pad design

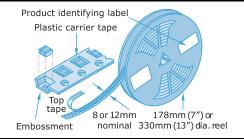
	0805	
С	1.80mm	0.071"
Υ	1.15mm	0.045"
X	1.45mm	0.057"



Packaging

Tape packaging information for tape-and-reel parts:

Tape and reel packing of surface mounting chip capacitors for automatic placement are in accordance with IEC60286-3.



Soldering

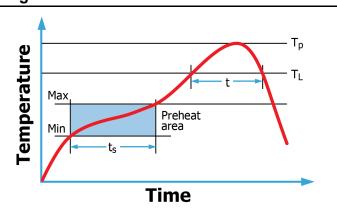
Reflow solder in accordance with IPC-A-610. Recommended reflow profile as laid down in IPC/JEDEC J-STD-020.

Wave soldering is also possible, but care must be taken for case sizes 1210 and larger and component thickness >1.0mm. Trials are encouraged.

Hand soldering is not recommended and can lead to component damage through thermal shock.

PdAg terminations are primarily intended for conductive epoxy attachment - they may be suitable for soldering but trials are recommended.

DLI



Application notes with mounting and handling guidance are available on request.

Johanson MFG

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Novacap

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Voltronics