

# Molding Type Module IGBT, 2-in-1 Package, 600 V and 400 A



PRIMARY CHARACTERISTICS						
600 V						
400 A						
1.60 V						
8 kHz to 30 kHz						
Dual INT-A-PAK						
Half bridge						

#### **FEATURES**

- Low V<sub>CE(on)</sub> trench IGBT technology
- · Low switching losses
- 5 µs short circuit capability
- V<sub>CE(on)</sub> with positive temperature coefficient
- Maximum junction temperature 175 °C
- · Low inductance case
- Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **TYPICAL APPLICATIONS**

- UPS
- · Switching mode power supplies
- Electronic welders

#### **DESCRIPTION**

Vishay's IGBT power module provides ultralow conduction loss as well as short circuit ruggedness. It is designed for applications such as UPS and SMPS.

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Collector to emitter voltage	V <sub>CES</sub>		600	V		
Gate to emitter voltage	V <sub>GES</sub>		± 20	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		
Collector current		T <sub>C</sub> = 25 °C	530			
Collector current	IC	T <sub>C</sub> = 80 °C	400			
Pulsed collector current	I <sub>CM</sub> <sup>(1)</sup>	t <sub>p</sub> = 1 ms	800	Α		
Diode continuous forward current	I <sub>F</sub>		400			
Diode maximum forward current	I <sub>FM</sub>		800			
Maximum power dissipation	P <sub>D</sub>	T <sub>J</sub> = 175 °C	1600	W		
Short circuit withstand time	t <sub>SC</sub>	T <sub>J</sub> = 125 °C	5	μs		
l <sup>2</sup> t-value, diode	l <sup>2</sup> t	$V_R = 0 \text{ V}, t = 10 \text{ ms}, T_J = 125  ^{\circ}\text{C}$	10 900	A <sup>2</sup> s		
RMS isolation voltage	V <sub>ISOL</sub>	f = 50 Hz, t = 1 min	2500	V		

#### Note

<sup>(1)</sup> Repetitive rating: pulse width limited by maximum junction temperature

IGBT ELECTRICAL SPECIFICATIONS (T <sub>C</sub> = 25 °C unless otherwise noted)						
Collector to emitter breakdown voltage	V <sub>(BR)CES</sub>	$V_{GE} = 0 \text{ V}, I_{C} = 2 \text{ mA}, T_{J} = 25 \text{ °C}$	600	-	-	
Collector to emitter saturation voltage	\/	$V_{GE} = 15 \text{ V}, I_{C} = 400 \text{ A}, T_{J} = 25 ^{\circ}\text{C}$	-	1.6	2.05	\/
Collector to enfitter saturation voltage	V <sub>CE(on)</sub>	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 400 A, T <sub>J</sub> = 175 °C	-	2.0	-	\ \ \
Gate to emitter threshold voltage	V <sub>GE(th)</sub>	$V_{CE} = V_{GE}$ , $I_C = 4$ mA, $T_J = 25$ °C	4.0	-	6.5	
Zero gate voltage collector current	I <sub>CES</sub>	$V_{CE} = V_{CES}$ , $V_{GE} = 0$ V, $T_{J} = 25$ °C	-	-	5.0	mA
Gate to emitter leakage current	I <sub>GES</sub>	$V_{GE} = V_{GES}$ , $V_{CE} = 0$ V, $T_{J} = 25$ °C	-	-	400	nA



SWITCHING CHARACTERISTICS	<b>3</b>					
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t <sub>d(on)</sub>		-	35	-	
Rise time	t <sub>r</sub>		-	70	-	ns
Turn-off delay time	t <sub>d(off)</sub>	$V_{CC} = 400 \text{ V}, I_{C} = 400 \text{ A}, R_{q} = 1.3 \Omega,$	-	180	-	
Fall time	t <sub>f</sub>	$V_{GE} = \pm 15 \text{ V}, T_{J} = 25 \text{ °C}$	-	75	-	
Turn-on switching loss	E <sub>on</sub>		-	14.1	-	1
Turn-off switching loss	E <sub>off</sub>		-	10.0	-	- mJ
Turn-on delay time	t <sub>d(on)</sub>		-	37	-	
Rise time	t <sub>r</sub>		-	72	-	ns
Turn-off delay time	t <sub>d(off)</sub>	$V_{CC} = 400 \text{ V}, I_{C} = 400 \text{ A}, R_{q} = 1.3 \Omega,$	-	220	-	
Fall time	t <sub>f</sub>	$V_{GE} = \pm 15 \text{ V}, T_{J} = 175 \text{ °C}$	-	84	-	
Turn-on switching loss	E <sub>on</sub>		-	23.2	-	m l
Turn-off switching loss	E <sub>off</sub>		-	16.8	-	mJ
Input capacitance	C <sub>ies</sub>		-	30.8	-	
Output capacitance	C <sub>oes</sub>	$V_{GE} = 0 \text{ V}, V_{CE} = 30 \text{ V}, f = 1.0 \text{ MHz}$	-	2.12	-	nF
Reverse transfer capacitance	C <sub>res</sub>		-	0.92	-	
SC data	I <sub>SC</sub>	$\begin{array}{c} t_{SC} \leq 5 \; \mu s, \; V_{GE} = 15 \; V, \; T_{J} = 125 \; ^{\circ}C, \\ V_{CC} = 360 \; V, \; V_{CEM} \leq 600 \; V \end{array}$	-	TBD	-	Α
Internal gate resistance	R <sub>gint</sub>		-	1.3	-	Ω
Stray inductance	L <sub>CE</sub>		-	-	20	nΗ
Module lead resistance, terminal to chip	R <sub>CC'+EE'</sub>	T <sub>C</sub> = 25 °C	-	0.35	-	mΩ

<b>DIODE ELECTRICAL SPECIFICATIONS</b> (T <sub>C</sub> = 25 °C unless otherwise noted)								
PARAMETER	SYMBOL	TEST CONDITI	MIN.	TYP.	MAX.	UNITS		
Diode forward voltage	V-	I <sub>E</sub> = 400 A	$T_J = 25  ^{\circ}C$	ı	1.38	1.80	V	
Diode forward voltage	VF	forward voltage $V_F = I_F = 0$	IF = 400 A	T <sub>J</sub> = 125 °C	-	1.41	-	V
Diada rayaraa raaayany aharga	Q <sub>rr</sub>		T <sub>J</sub> = 25 °C	-	15.5	-		
Diode reverse recovery charge		Q <sub>rr</sub>		T <sub>J</sub> = 125 °C	-	28.5	-	μC
Diada paak vayawa vaasyaw ayyawa	I <sub>rr</sub>	$I_F = 400 \text{ A}, V_R = 300 \text{ V},$	T <sub>J</sub> = 25 °C	-	265	-	^	
Diode peak reverse recovery current		<sup>1</sup> rr	t $I_{rr}$ $dI/dt = -7000 A/\mu s$ , $V_{GF} = -15 V$	T <sub>J</sub> = 125 °C	-	335	-	A
Diada wayawa waaayam anaway	E <sub>rec</sub>	GE -	T <sub>J</sub> = 25 °C	-	3.5	-		
Diode reverse recovery energy		⊏ <sub>rec</sub>		T <sub>J</sub> = 125 °C	-	7.5	-	mJ

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Operating junction temperature	range	TJ		-	-	175	°C
Storage temperature range		T <sub>Stg</sub>		-40	-	125	
Junction to case	GBT	6		-	-	0.094	
per ½ module	Diode	$R_{thJC}$		-	-	0.158	K/W
Case to sink		R <sub>thCS</sub>	Conductive grease applied	-	0.035	-	
Mounting toward			Power terminal screw: M6		2.5 to 5.0	)	Nima
Mounting torque			Mounting screw: M6	3.0 to 5.0		Nm	
Weight					300		g





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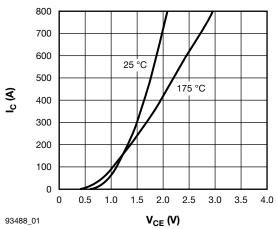


Fig. 1 - IGBT Typical Output Characteristics  $V_{GE} = 15 \text{ V}$ 

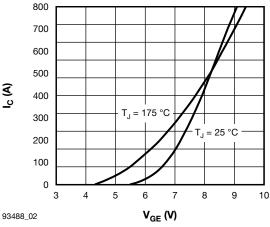


Fig. 2 - IGBT Typical Transfer Characteristics  $V_{\text{CE}} = 20 \text{ V}$ 

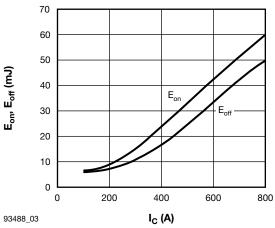


Fig. 3 - IGBT Switching Loss vs. Collector Current V<sub>CC</sub> = 600 V, R<sub>g</sub> = 1.3  $\Omega$ , V<sub>GE</sub> =  $\pm$  15 V, T<sub>J</sub> = 175 °C

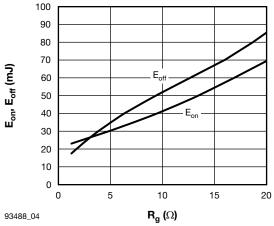
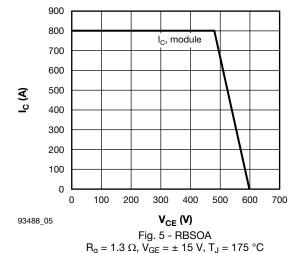


Fig. 4 - Switching Loss vs. Gate Resistor  $V_{CE}$  = 600 V,  $I_{C}$  = 400 A,  $V_{GE}$  =  $\pm$  15 V,  $T_{J}$  = 175  $^{\circ}C$ 



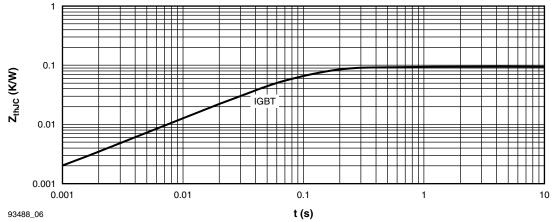
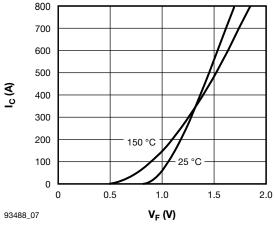


Fig. 6 - IGBT Transient Thermal Impedance



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Fig. 7 - Forward Characteristics of Diode

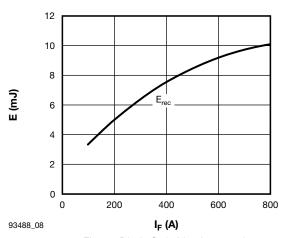


Fig. 8 - Diode Switching Loss vs. I<sub>F</sub>  $V_{CC}$  = 600 V,  $R_g$  = 1.3  $\Omega,\,V_{GE}$  = - 15 V,  $T_J$  = 125 °C

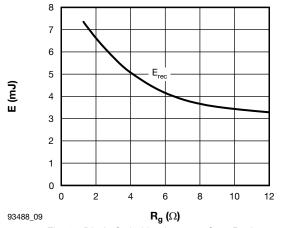


Fig. 9 - Diode Switching Loss vs. Gate Resistance  $V_{CC} = 600 \text{ V}$ ,  $I_{C} = 400 \text{ A}$ ,  $V_{GE} = -15 \text{ V}$ ,  $T_{J} = 125 \,^{\circ}\text{C}$ 

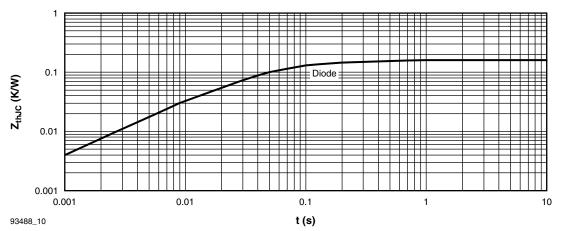
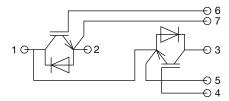


Fig. 10 - Diode Transient Thermal Impedance

#### **CIRCUIT CONFIGURATION**

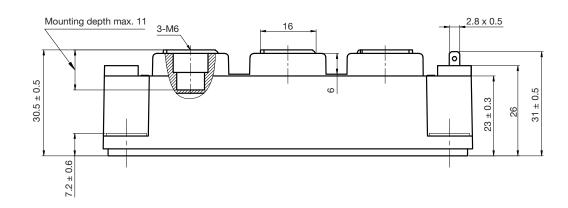


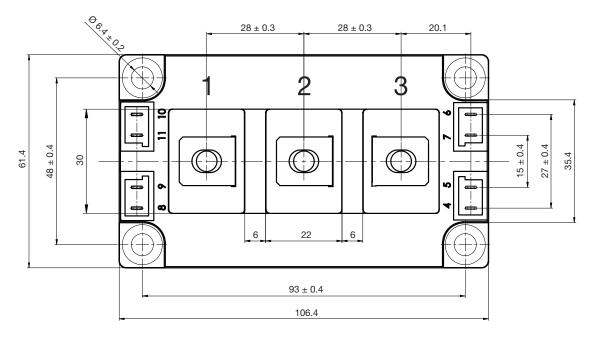
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95525			



## **Double INT-A-PAK**

#### **DIMENSIONS** in millimeters (inches)







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