

# swissbit®

## Product Fact Sheet

### Industrial M.2 PCIe SSD

### N2000 Series PCIe 3.1, 3D TLC

Industrial Temperature Grade

Date: July 03, 2025  
Revision: 1.05



## Product Summary

- **Capacities:** 60 GBytes, 120 GBytes, 240 GBytes, 480 GBytes
- **Form Factor:** PCI Express® M.2 (2230/2242/2280, S4) (30/42/80 mm x 22 mm x 2.63 mm)
- **Compliance<sup>1</sup>:** PCI Express (PCIe) Specification Revision 3.1
- **Interface:** Gen3 x 4 Lanes
  - Drive operates in x1 mode in x1 M.2 PCIe slots
  - Drive operates in x2 mode in x2 M.2 PCIe slots
  - Drive operates in x4 mode in x4 M.2 PCIe slots
- **Command Sets:** Supports NVMe 1.3
- **Performance:**
  - Read Performance: Sequential Read up to 1,753 MBytes/s, Random Read 4K up to 140,000 IOPS
  - Write Performance: Sequential Write up to 864 MBytes/s, Random Write 4K up to 134,000 IOPS
- **Host Memory Buffer (HMB):** Support for increased random performance
- **Operating Temperature Range<sup>2</sup>:**
  - Industrial: -40 °C to 85 °C ( $T_{ambient}$ )
  - Industrial: -40 °C up to 95 °C ( $T_{case}$ )<sup>3</sup>
- **Storage Temperature Range:** -40 °C to 85 °C
- **Operating Voltage:** 3.3 V supply voltage
- **Low Power Consumption**
- **Power:**
  - Power States PSo, PS1, PS2, PS3 and PS4
  - Thermal Throttling supported
- **Data Retention<sup>4</sup>:** 10 Years @ Life Begin; 1 Year @ Life End, @40 °C
- **Endurance in TeraBytes Written (TBW) @ 480GB capacity:**
  - Client  $\geq 964$
- **Shock/Vibration:** 1,500 g / 50 g
- **High-Performance Processor with Integrated, Parallel Flash Interface Engines:**
  - Triple-Level Cell (TLC) 3D NAND Flash
  - LDPC Code ECC with up to 120 bit correction per 1 KByte page
- **High Reliability:**
  - Mean Time Between Failure (MTBF): > 2,000,000 hours
  - Data Reliability: < 1 non-recoverable error per  $10^{16}$  bits read

<sup>1</sup> To check the compatibility of the customer system and the storage device is part of the customer's responsibility. Swissbit can provide guidance and support on request.

<sup>2</sup> Adequate airflow is required to ensure the drive temperature, as reported in the S.M.A.R.T. data, does not exceed CCTEMP (Critical Composite Temperature Threshold) reported in the "Identify Controller Data Structure"

<sup>3</sup> Tcase is the case surface temperature at the center of the top side of the device.

<sup>4</sup> NAND Flash suppliers refer to JEDEC JESD47 and JESD22 for Data Retention testing. Based on the information provided by the NAND Flash suppliers, Data Retention is targeted as shown in the table for reference.

## Product Features

- Dynamic and Static Wear Leveling
- Subpage Mode Flash Translation Layer (FTL)
- Data Care Management
  - Active: Adaptive Read Refresh
  - Passive: Background Media Scan
- Lifetime Enhancements
  - Dynamic Bad Block Remapping
  - Write Amplification Reduction
- Power Fail Data Loss Protection
- Data set management support (TRIM)
- Active State Power Management (ASPM) Support
- In-Field Firmware Update<sup>5</sup>
- Enterprise-Grade Self-Monitoring, Analysis, and Reporting Technology (S.M.A.R.T.)
- 30 pinch Gold-Plated Connector (IPC-6012B Class 2 Compliant)
- End-to-End (E2E) Data Protection
- Life Cycle Management
- Controlled "Locked" BOM
- RoHS / REACH Compliant
- Swissbit Device Manager (SBDM) Tool and SDK for SBDM (on request)

## 1 Security features

- AES256 encryption
- TCG OPAL 2.0
- Crypto erase



## Why Swissbit?

Swissbit is focused on the design, development, manufacture, and support of leading edge memory and storage solutions for the worldwide OEM/ODM marketplace. As a global supplier, Swissbit recognizes and addresses the higher level of application requirements of today's industrial, Netcom, and automotive customers by providing best-in-class products and services, with uncompromised attention to driving overall value and quality.

<sup>5</sup> The support of In-Field FW update capabilities on host systems is recommended.