SPEC for Mass Production

Spec No.	TQ3C-8EAF0-E1YCA08-00
Date	July 14, 2023

TYPE: TN0227ANVNANN-GN00

< 2.27 inch Reflective (slightly transmissive) Dot Matrix Memory LC Display>

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KYOCERA CORPORATION

This specification is subject to change without notice. Consult Kyocera before ordering.

Original	Designed by: Eng	Confirmed by: QA dept.		
Issue Date	Prepared	Prepared Checked		Approved
July 14, 2023	M. Yamada	T. Fukui	A. Iwasaki	T. Sawada



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Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss.

 Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

- 1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.
- 2. Please note that we may not be able to respond to new environmental regulations after receiving the final mass production order for this product.



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Revision record

	Date	Design	ed by : Eng		Confirmed by : QA dept.	
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1. Application

This document defines the specification of TN0227ANVNANN-GN00. (RoHS Compliant)

2. Construction and Outline

LCD : Reflective (slightly transmissive) dot matrix memory LC display

The display should be readable with polarized sunglasses in portrait

mode.

Backlight System

: None : Glare type

Polarizer : Glare type

Additional circuit : 1-bit pixel memory function in LCD

3. Mechanical Specifications

Item		Specification	Unit
Outline dimensions	1)	41.65(W) × 48.5(H)	mm
Glass thickness		0.5 + 0.5	mm
Active area		36.00(W) × 45.00(H) (Diagonal 2.27 inch)	mm
Dot format		240(W) × 300(H)	Dot
Dot pitch		150(W) × 150(H)	μm
Color	2)	Black-and-White (Binary) (Normally black)	-
LC Mode		ECB mode	-
Weight		6.2	g

- 1) FPC is not included. Please refer to the drawing for details.
- 2) The tone of display depends on ambient temperature.



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4. Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage	$V_{ m DD}$	-0.3	4.0	V
Input signal voltage 1)	$V_{\rm IN}$	-0.3	4.0	V

1) Input signals: SCLK, SI, SCS, RST, VCOM

4-2. Environmental absolute maximum ratings

Item	Symbol	Min.	Max.	Unit	
Operating temperature	1)	T_{OP}	-25	80	$^{\circ}$ C
Storage temperature	2)	T_{STO}	-30	85	$^{\circ}$
Operating humidity	3)	H_{OP}	10	4)	%RH
Storage humidity	3)	H_{STO}	10	4)	%RH

- 1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- 2) Store LCD at normal temperature/humidity. Keep them free from vibration and shock. An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard. (Please refer to "Precautions for Use" for details.)
- 3) Non-condensing
- 4) Temp. ${\leq}40^{\circ}\!C$, 85%RH Max. Temp. ${>}40^{\circ}\!C$, Absolute humidity shall be less than 85%RH at 40°C



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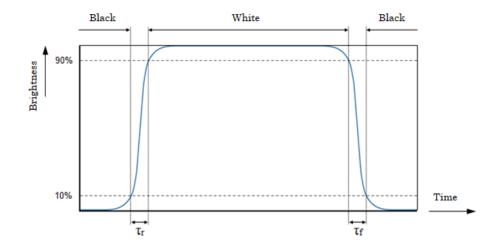
5. Optical Characteristics

Temp. = 25° C

Iten	n	Symbol	Temp.	Unit	Min.	Тур.	Max.
Contrast ratio 1) 2)		CR	$25^{\circ}\!\mathbb{C}$	-	-	40	-
Reflectance	2)	-	$25^{\circ}\!\mathbb{C}$	%	-	18	-
			-20°C		-	10	-
Response	Rise	τr	25℃	msec	-	4	-
time			40°C		-	4	-
3) 4)		Fall tf	-20℃		-	30	-
3, 1,	Fall		$25^{\circ}\!\mathbb{C}$	msec	-	5	-
			40°C		-	5	-
77:		θupper			-	60	-
Viewing CR≧		θlower θleft	$25^{\circ}\!\mathrm{C}$	dom	-	60	-
5) 6)		20 C	deg.	-	60	-
5) 6)	θ right			-	60	-
Chromaticity Coordinates	Wx	-	25℃	-	-	0.30	-
2) 7)	Wy	-	$25^{\circ}\!$		1	0.33	-

1) Definition of contrast ratio

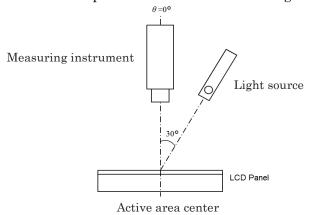
- 2) Reflectance and Chromaticity coordinates The measuring instrument is CM-2600d.
- 3) Definition of response time



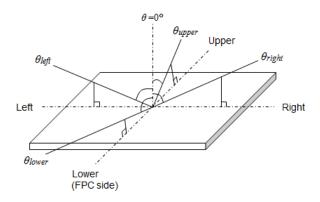


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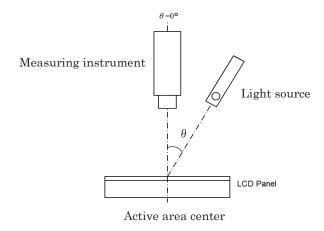
4) Measurement of response time
The measurement is performed under the following conditions on LCD-5200.



5) Definition of viewing angle



6) Measurement of viewing angle The measurement is performed by changing the angle of the light source on LCD-5200.



7) Condition: Reflection

Light source: Illuminant C (Converted value)



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6. Electrical Characteristics

6-1. LCD driving characteristics

Temp. = 25° C

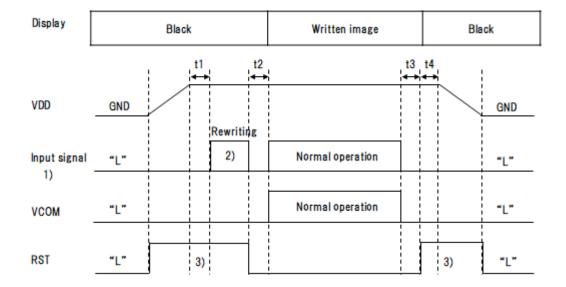
Item		Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage		$V_{ m DD}$	_	3.0	3.3	3.6	V
To seek of more level to me	1) 0)	V_{IL}	"Low" level	GND-0.2	_	VDD*0.25	V
Input signal voltage	1) 2)	V_{IH}	"High" level	VDD*0.75	_	VDD+0.2	V
Input leak current	1)	$I_{\rm IN}$	T _{OP} =25°C	_	5	20	nA
C .	0)	$I_{\mathrm{DD_opr}}$	4)	_	28.1	57.5	μΑ
Current consumption	3)	${ m I}_{ m DD_stb}$	5)	_	1.9	7.0	μΑ
Input capacitance	1)	$\mathrm{C_{IN1}}$	_	_	5	10	pF

- 1) SCLK, SI, SCS, RST, VCOM
- 2) Since input leakage current is less than 20 nA, please control voltage so that V_{IL} =GND and V_{IH} =VDD in static state.
- 3) Value of the current consumption is without smoothing capacitor.
- 4) 1Hz full display rewriting (Without backlight)
- 5) Keeping static image, and polarity inversion of VCOM with a period of 1 sec. (Without backlight) SCLK, SCS, SI, RST: "L" state.



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6-2. Power ON-OFF sequence



Item	Symbol	Min.	Typ.	Max.	Unit
D.	t1	0			μsec
Power	t2	1	10	_	msec
ON-OFF	t3	1	10	_	msec
sequence	t4	1	10	_	msec

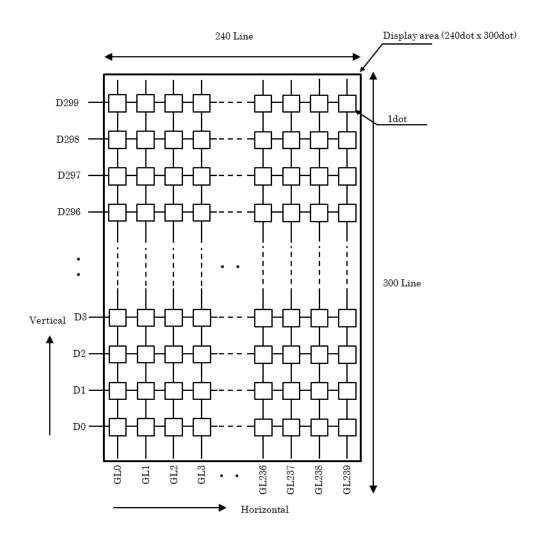
- 1) SCLK, SI, SCS
- 2) Rewriting to black for all addresses.
- 3) Display OFF when RST = "H". VCOM = "L" is necessary when RST = "H". Black display when display is OFF.

Data in pixel memory is random with black or white at power ON. Possible to prevent black-white random data display by black display when RST = "H".



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7. Address Mapping



Addressing of Gate line (GL*) to be selected is necessary for horizontal direction. Rewriting data for one line is necessary for vertical direction due to rewriting one line at once.

-Addressing table for vertical direction

AG7	AG6	AG5	AG4	AG3	AG2	AG1	AG0	Selected GL
0	0	0	0	0	0	0	0	GL0
0	0	0	0	0	0	0	1	GL1
0	0	0	0	0	0	1	0	$\operatorname{GL2}$
0	0	0	0	0	0	1	1	GL3
•	•	•	•	•	•	•	•	•
•	•	•	•	•	•	•	•	•
1	1	1	0	1	1	0	0	GL236
1	1	1	0	1	1	0	1	GL237
1	1	1	0	1	1	1	0	GL238
1	1	1	0	1	1	1	1	GL239



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8. Interface Signals

No.	Symbol	Description	I/O 1)	Voltage	Unit	Note
1	VDD	Power supply(3.3V)	P	3.3	V	
2	VSS	GND	P	0.0	V	
3	SCLK	Clock signal for serial input	I	0.0/3.3	V	3)
4	SCS	Chip select signal	I	0.0/3.3	V	3)
5	SI	Serial input signal	I	0.0/3.3	V	
6	RST	Display ON/OFF signal	Ι	0.0/3.3	V	2)3)
7	VCOM	Common power supply control	I	0.0/3.3	V	
8	VSS	GND	P	0.0	V	
9	VDD	Power supply(3.3V)	P	3.3	V	
10	NC	NC	_	_	_	4)

Matching connector: 04 6824 610 000 846+ (KYOCERA)

1) P: Power supply

I: Input

2) RST = L Display ON

RST = "H": Display OFF (Black display keeping pixel memory)

VCOM = "L" is necessary when RST = "H"

(When VCOM="H", Display does not turn to black, and current consumption increases by shoot-through-current in panel. (several mA)

3) Need to guard from signal noise

If there is an abnormal signal not described in the timing chart on these signals, display may be distorted. Please carefully guard these signals since even if signal noise with small pulse width may cause malfunction.

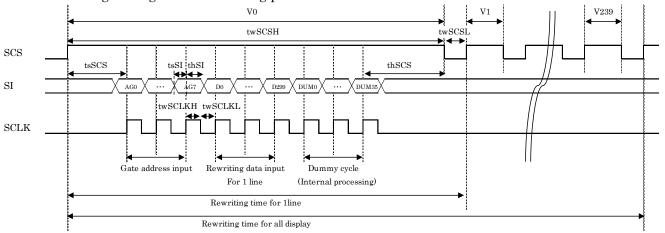
4) This pin is floating pin on LCD module.



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9. Input Timing Characteristics

9-1. Rewriting timing <SCS activating period: 1 line>



3 lines (SCS, SCLK, and SI) are control signals to rewrite.

Rewriting 1 gate line by 1 gate line.

Input order of serial data

- 1. Input gate address
- 2. Input rewriting data for 1 horizontal line
- 3. Dummy cycle (internal processing for rewriting)

Relationship between rewriting data and display.

Data 0: Display BLACK Data 1: Display WHITE

Time to rewrite in case of SCLK frequency = 1.5MHz (twSCLKH=twSCLKL=0.33us)

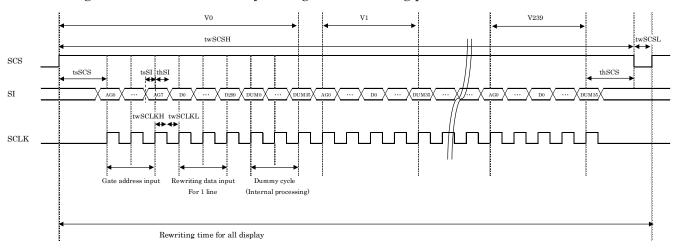
No.	Item	Parameters
Serial data input	Gate address	AG0~AG7
(1 line)	Rewriting data	D0~D299
	Dummy cycle	DUM0~DUM35
Total cycle for 1line		344 cycs
Time to rewrite 1 line		5.236 msec
Number of gate line		240 lines
Time to rewrite full display		1,256.7 msec



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9-2. Rewriting timing <SCS activating period: 1 frame>

Rewriting V₀ to V max continuously during SCS activating period



3 lines (SCS, SCLK, and SI) are control signals to rewrite.

Rewriting 1 gate line by 1 gate line.

Input order of serial data

- 1. Input gate address
- 2. Input rewriting data for 1 horizontal line
- 3. Dummy cycle (internal processing for rewriting)

Possible to rewrite numerous lines continuously during SCS activating period. Gate addresses for continuous rewriting of numerous lines can be set in any order.

Relationship between rewriting data and display.

Data 0: Display BLACK Data 1: Display WHITE

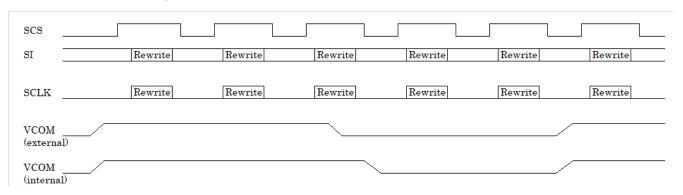
Time to rewrite in case of SCLK frequency = 1.5MHz (twSCLKH=twSCLKL=0.33us)

No.	Item	Parameters
Serial data input Gate address		AG0~AG7
(1 line)	Rewriting data	D0 ~ D299
	Dummy cycle	DUM0~DUM35
Total cycle for 1line		344 cycs
Number of gate line		240 lines
Time to rewrite full	display	59.3 msec



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9-3. VCOM AC driving



AC drive between 0 V and 3.3 V is required for VCOM signal.

External VCOM switching is reflected immediately on VCOM inside the panel when switching is performed at SCS = "L", however when switching is performed in SCS = "H", external VCOM switching is reflected at the next timing when SCS = "L".

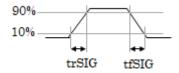


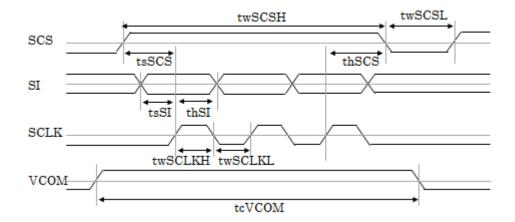
9-4. Timing characteristics

Item		Symbol	Min.	Тур.	Max.	Unit	Note
Time to rewrite 1 line			5.236	_	_	msec	
Time to rewri	te full display		59.34	_	_	msec	
Input signal	Signal rise time	trSIG		_	50	nsec	2)
1)	Signal fall time	tfSIG		_	50	nsec	2)
G GT 17	SCLK high width	twSCLKH	0.33	_	_	μsec	3)
SCLK	SCLK low width	twSCLKL	0.33	_	_	μsec	3)
CIT.	SI set-up time	tsSI	0.33	_	_	μsec	3)
SI	SI hold time	thSI	0.33	_	_	μsec	3)
	SCS high width	twSCSH	5.226	_	_	msec	3)
SCS	SCS low width	twSCSL	10	_	_	μsec	3)
202	SCS set-up time	tsSCS	4	_	_	msec	3)
	SCS hold time	thSCS	1	_	_	msec	3)
110015	VCOM duty	_	_	50	_	%	
VCOM	VCOM cycle time	tcVCOM	_	1000	_	msec	3)

- 1) SCLK, SI, SCS, RST, VCOM
- 2) Time for the signal to shift from 10% and 90% or 90% to 10%
- 3) Defined as 50% value of the signal.

-Timing charts







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10. Warranty

10-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

10-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



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11. Precautions for Use

11-1. Installation of the LCD

- 1) A transparent protection plate shall be added to protect the LCD and its polarizer.
- 2) The LCD shall be installed flat, without twisting or bending.
- 3) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.
- 4) FPC shall be installed after fixing the glass.
- 5) Do not apply a load to the FPC.
- 6) Do not use an assembly structure where the FPC is pulled or partially stressed.

11-2. Static electricity

- 1) Protect the LCD from static electricity.
- 2) Workers should use body grounding. Operator should wear ground straps.

11-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

11-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

11-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD because it will result in damage.
- 7) This Kyocera LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.
- 10) Do not hold the FPC alone while working.
- 11) Do not scratch or damage the exposed conductor of the FPC or FPC.
- 12) Do not touch the exposed conductor of the FPC.
- 13) Do not bend the FPC toward the surface of LCD near the glass.
- 14) Do not bend the FPC so that stress is concentrated on the FPC.



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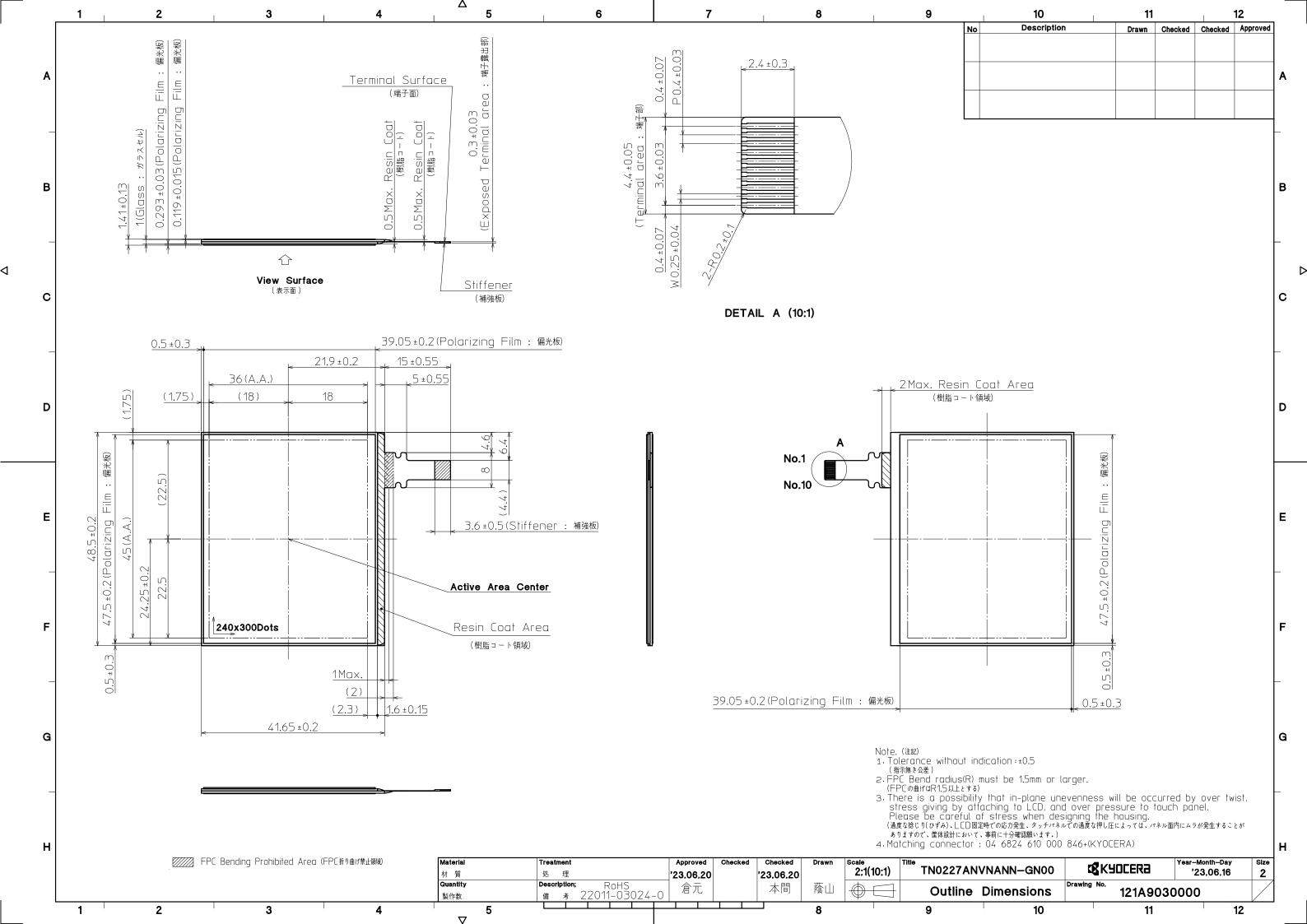
12. Reliability Test Data

Test item	Test condition	Test time	Judgement
High temp. atmosphere	85℃	240h	Function/Display : No defect Current consumption : No defect
Low temp. atmosphere	-30℃	240h	Function/Display : No defect Current consumption : No defect
High temp. humidity atmosphere	60°C 90%RH	240h	Function/Display : No defect Current consumption : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 85°C 0.5h	10cycles	Function/Display : No defect Current consumption : No defect
High temp. operation	80°C	240h	Function/Display : No defect Current consumption : No defect

- 1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- 2) The LCD is tested in circumstances in which there is no condensation.
- 3) The reliability test is not an out-going inspection.
- 4) The result of the reliability test is for your reference purpose only.

 The reliability test is conducted only to examine the LCD's capability.





Spec No.	TQ3C-8EAF0-E2YCA08-00
Date	July 14, 2023

KYOCERA INSPECTION STANDARD

TYPE: TN0227ANVNANN-GN00

KYOCERA CORPORATION

Original					
Issue Date	Prepared	Checked	Approved	Approved	
July 14, 2023	M. Yamada	T. Fukui	A. Iwasaki	T. Sawada	



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Revision record

Date		Designed by : Engineering dept.				Confirmed by : QA dept.
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Visuals specification

1) Note

1) Note			Note				
General			ot defined within this inspection standard shall be reviewed by				
	Kyocera, and an additional standard shall be determined by mutual consent.						
	2. Dot def	ect and external inspection	n about the image quality shall be applied to any defect within				
	the active area and shall not be applicable to outside of the active area.						
	3. Inspection conditions						
	Lumin	ance	: 500 Lux min.				
	Inspect Tempe	tion distance	: 300 mm. : 25 ± 5°C				
	Directi		Directly above				
Definition of inspection	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen.				
item			In an action to all 70/ The area and action that I do not to 614 and				
			Inspection tool: 5% Transparency neutral density filter. Count dot: If the dot is visible through the filter.				
			Don't count dot: If the dot is not visible through the filter.				
			D(1,1) $D(2,1)$ $D(3,1)$ $D(4,1)$ $D(5,1)$				
			D(1,2) D(2,2) D(3,2) D(4,2) D(5,2) dot defect				
			$\begin{array}{ c c c c c c c c c c c c c c c c c c c$				
		Black dot defect	The dot is constantly "off" when power applied to the LCD,				
		(Included circular	even when all "White" data sent to the screen.				
		shape)	Similar size compared to bright dot. For circular shape black dot, refer to inspection item of				
			White dot, Black dot (Circular shape).				
		White dot (Circular shape)	Pixel works electrically, however, circular/foreign particle makes dot appear to be "on" even when all "Black" data is sent to the screen.				
	External	Bubble, Scratch,	Visible operating (all pixels "Black" or "White") and				
	inspection	Foreign particle	non-operating.				
		Appearance inspection	Does not satisfy the value at the spec.				
	Definition of size	Definition of circle size Definition of linear size					
		X					
		0)	* <u>• </u>				
		V					
		a: major axis, b: r	ninor axis				
		d = (a + b)					



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2) Standard

2) Standa	rd						
Classification Inspection item			Judgement standard				
Defect Dot (In LCD defect		Bright dot defect	Acceptable number : 0				
glass)		Black dot defect	Acceptable number : 0				
		Total dot defects	Acceptable number	: 0			
	Others	White dot, Black dot (Circular shape)					
			Size (mm)		Acc	eptable number	
			$d \leq 0$.			Neglected	
			$0.1 < d \le 0.$.15		2	
			0.15 < d			0	
External		Foreign particle	TT: 1.1 ()	T 11 (
inspection		Scratch	Width (mm)	Length (r	nm)	Acceptable number	
(Defect or Polarizer		(Linear shape)	$W \leq 0.02$		- 0.0	Neglected	
between I			$W \leq 0.03$		≤ 2.0	2	
and LCD			$W \leq 0.05$	<u></u>	≤ 0.5	1	
			0.05 < W			0	
		Foreign particle					
		Bubble	Size (mm)		Acc	Acceptable number	
		Scratch	d ≤ 0			Neglected	
		(Circular shape)	$0.1 < d \le 0$	0.2		2	
			0.2 < d 0				
	Color variation (Mura)		Determination of color variation: Invisible with ND 5% filter Distance between LCD and ND filter: 150mm Inspection time: 1 second				
<u> </u>							



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Inspection item	Judgement standard						
Glass crack	(t = Glass Thickness)						
	Item Size (mm)				Judgement		
) b	a	b	c		
	Back side of the terminal	w t a	≦t	≦w	≦2.0	Neglected	
			a	b	c		
	Terminal	w t	≦t	≦w	≦ 2.0	Neglected	
		Sealant areat	a	b	c		
	Display side glass	a c	≤t	< Sealant area inner boundary	≦3.0	Neglected	
	Double glass	Sealant area	a	b	c		
			-	-	≦0.2	Neglected	
			≦t	< Sealant area inner boundary	≦3.0		
			>t	< Sealant area outer boundary	≦ 2.0		
		Sealant area	a	b	c		
	Corner	w b c a t	\leqq t	≦w	≦ 2.0	Neglected	
		a:Thickness	a	b	c		
	Projection	c b	≦t	≦0.3	-	Neglected	



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KYOCERA PACKAGING STANDARD

TYPE: TN0227ANVNANN-GN00

KYOCERA CORPORATION

Original	Designed by : Eng	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Approved
July 14, 2023	M. Yamada	T. Fukui	A. Iwasaki	T. Sawada



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Revision record

Date		Designed by : Engineering dept.				Confirmed by : QA dept.
		Prepared		Checked	Approved	Approved
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TN0227ANVNANN_Packaging Standard 1. How to store LCDs in the tray Spacer Spacer Empty tray Module strage tray Product Tray Spacer Number of storage Max.16 pcs/ tray Number of storage Max. 96 pcs/ tray

TN0227ANVNANN_Packaging Standard 2. How to store LCDs in the outer case Cushioning material PP band Outer case Confirmation label 客先 品名 ラ株式 数量 検査者 (Check) 検査者 Number of storage Max. 384 pcs/ outer case 3. Location of the labels Single-sided tape PP band KKADTERA AUCERA Confirmation label Shipping label 客先 京 品名 (Parts No. t ラ 数量 株 式 検査者 슾 検査者 社