

APPROVAL SHEET

MF12Q/R, MF08Q/R, MF06Q/R, MF04Q/R

$\pm 1\%$, $\pm 0.5\%$, $\pm 0.25\%$, $\pm 0.1\%$, $\pm 0.05\%$

TC50 , TC25

Anti-Sulfuration

Precision Thin Film Chip Resistor

Size 1206, 0805, 0603, 0402

AEC-Q200 Qualified

RoHS Exemption free and Lead free products

Halogen free



*Contents in this sheet are subject to change without prior notice.

FEATURES

1. SMD TaN thin film resistor
2. Special passivation layer on resistive film
3. AEC-Q200 qualified
4. Products with lead free terminations meet RoHS requirements
5. Sulfur resistant (per ASTM B809-95 humid vapor test)
6. AEC-Q200 ESD rated class 1C
7. $\pm 0.02\%$ is upon the customer request.
8. Laser trimmed to any value
9. 100% visual inspection

APPLICATIONS

- Automotive electronics
- Medical equipment
- Measuring instrument
- Industrial Equipment

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive layer that is applied to the top surface of the substrate. The composition of the resistive layer is adjusted to give the approximate resistance required and the value is trimmed to nominated value within tolerance which controlled by laser trimming of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For environmental soldering issue, the outer layer of these end terminations is a Lead-free solder.

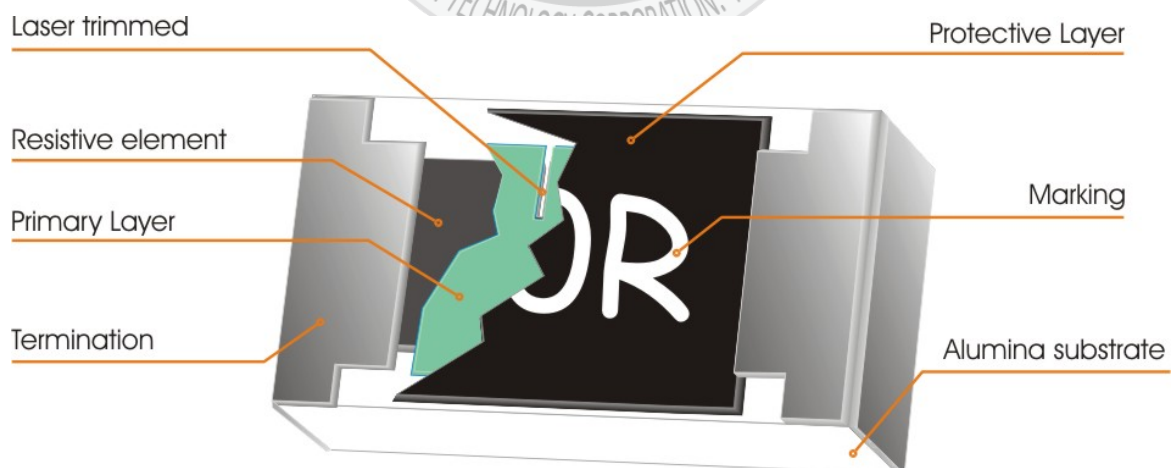


Fig 1. Construction of Chip-R MFxxQ

QUICK REFERENCE DATA

Item	General Specification			
Series No.	MF12Q&R	MF08Q&R	MF06Q&R	MF04Q&R
Size code	1206 (3216)	0805 (2012)	0603 (1608)	0402 (1005)
Resistance Tolerance	$\pm 1\%$, $\pm 0.5\%$, $\pm 0.25\%$, $\pm 0.1\%$, $\pm 0.05\%$			
Resistance Range	10 Ω ~ 1M Ω	10 Ω ~ 350K Ω	40 Ω ~ 180K Ω	40 Ω ~ 50K Ω
TCR (ppm/°C)	± 50 / ± 25 ppm/°C			
Max. dissipation at T _{amb} =85°C	0.4W	0.2W	0.15W	0.0625W
Max. Operation Voltage (DC or RMS)	200V	100V	75V	50V
Max. Overload Voltage (DC or RMS)	400V	200V	150V	100V
Operating Temperature Range	- 55~ +175°C			

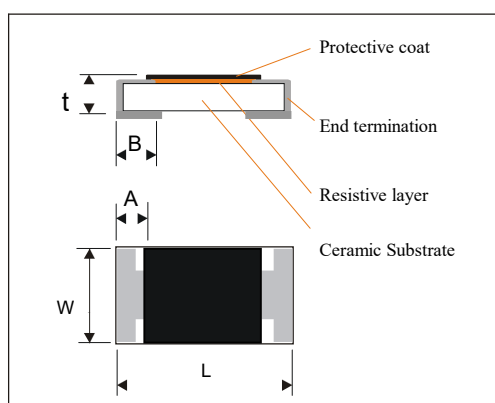
Note :

1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

$$RCWV = \sqrt{\text{Rated Power} \times \text{Resistance Value}} \text{ or Max. RCWV listed above, whichever is lower.}$$

DIMENSIONS:(unit:mm)

Type	MF12	MF08	MF06	MF04
L	3.00 ± 0.10	2.00 ± 0.10	1.55 ± 0.10	1.00 ± 0.10
W	1.50 ± 0.10	1.25 ± 0.10	0.80 ± 0.10	0.50 ± 0.05
A	0.40 ± 0.20	0.35 ± 0.20	0.30 ± 0.20	0.25 ± 0.15
B	0.40 ± 0.20	0.40 ± 0.20	0.30 ± 0.15	0.30 ± 0.10
t	0.60 ± 0.15	0.50 ± 0.15	0.45 ± 0.15	0.30 ± 0.10



CATALOGUE NUMBERS

The resistors have a catalogue number starting with .

MF06	Q	1003	B	T	L
Size code MF12 : 1206 MF08 : 0805 MF06 : 0603 MF04 : 0402	Type code Q : TCR ± 50 ppm R : TCR ± 25 ppm	Resistance code 3 significant digits followed by no. of zeros 102 Ω =1020 37.4K Ω =3742 220 Ω =2200	Tolerance F : $\pm 1\%$ D : $\pm 0.5\%$ C : $\pm 0.25\%$ B : $\pm 0.1\%$ A : $\pm 0.05\%$	Packaging code T : 7"Taped & Reeled	Termination code L : Sn base (lead free)

Reeled tape packaging: 8mm width paper taping.

5,000pcs/reel for MF06, MF08, MF12

10,000pcs/reel for MF04

MARKING

- No marking for 0402 size
- 3 digits marking for 0603 size

Nominal resistance	Description																																						
1.E24 series	2 significant digits followed by No. of zeros . Example <table><tr><td>RESISTANCE</td><td>4.7Ω</td><td>47Ω</td><td>470Ω</td><td>4K70</td><td>47K0</td><td>470K</td><td>4M70</td></tr><tr><td>3 digits marking</td><td>4R7</td><td>470</td><td>471</td><td>472</td><td>473</td><td>474</td><td>475</td></tr></table>	RESISTANCE	4.7Ω	47Ω	470Ω	4K70	47K0	470K	4M70	3 digits marking	4R7	470	471	472	473	474	475																						
RESISTANCE	4.7Ω	47Ω	470Ω	4K70	47K0	470K	4M70																																
3 digits marking	4R7	470	471	472	473	474	475																																
2.E96 series	The 1st two digit codes are referring to the CODE on the table, the 3rd code is the index of resistance value. Repeat values between E24 and E96 series, whose marking are based on the E96 CODE table. <table><tr><td>Code</td><td>Z</td><td>Y</td><td>X</td><td>A</td><td>B</td><td>C</td><td>D</td><td>E</td><td>F</td><td>G</td></tr><tr><td>Multiplier</td><td>10⁻³</td><td>10⁻²</td><td>10⁻¹</td><td>10⁰</td><td>10¹</td><td>10²</td><td>10³</td><td>10⁴</td><td>10⁵</td><td>10⁶</td></tr></table> Example <table><tr><td>RESISTANCE</td><td>1.78Ω</td><td>17.8Ω</td><td>178Ω</td><td>1K78</td><td>17K8</td><td>178K</td><td>1M78</td></tr><tr><td>3 digits marking</td><td>25Y</td><td>25X</td><td>25A</td><td>25B</td><td>25C</td><td>25D</td><td>25E</td></tr></table>	Code	Z	Y	X	A	B	C	D	E	F	G	Multiplier	10 ⁻³	10 ⁻²	10 ⁻¹	10 ⁰	10 ¹	10 ²	10 ³	10 ⁴	10 ⁵	10 ⁶	RESISTANCE	1.78Ω	17.8Ω	178Ω	1K78	17K8	178K	1M78	3 digits marking	25Y	25X	25A	25B	25C	25D	25E
Code	Z	Y	X	A	B	C	D	E	F	G																													
Multiplier	10 ⁻³	10 ⁻²	10 ⁻¹	10 ⁰	10 ¹	10 ²	10 ³	10 ⁴	10 ⁵	10 ⁶																													
RESISTANCE	1.78Ω	17.8Ω	178Ω	1K78	17K8	178K	1M78																																
3 digits marking	25Y	25X	25A	25B	25C	25D	25E																																
3. Remark	There is no marking for the items not under E24 and E96 series.																																						
E96 CODE table:																																							
CODE	R value	CODE	R-value	CODE	R-Value	CODE	R-value	CODE	R-value	CODE	R-value	CODE	R-value	CODE	R-value																								
01	100	13	133	25	178	37	237	49	316	61	422	73	562	85	750																								
02	102	14	137	26	182	38	243	50	324	62	432	74	576	86	768																								
03	105	15	140	27	187	39	249	51	332	63	442	75	590	87	787																								
04	107	16	143	28	191	40	255	52	340	64	453	76	604	88	806																								
05	110	17	147	29	196	41	261	53	348	65	464	77	619	89	825																								
06	113	18	150	30	200	42	267	54	357	66	475	78	634	90	845																								
07	115	19	154	31	205	43	274	55	365	67	487	79	649	91	866																								
08	118	20	158	32	210	44	280	56	374	68	499	80	665	92	887																								
09	121	21	162	33	215	45	287	57	383	69	511	81	681	93	909																								
10	124	22	165	34	221	46	294	58	392	70	523	82	698	94	931																								
11	127	23	169	35	226	47	301	59	402	71	536	83	715	95	953																								
12	130	24	174	36	232	48	309	60	412	72	549	84	732	96	976																								

- 4 digits marking for 0805/1206 size:

Each resistor is marked with a four digits code on the protective coating to designate the nominal resistance value. The first 3 digits are significant, and the fourth digit indicates the number of multiple to follow. For non E24/E96 series, no marking is applied.

Example

RESISTANCE	10 Ω	47 Ω	470 Ω	6800 Ω	47000 Ω	470000 Ω	1000000 Ω
4 digits marking	10R0	47R0	4700	6801	4702	4703	1004

FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E192 & E24 series for resistors with a tolerance of $\pm 1\%$, $\pm 0.5\%$, $\pm 0.25\%$, $\pm 0.1\%$, $\pm 0.05\%$. The values of the E24/E192 series are in accordance with "IEC publication 60063".

DERATING

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

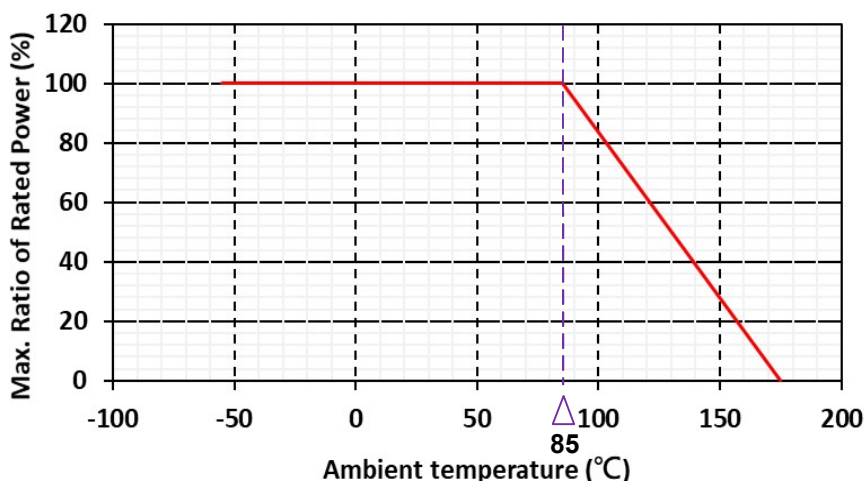


Fig. 2 Maximum dissipation in percentage of rated power as a function of ambient temperature

MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

Storage and Handling Conditions:

- Products are recommended to be used up within two years since operation date as ensured shelf life. Check solderability in case shelf life extension is needed.
- To store products with following condition:
 - Temperature :5 to 40°C
 - Humidity :20 to 70% relative humidity
- Caution:
 - Don't store products in a corrosive environment such as sulfide, chloride gas, or acid.
It may cause oxidation of electrode, which easily be resulted in poor soldering.
 - To store products on the shelf and avoid exposure to moisture.
 - Don't expose products to excessive shock, vibration, direct sunlight and so on.

SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds within lead-free solder bath. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering profile and condition that provide reliable joints without any damage are given in Fig 3. and Table 1.

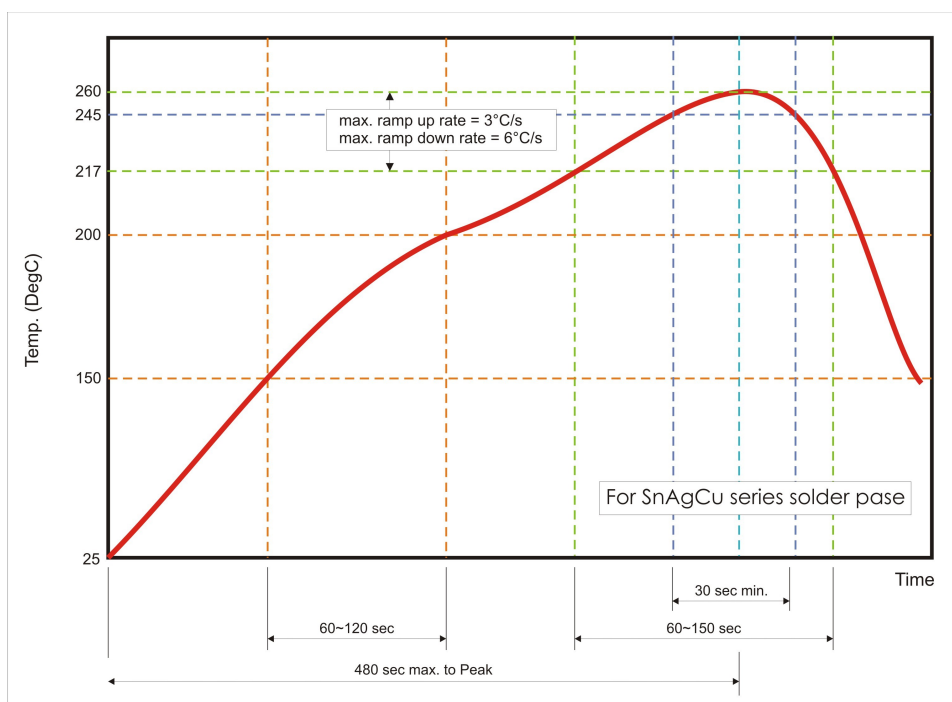


Fig. 3 Infrared soldering profile for Chip Resistors

Table 1. Infrared soldering condition for Chip Resistors

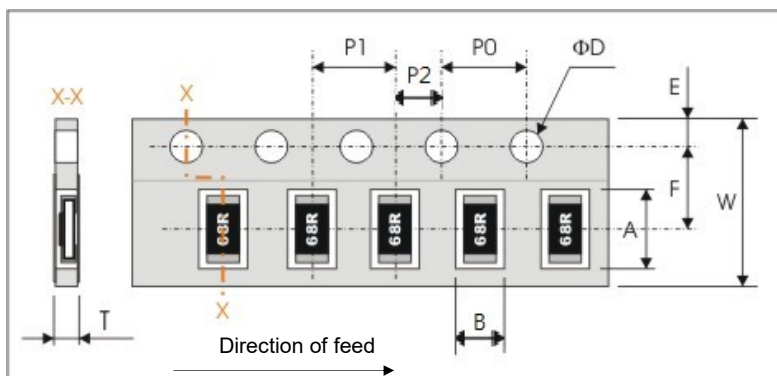
Temperature Condition	Exposure Time
Average ramp-up rate (217°C to 260°C)	Less than 3°C/second
Between 150 and 200°C	Between 60-120 seconds
> 217°C	Between 60-150 seconds
Peak Temperature	260°C +0/-5°C
Time within 245°C	Min. 30 seconds
Ramp-down rate (Peak to 217°C)	Less than 6°C/second
Time from 25°C to Peak	No greater than 480 seconds

TEST AND REQUIREMENTS

TEST	PROCEDURE	REQUIREMENT
Electrical Characteristics IEC 60115-1 4.8	- DC resistance values measurement - Temperature Coefficient of Resistance (T.C.R) Natural resistance change per change in degree centigrade. $\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/}^\circ\text{C)}$ $t_1 : 20^\circ\text{C}+5^\circ\text{C/-1}^\circ\text{C}$ $R_1 : \text{Resistance at reference temperature (20}^\circ\text{C}+5^\circ\text{C/-1}^\circ\text{C)}$ $R_2 : \text{Resistance at test temperature (-55}^\circ\text{C or +125}^\circ\text{C)}$	Within the specified tolerance
Short time overload (S.T.O.L) IEC60115-1 4.13	Permanent resistance change after a 5second application of a voltage 2.5 times RCWV or the maximum overload voltage specified in the above list, whichever is less.	$\Delta R/R \text{ max. } \pm(0.1\%+0.02\Omega)$
Resistance to soldering heat(R.S.H) AEC-Q200-15	Un-mounted chips completely immersed for 10 ± 1 second in a SAC solder bath at $260^\circ\text{C} \pm 5^\circ\text{C}$	no visible damage $\Delta R/R \text{ max. } \pm(0.1\%+0.02\Omega)$
Solderability IEC 60068-2-58	Un-mounted chips completely immersed for 2 ± 0.5 second in a SAC solder bath at $235^\circ\text{C} \pm 5^\circ\text{C}$	good tinning (>95% covered) no visible damage
Temperature cycling JESD22 method JA-104	1000 cycles, $-55^\circ\text{C} \sim +125^\circ\text{C}$, dwell time 5~10min	$\Delta R/R \text{ max. } \pm(0.1\%+0.02\Omega)$
	1000 cycles, $-55^\circ\text{C} \sim +155^\circ\text{C}$, dwell time 5~10min	$\Delta R/R \text{ max. } \pm(0.2\%+0.02\Omega)$
Bias Humidity AEC-Q200-7	1000 +48/-0 hours, loaded with 10% rated power in humidity chamber controller at $+85^\circ\text{C} / 85\%\text{RH}$	$\Delta R/R \text{ max. } \pm(0.1\%+0.02\Omega)$
High Temperature Operating Life MIL-STD-202 Method 108	1000 +48/-0 hours, loaded with RCWV or Vmax in chamber controller $85 \pm 2^\circ\text{C}$, 1.5 hours on and 0.5 hours off	$\Delta R/R \text{ max. } \pm(0.1\%+0.02\Omega)$
High Temperature Exposure AEC-Q200-3	1000 hours at 125°C , un-powered	$\Delta R/R \text{ max. } \pm(0.1\%+0.02\Omega)$
	1000 hours at 155°C , un-powered	$\Delta R/R \text{ max. } \pm(0.15\%+0.02\Omega)$
	1000 hours at 175°C , un-powered	$\Delta R/R \text{ max. } \pm(0.30\%+0.02\Omega)$
Moisture Resistance AEC-Q200 -6 MIL-STD-202 Method 106	$65 \pm 2^\circ\text{C}$, 80~100% RH, 10 cycles, 24 hours/ cycle	$\Delta R/R \text{ max. } \pm(0.1\%+0.02\Omega)$
Mechanical Shock MIL-STD-202 Method 213	1/2 Sine Pulse / 1500g Peak / Velocity 15.4ft/sec	$\Delta R/R \text{ max. } \pm(0.1\%+0.02\Omega)$
Vibration MIL-STD-202 Method 204	5 g's for 20 min , 12 cycles each of 3 orientations	$\Delta R/R \text{ max. } \pm(0.1\%+0.02\Omega)$
Terminal strength AEC-Q200-6	1 kg for 60 s	No broken
Board flex AEC-Q200-005	Bending 2mm for 60 sec	$\Delta R/R \text{ max. } \pm(0.1\%+0.02\Omega)$
Flower of sulfur ASTM-B809-95	$105 \pm 2^\circ\text{C}$, duration 1000hours.	0402 : $\Delta R/R \text{ max. } \pm(0.2\%+0.02\Omega)$ Others: $\Delta R/R \text{ max. } \pm(0.1\%+0.02\Omega)$
ESD AEC-Q200-002	0402 : 500V 0603 : 1000V 0805 : 1500V 1206 : 2000V	$\Delta R/R \text{ max. } \pm(0.5\%+0.02\Omega)$ No visible damage

PACKAGING

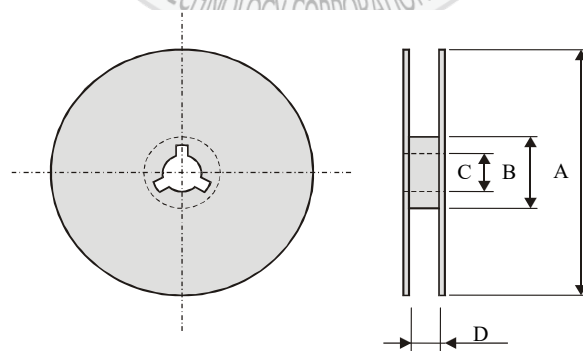
Tape specifications (unit :mm)



Series No.	A	B	W	F	E
MF12	3.60±0.20	2.00±0.20	8.00±0.30	3.50±0.20	1.75±0.10
MF08	2.40±0.20	1.65±0.20	8.00±0.30	3.50±0.20	1.75±0.10
MF06	1.90±0.20	1.10±0.20	8.00±0.30	3.50±0.20	1.75±0.10
MF04	1.20±0.10	0.7±0.10	8.00±0.30	3.50±0.05	1.75±0.10

Series No.	P1	P0	P2	ΦD	T
MF12	4.00±0.10	4.00±0.10	2.00±0.10	Φ1.50 ^{+0.1} _{-0.0}	Max. 1.0
MF08	4.00±0.10	4.00±0.10	2.00±0.10	Φ1.50 ^{+0.1} _{-0.0}	Max. 1.0
MF06	4.00±0.10	4.00±0.10	2.00±0.10	Φ1.50 ^{+0.1} _{-0.0}	0.65±0.05
MF04	2.00±0.10	4.00±0.10	2.00±0.10	Φ1.50 ^{+0.1} _{-0.0}	0.45±0.05

Reel dimensions



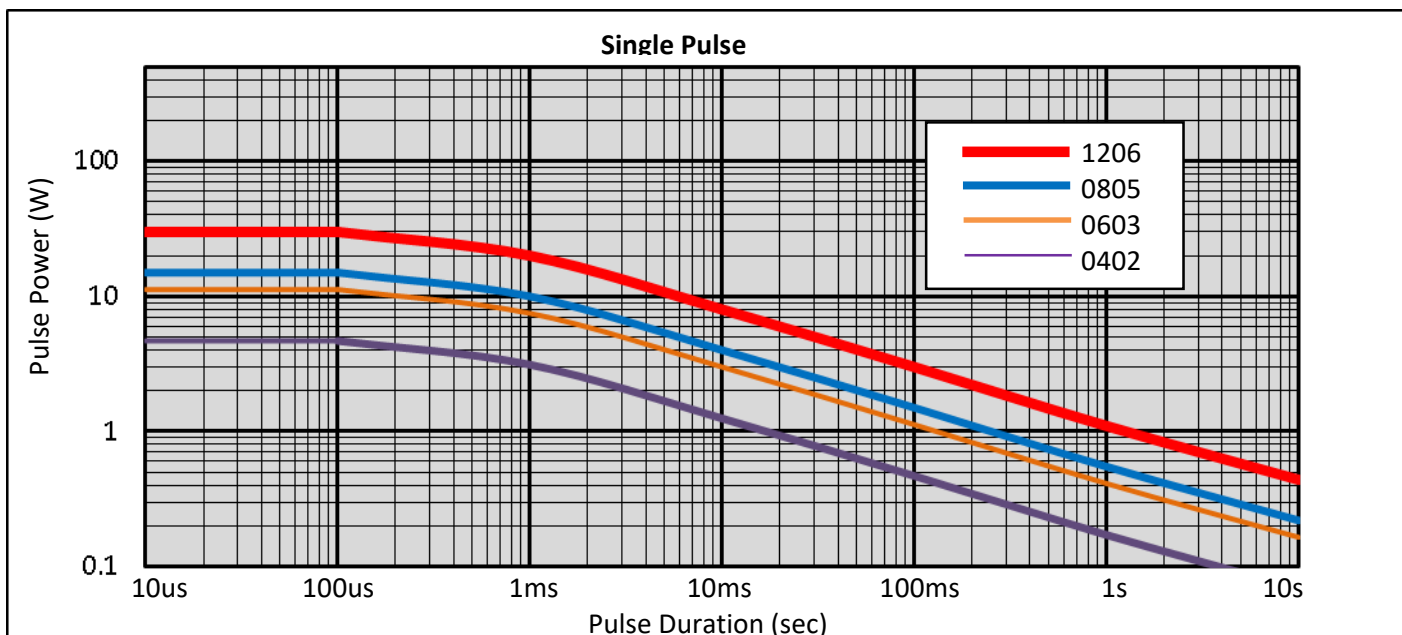
Symbol	A	B	C	D
(unit : mm)	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	9.0±0.5

Taping quantity

- Chip resistors 5,000 pcs per reel (MF06, MF08, MF12)
- Chip resistors 10,000 pcs per reel (MF04)

PULSE LOAD PERFORMANCE:

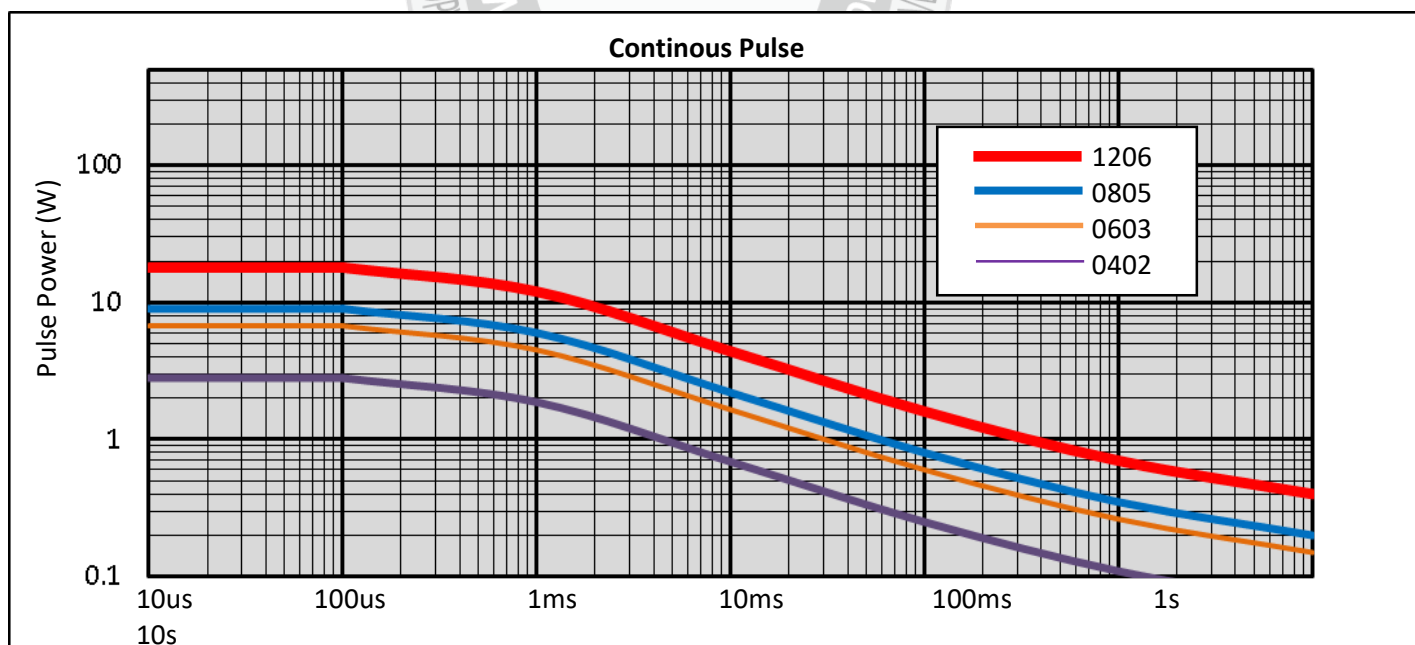
Single Pulse :



Single Pulse for $R \geq 10 \Omega$

50 rectangular pulse amplitudes are applied to the component at intervals of 60seconds, permissible the resistance to be varied by $\pm (0.5\% R + 0.01\Omega)$.

Continuous Pulse :

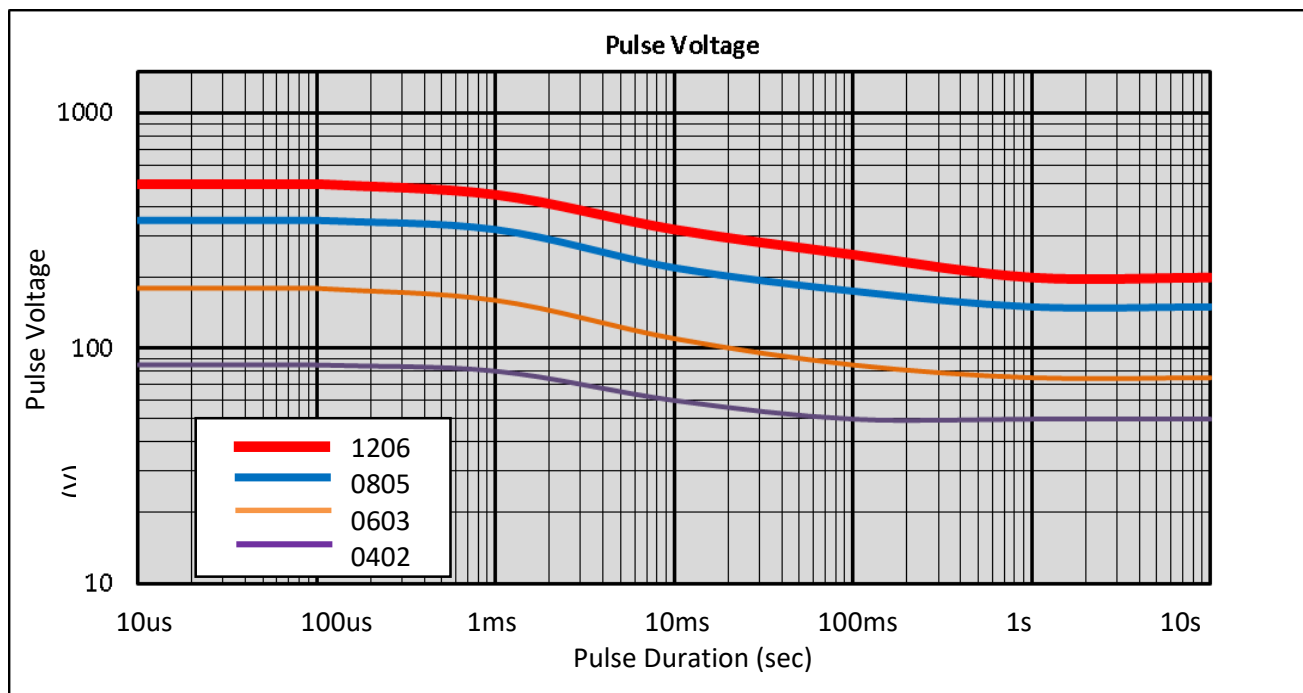


Continuous Pulse for $R \geq 10 \Omega$

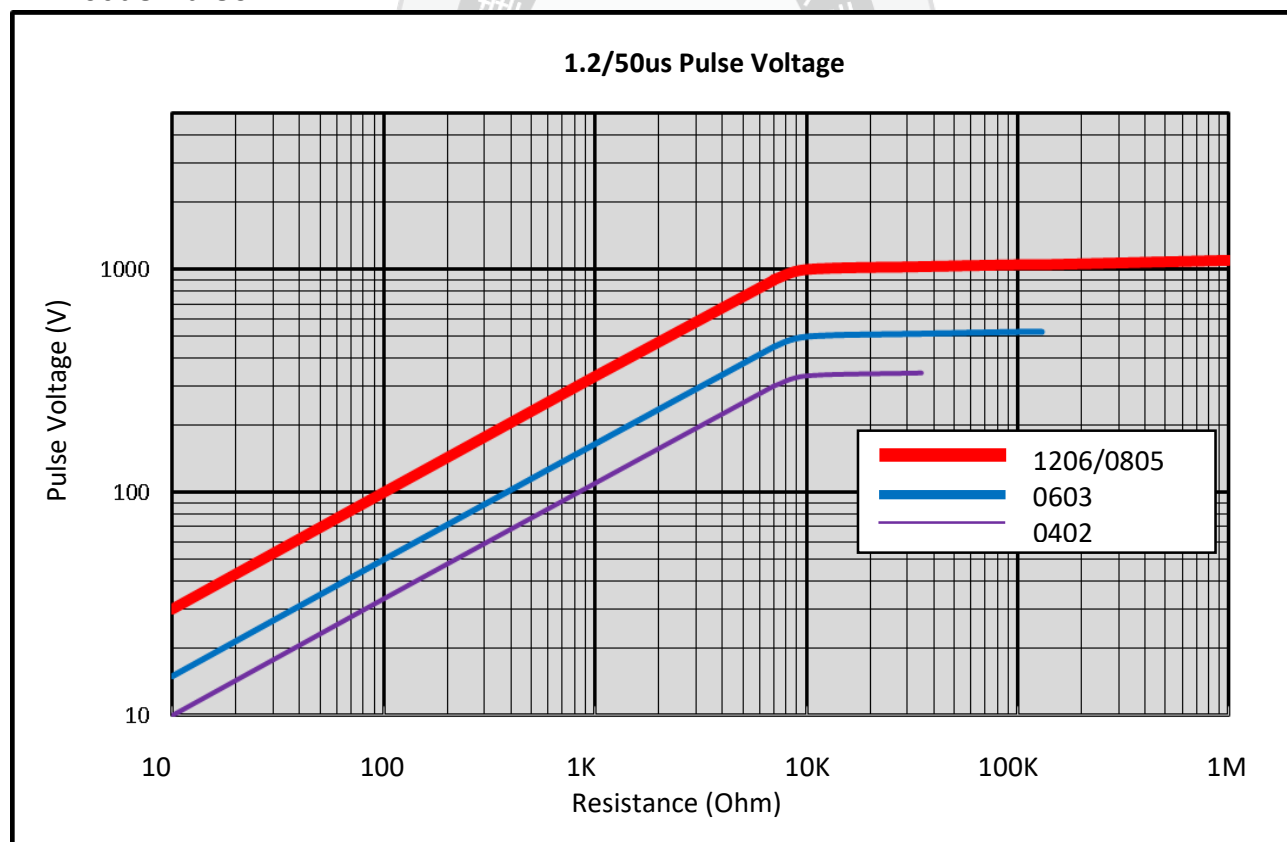
Continuous load is a pulse period generated by the repetitive rectangular pulse amplitude, the applied power dissipation is at a rated power of 70°C .

Permissible the resistance to be varied by $\pm (0.5\% R + 0.01\Omega)$.

Pulse Voltage :

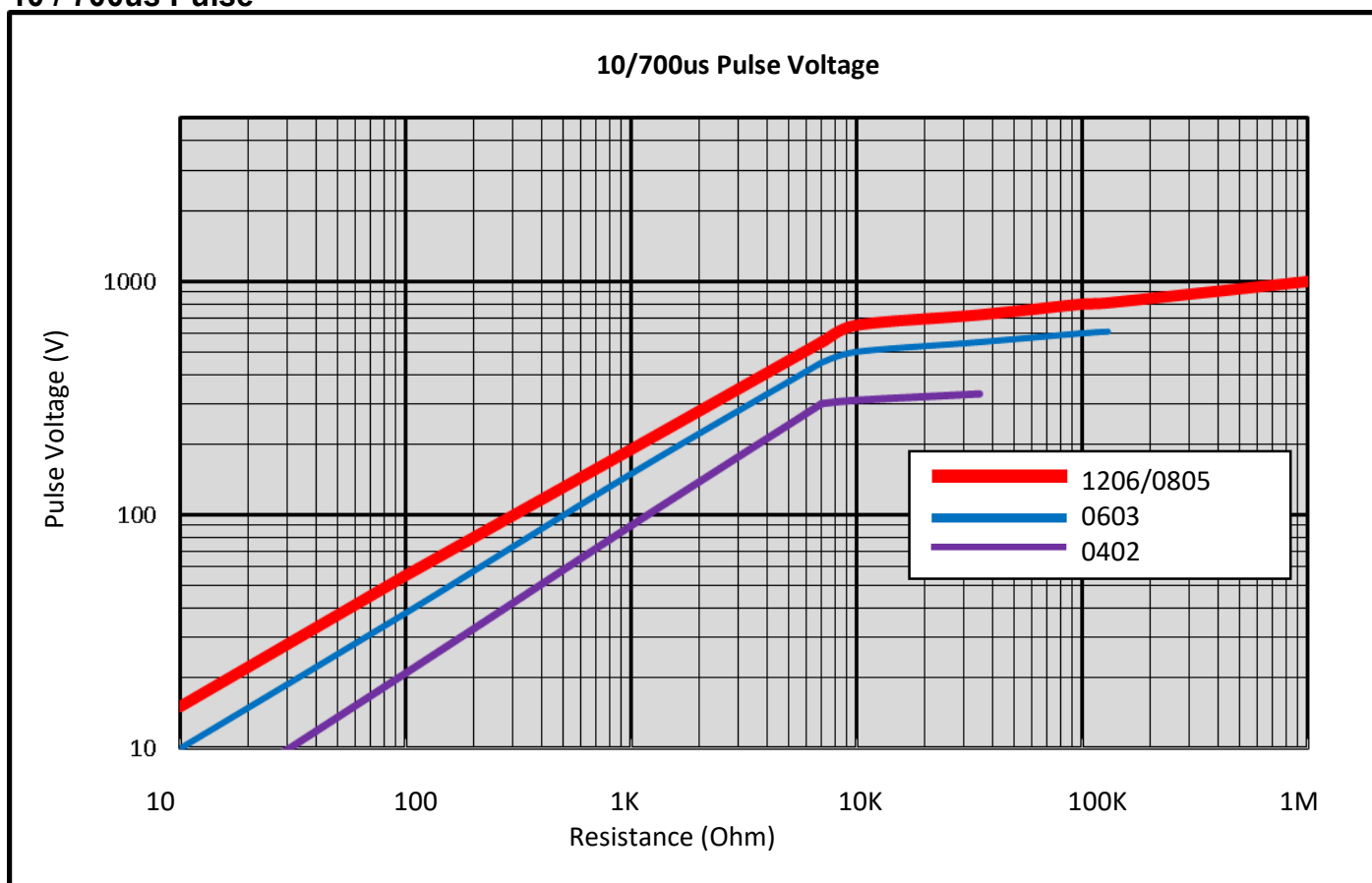


Pulse Surge Overload : 1.2 / 50us Pulse



According to IEC 60115-1 4.27 1.2 / 50us use 5 pulses at 12sec intervals pulse shapes test resistor, permissible the resistance to be varied by $\pm (0.5\% R + 0.01\Omega)$.

10 / 700us Pulse



According to IEC 60115-1 4.27 10 / 700us use 10 pulses at 60sec intervals pulse shapes test resistor, permissible the resistance to be varied by $\pm (0.5\% R + 0.01\Omega)$.