



Product Change Notification: DSNO-10YXV0948

Date:

18-Aug-2025

Product Category:

General Purpose LED Drivers, Linear Regulators, Low-Med Voltage Power Modules, Power Management - PMIC, Power Management - Power Switches, Power Management - System Supervisors/Voltage Detectors, Power Management - Terminators, Switching Regulators

Notification Subject:

CCB 7364.003 Final Notice: Qualification of Microchip Technology Colorado – Fab 5 (MCSO) as a new fabrication location for selected MIC18xx, MIC20xx, MIC23xx, MIC27xx, MIC28xx, MIC33xx, MIC47xx, MIC51xx, MIC53xx, MIC63xx, MIC80xx, MIC81xx, MIC82xx and MIC94xx device families available in various packages.

Affected CPNs:

[DSNO-10YXV0948_Affected_CPN_08182025.pdf](#)

[DSNO-10YXV0948_Affected_CPN_08182025.csv](#)

PCN Status: Final Notification

PCN Type: Manufacturing Change

Microchip Parts Affected: Please open one of the files found in the Affected CPNs section.

Note: For your convenience Microchip includes identical files in two formats (.pdf and .xls)

Description of Change: Qualification of Microchip Technology Colorado – Fab 5 (MCSO) as a new fabrication location for selected MIC18xx, MIC20xx, MIC23xx, MIC27xx, MIC28xx, MIC33xx, MIC47xx, MIC51xx, MIC53xx, MIC63xx, MIC80xx, MIC81xx, MIC82xx and MIC94xx device families available in various packages.

Pre and Post Summary Changes:

	Pre Change	Post Change
Fabrication Site	Microchip Technology Tempe – Fab 2 (TMGR)	Microchip Technology Colorado (MCSO)

Wafer Size	8"	6"
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Impacts to Datasheet: None

Change Impact: None

Reason for Change: To improve manufacturability and on time delivery performance by qualifying a new fabrication location (MCSO - FAB 5), which is a Microchip-owned facility that offers significant expansion potential to better meet future client demand. *Note: The attached file called Tempe_Fab2_IATF_Decertification is for the manufacturing site deactivation of Microchip Technology Tempe – Fab 2 (TMGR), contact your local Microchip sales office for inquiries.

Change Implementation Status: In Progress

Estimated First Ship Date: 29 October 2025 (date code: 2544)

Note Below EFSD: Note: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

Timetable Summary:

	March 2025					>	August 2025					>	October 2025				
Work Week	10	11	12	13	14		32	33	34	35	36		40	41	42	43	44
Initial PCN Issue Date		X															
Qual Report Availability									X								
Final PCN Issue Date									X								
Estimated Implementation Date																	X

Method to Identify Change: Traceability Code

Qualification Report: Please open the attachments included with this PCN labeled as PCN_#_Qual_Report.

Revision History: March 14, 2025: Issued initial notification.

March 17, 2025: Reissued the initial notification to correct the typographical error in the wafer size in the post-change column of the pre- and post-change table summary.

August 18, 2025: Issued final notification. Attached the Qualification Report. Provided the estimated first ship date to be on October 29, 2025.

Note: The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable product.

Attachments:

[PCN_DSNO-10YXV0948_Qual Report.pdf](#)

[Tempe Fab 2-IATF_Decertification.pdf](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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