

**Current Probe Connection** 

small size.

measurement will not run.

**Components and Connections** 

GROUND

**VOUT-**

**VOUT+** 

IN-

IN+

OUT-

OUT+

**T-**

T+

**W1** 

8 A to 12 A

**Power Analyzer Board Connections** 

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The current probe has circuitry that measures the DUT output voltage and the board temperature. Before running a measurement, the software estimates the temperature rise of the current source FETs based on

The current probe connection to the board must be made to minimize the parasitic inductance and resistance. The best way to do this for currents  $> \sim 5$ A is to scrape away an opening in the solder mask on the

the DUT output voltage, the current waveform profile, and the measured board temperature. If the calculation predicts that the maximum safe operating area (SOA) of FETs would be exceeded, the

120 mA

3

board, then solder the probe directly to the board using a piece of solder braid folded at a 90-degree angle on either side to provide a strong mechanical connection.

The LTpowerAnalyzer Main Board (LB3031A) extends the electrical capabilities of the ADALM2000 Active Learning Module (M2K) by providing an analog signal chain interface that can accommodate ±100

**BOARD PINOUT** 

Input negative terminal pin for the DUT output voltage sensing

Input positive terminal pin for the DUT output voltage sensing

Output negative terminal for the Isolated Signal Injection from W1

Output positive terminal for the Isolated Signal Injection from W1

**LEGENDS** 

16-PIN MALE SOCKET TO CURRENT

ADALM2000 (M2K) INTERFACE

GROUND VOUT-VOUT+ IN-IN+

OUT+

W1

CONNECTOR

Pass-through for ADALM2000 W1 Waveform Generator

Ground pin of the LTpowerAnalyzer Main Board

Kelvin Sense Pair for the DUT input T-

Kelvin Sense Pair for the DUT input T+

TO ADALM2000 (M2K)

LTpowerAnalyzer Board (LB3031A) Pinout

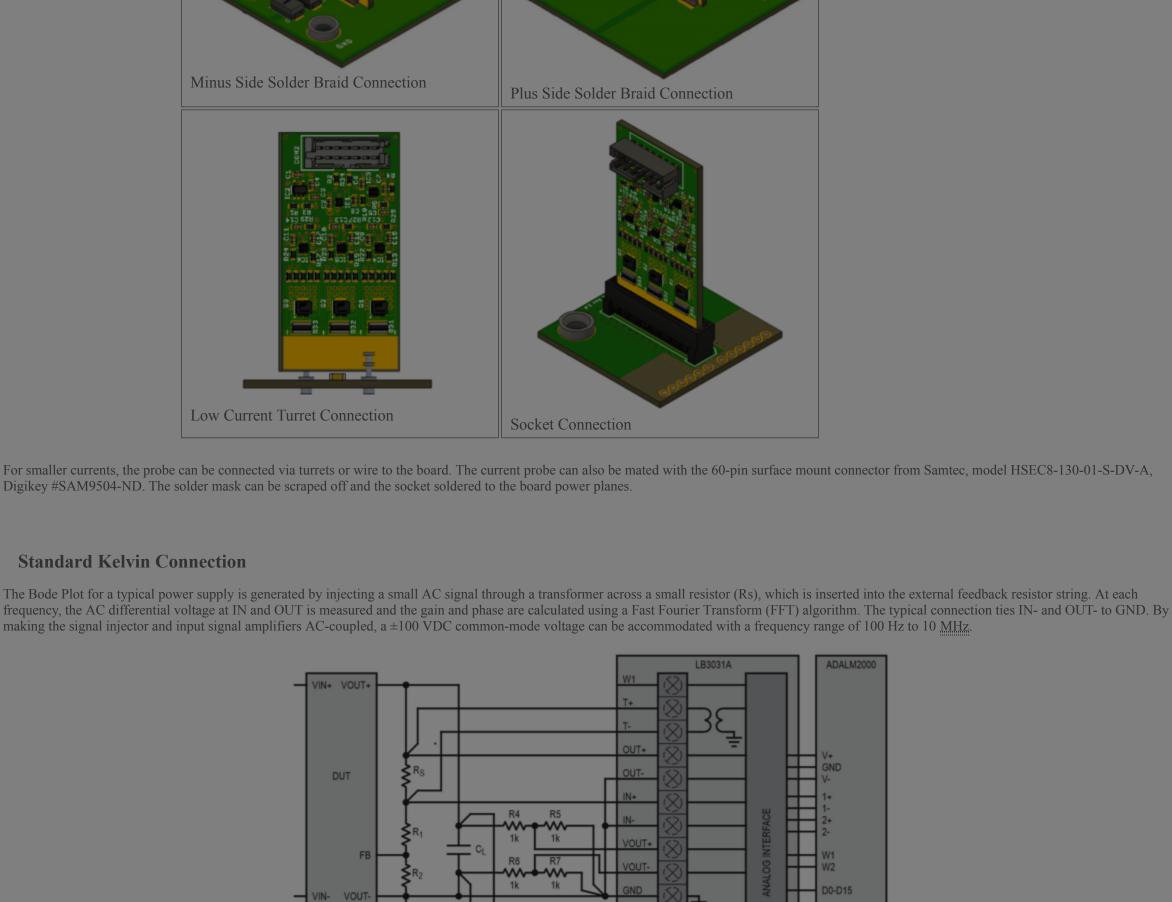
The current probes are used to generate a current pulse for the transient measurement, and a sine wave current for the impedance measurement. There are four versions of the LB3058A: 1A, 10A, 50A, and 100A full scale with a 20 V maximum output voltage. Each probe has three current sources in parallel to provide a wide dynamic range and distribute the power dissipation at full scale. The probes are

connected to the Power Analyzer board using a 14-pin ribbon cable, which is the same as the DC590. The probe is designed to work for positive voltages only, and the FET body diode will turn on if the + pin is pulled below -. The software will automatically recognize which probe is connected, and adjust the current range accordingly. The probes are not designed to generate a continuous DC current due to their

Kelvin Sense Pair for the DUT output VOUT-

Kelvin Sense Pair for the DUT output VOUT+

VDC of differential input voltage at its ports. This enables a broader range of power supply DUTs that can be evaluated with the ADALM2000.



At low frequencies, the open loop gain of the supply is high, so most of the injected AC signal appears at OUT and a tiny amount at IN. At the crossover frequency, the injected signal is evenly split between IN and OUT, and at high frequencies when the gain is less than one, most of the injected signal appears at IN.

A typical setup will use short length 24 to 32 gauge twisted pair wires that are soldered directly to Rs on the DUT board, and the other ends inserted into the screw terminals on the LB3031A or the interface board. The setup requires a kelvin connection that has 4 wires (T+, OUT+, T-, IN+) connected to Rs to get the best results.

The LB3058A current probe must be connected to the DUT board with a minimum of series resistance and inductance. The output voltage for the transient and impedance measurements should be sensed

directly at the output capacitors on the DUT and is measured though a fully differential signal path. The LB3058A current probe is not designed to generate a constant DC current due to its small size, so an external DC load generator will be needed for the Bode Plot and Output Impedance measurements. The transient measurement does not require an external DC load current as long as the pulse times are short.

The ground plane of the DUT must be connected to the GND pin on the terminal block.

An example is shown below where 6-inches of 30 gauge twisted pair wire was used for the two-wire and kelvin connections.

/IN+ VOUT+

DUT

VIN- VOUT-

**Kelvin vs. Two-Wire Connection** 

LB3058A

VOUT

GND

LB3031A

VOUT+

TEMPERATURE

ADALM2000

D0-D15

Many power modules include the top side feedback resistor R1 inside the package, requiring only one external resistor R2 to set the output voltage. In this case, the injection resistor R3 cannot be inserted at the top of the feedback network but is instead connected to ground at the bottom.

To measure the true top gain, another dummy resistor divider, R3 and R4, is connected to the output. The ratio of R3:R4 must be the same as the ratio of R1:R2. The problem with spolegy is that many network analyzers have up to 50 gb of input capacitance, which forms a pole at the k3:R4 connection point. This can lead to gain and phase errors when the frequency reaches the 50 kHz/to 1 MHz/range, depending on the absolute value of R3 and R4. The best practice is to reduce the absolute value or R3 compared to R1, and R4 compared to R2 by a factor of 10 to 100, pushing the pole out to beyond the frequencies of interest. The impulies impedance of the L1-proverAnalyzers is about 1 pg. in parallel with 1 MO, which should minimize this problem.

Follow below configuration to set up the system using Module Connection:

\*\*Budge Plot Module Connection\*\*

Budge Plot Module Connection\*\*

Budge Plot Module Connection\*\*

**2-Wire Bode Plot Connection** 

open loop gain drops and the input signal level increases. Notice however, that the phase margin and gain margin are the same for the two sweeps.

The results below 5 kHz are quite different because the voltage drop across the wires swamps out the signal at the input, which is very low because of the high open loop gain. The two sweeps merge as the

In this type of connection, the test voltage is injected across Rs which is in series with the ISN current sense line, and input and output voltage must be measured differentially from the respective Rs connection

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-LTPA-KIT Hardware Guide

Next.: EVAL-LTPA-KIT Software User

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**Bode Plot LED Driver Connection** 

Please refer to the following pages for detailed instructions on how to set up the LTpowerAnalyzer system for evaluation.

• EVAL-LTPA-KIT Hardware Setup Guide
• EVAL-LTPA-KIT Software Setup Guide

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The common-mode range of the LTPowerAnalyzer is  $\pm 100 \, \text{M}$ , and Rs is typically 20  $\Omega$  to 40  $\Omega$ .

**LED Driver Connection** 

**Getting Started** 

to the ISP current sense line.

	Up: <u>EVAL</u>

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