PCN Number:	20241112001.1	PCN Date:	November 13, 2024
Title:	Datasheet for OPAx132		
Customer Contact:	Change Management team	Dept:	Quality Services
Proposed 1st Ship Date:	February 11, 2025		
Change Type:			

☐	Assembly Site	☐	Design	☐	Wafer Bump Material
☐	Assembly Process	☒	Datasheet	☐	Wafer Bump Process
☐	Assembly Materials	☐	Part number change	☐	Wafer Fab Site
☐	Mechanical Specification	☐	Test Site	☐	Wafer Fab Materials
☐	Packing/Shipping/Labeling	☐	Test Process	☐	Wafer Fab Process

PCN Details

Description of Change:

The product datasheet(s) is being updated as summarized below.
The following change history provides further details.

TEXAS INSTRUMENTS

OPA132, OPA2132, OPA4132
SBOS054C – JANUARY 1995 – REVISED AUGUST 2024

Changes from Revision B (September 2015) to Revision C (August 2024) **Page**

- Updated the numbering format for tables, figures, and cross-references throughout the document ... 1
- Added information about newer, next-generation OPA2156 ... 1
- Changed high open-loop gain from 130dB (600Ω load) to 126dB (2kΩ load) ... 1
- Deleted all single and quad channel PDIP package references from this data sheet ... 1
- Updated *Device Information* table ... 1
- Updated *Pin Configuration and Functions* format ... 3
- Changed OPA132 pin 1 and pin 8 from "Offset Trim" to "NC" ... 3
- Changed input voltage from (V–) – 0.7 and (V+) + 0.7 to (V–) – 0.5 and (V+) + 0.5 in *Absolute Maximum Ratings* ... 5
- Added input current and related footnote to *Absolute Maximum Ratings* ... 5
- Added *Thermal Information* ... 6
- Changed format of *Electrical Characteristics* to latest standard ... 7
- Updated nominal conditions in the header of *Electrical Characteristics* ... 7
- Added ± to power supply rejection ratio (offset vs temperature) and input bias current values ... 7
- Changed common-mode voltage MAX value from (V+) – 2.5V to (V+) – 3.5V ... 7
- Changed common-mode rejection ratio and common-mode input impedance test conditions from –12.5V ≤ V_{CM} ≤ 12.5V to –12.5V ≤ V_{CM} ≤ 11.5V ... 7
- Changed differential input impedance from 10^{10}Ω || 2pF to 10^{10}Ω || 10pF ... 7
- Changed common-mode input impedance from 10^{10}Ω || 6pF to 10^{10}Ω || 7pF ... 7
- Changed overload recovery time from 0.5µs to 600ns ... 7
- Changed overload recovery time test condition from G = ± to G = ±1 to fix typo ... 7
- Moved voltage output negative MIN values to MAX values ... 7
- Deleted redundant power supply and temperature range sections already covered in *Recommended Operating Conditions* ... 7
- Deleted note 1 from Electrical Characteristics ... 7
- Changed *Typical Characteristics* header test conditions to match *Electrical Characteristics* ... 8
- Changed Figure 16, *Small-Signal Overshoot vs Load Capacitance* into two plots, Figure 5-16 for G = +1 and Figure 5-17 for G = –1 ... 8
- Updated Figure 5-18, *Output Voltage Swing vs Output Current* ... 8
- Updated *Functional Block Diagram* ... 11
- Updated *Offset Voltage Trim* ... 12
- Updated Figure 7-3, *OPA132 Layout Example for the Noninverting Configuration* ... 15

The datasheet number will be changing.

Device Family	Change From:	Change To:
OPAx132	SBOS054B	**SBOS054C**

The changes may be reviewed at the datasheet links provided.
http://www.ti.com/product/OPA132

Reason for Change:

To accurately reflect device characteristics. The datasheet update is driven by the changes announced in PCNs 20231219008 and 20240613004

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

Electrical specification performance changes as indicated above.

Changes to product identification resulting from this PCN:

None.

Product Affected:

Devices affected by changes in PCN 20231219008

OPA132U	OPA132UA	OPA2132U	OPA2132UA
OPA132U/2K5	OPA132UA/2K5	OPA2132U/2K5	OPA2132UA/2K5

Devices affected by changes in PCN 20240613004

OPA4132UA	OPA4132UA/2K5		

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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