

<b>PCN Number:</b>	20241112001.1	<b>PCN Date:</b>	November 13, 2024
<b>Title:</b>	Datasheet for OPAx132		
<b>Customer Contact:</b>	Change Management team	<b>Dept:</b>	Quality Services
<b>Proposed 1<sup>st</sup> Ship Date:</b>	February 11, 2025		
<b>Change Type:</b>			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Datasheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

## PCN Details

### Description of Change:

The product datasheet(s) is being updated as summarized below. The following change history provides further details.



OPA132, OPA2132, OPA4132

SBOS054C – JANUARY 1995 – REVISED AUGUST 2024

### Changes from Revision B (September 2015) to Revision C (August 2024)

**Page**

• Updated the numbering format for tables, figures, and cross-references throughout the document.....	1
• Added information about newer, next-generation OPA2156.....	1
• Changed high open-loop gain from 130dB (600Ω load) to 126dB (2kΩ load).....	1
• Deleted all single and quad channel PDIP package references from this data sheet.....	1
• Updated <i>Device Information</i> table.....	1
• Updated <i>Pin Configuration and Functions</i> format.....	3
• Changed OPA132 pin 1 and pin 8 from "Offset Trim" to "NC".....	3
• Changed input voltage from (V <sup>-</sup> ) – 0.7 and (V <sup>+</sup> ) + 0.7 to (V <sup>-</sup> ) – 0.5 and (V <sup>+</sup> ) + 0.5 in <i>Absolute Maximum Ratings</i> .....	5
• Added input current and related footnote to <i>Absolute Maximum Ratings</i> .....	5
• Added <i>Thermal Information</i> .....	6
• Changed format of <i>Electrical Characteristics</i> to latest standard.....	7
• Updated nominal conditions in the header of <i>Electrical Characteristics</i> .....	7
• Added ± to power supply rejection ratio (offset vs temperature) and input bias current values.....	7
• Changed common-mode voltage MAX value from (V <sup>+</sup> ) – 2.5V to (V <sup>+</sup> ) – 3.5V.....	7
• Changed common-mode rejection ratio and common-mode input impedance test conditions from –12.5V ≤ V <sub>CM</sub> ≤ 12.5V to –12.5V ≤ V <sub>CM</sub> ≤ 11.5V.....	7
• Changed differential input impedance from 10 <sup>10</sup> Ω    2pF to 10 <sup>10</sup> Ω    10pF.....	7
• Changed common-mode input impedance from 10 <sup>10</sup> Ω    6pF to 10 <sup>10</sup> Ω    7pF.....	7
• Changed overload recovery time from 0.5μs to 600ns.....	7
• Changed overload recovery time test condition from G = ± to G = ±1 to fix typo.....	7
• Moved voltage output negative MIN values to MAX values.....	7
• Deleted redundant power supply and temperature range sections already covered in <i>Recommended Operating Conditions</i> .....	7
• Deleted note 1 from <i>Electrical Characteristics</i> .....	7
• Changed <i>Typical Characteristics</i> header test conditions to match <i>Electrical Characteristics</i> .....	8
• Changed Figure 16, <i>Small-Signal Overshoot vs Load Capacitance</i> into two plots, Figure 5-16 for G = +1 and Figure 5-17 for G = –1.....	8
• Updated Figure 5-18, <i>Output Voltage Swing vs Output Current</i> .....	8
• Updated <i>Functional Block Diagram</i> .....	11
• Updated <i>Offset Voltage Trim</i> .....	12
• Updated Figure 7-3, <i>OPA132 Layout Example for the Noninverting Configuration</i> .....	15

The datasheet number will be changing.			
Device Family	Change From:	Change To:	
OPAx132	SBOS054B	SBOS054C	
The changes may be reviewed at the datasheet links provided. <a href="http://www.ti.com/product/OPA132">http://www.ti.com/product/OPA132</a>			
<b>Reason for Change:</b>			
To accurately reflect device characteristics. The datasheet update is driven by the changes announced in PCNs 20231219008 and 20240613004			
<b>Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):</b>			
Electrical specification performance changes as indicated above.			
<b>Changes to product identification resulting from this PCN:</b>			
None.			
<b>Product Affected:</b>			
Devices affected by changes in PCN 20231219008			
OPA132U	OPA132UA	OPA2132U	OPA2132UA
OPA132U/2K5	OPA132UA/2K5	OPA2132U/2K5	OPA2132UA/2K5
Devices affected by changes in PCN 20240613004			
OPA4132UA	OPA4132UA/2K5		

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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