

NOT RECOMMENDED FOR NEW DESIGN USE DMG2301L



DMG2301U

P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
001/	$80m\Omega @ V_{GS} = -4.5V$	-2.7A
-20V	$110m\Omega @ V_{GS} = -2.5V$	-2.1A

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

Applications

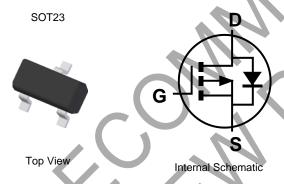
- Backlighting
- Power Management Functions
- DC-DC Converters
- Motor Control

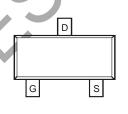
Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 @3
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)





Top View Pin Configuration

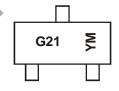
Ordering Information (Note 4)

Part Number	Case	Packaging
DMG2301U-7	SOT23	3,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



G21 = Product Type Marking Code YM = Date Code Marking Y or \overline{Y}= Year (ex: E = 2017) M = Month (ex: 9 = September)

Date Code Key

Year	2009		20	17	2018	2019	2020	2021	20)22	2023	2024
Code	W		E	E	F	G	Н			J	K	L
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	-20	V
Gate-Source Voltage		V_{GSS}	±8	V
Continuous Drain Current (Note 5) V _{GS} = -4.5V	I _D	-2.7 -2.1	Α	
Continuous Drain Current (Note 5) V _{GS} = -2.5V	I _D	-2.1 -1.7	Α	
Pulsed Drain Current (Note 6)		I _{DM}	-27	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	0.8	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	R _{0JA}	157	°C/W
Operating and Storage Temperature Range	T_{J}, T_{STG}	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	-20		\leftarrow	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}			-1.0	μΑ	$V_{DS} = -16V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_		±100	nA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(TH)}$	-0.45	_	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance	Program			80	mΩ	$V_{GS} = -4.5V$, $I_{D} = -2.8A$	
Static Brain Source On Nesistance	Rds(on)			110	11122	$V_{GS} = -2.5V$, $I_D = -2.0A$	
Forward Transfer Admittance	Y _{fs}	-	10	_	S	$V_{DS} = -5V, I_{D} = -2.8A$	
Diode Forward Voltage	V _{SD}		-0.75	-1.0	٧	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	<u> </u>	608	_	рF	V 6V V 6V	
Output Capacitance	Coss		82	_	pF	$V_{DS} = -6V, V_{GS} = 0V$ -f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	72	_	рF	1 = 1.0101112	
Gate Resistance	R _G	_	44.9	_	Ω	$V_{GS} = 0V, V_{DS} = 0V, f = 1.0MHz$	
Total Gate Charge	Q_g	_	6.5	_	nC		
Gate-Source Charge	Qgs	_	0.9	_	nC	$V_{GS} = -4.5V$, $V_{DS} = -10V$, $I_{D} = -3A$	
Gate-Drain Charge	Q_{gd}	_	1.5	_	nC		
Turn-On Delay Time	t _{D(ON)}	_	12.5	40	ns		
Turn-On Rise Time	t _R	_	10.3	30	ns	$V_{DS} = -10V, V_{GS} = -4.5V,$	
Turn-Off Delay Time	t _{D(OFF)}	_	46.5	140	ns	$R_L = 10\Omega, R_G = 1.0\Omega, I_D = -1A$	
Turn-Off Fall Time	t _F	_	22.2	66	ns		

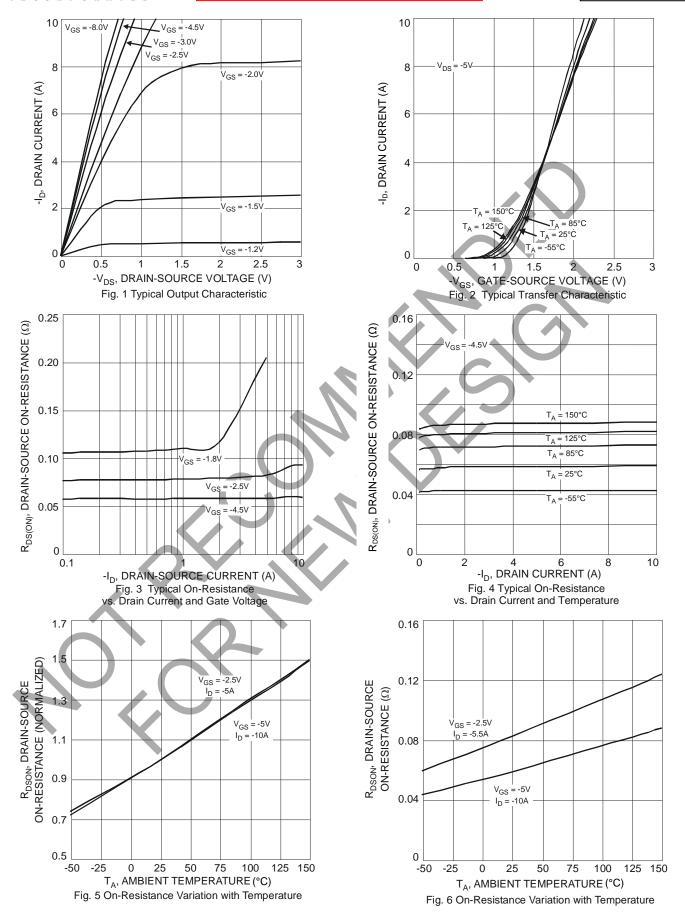
Notes:

- Device mounted on FR-4 PCB with minimum recommended pad layout.
 Repetitive rating, pulse width limited by junction temperature.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.



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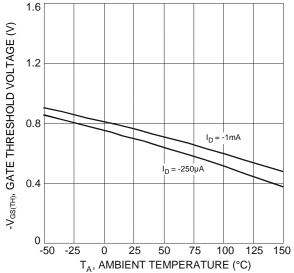
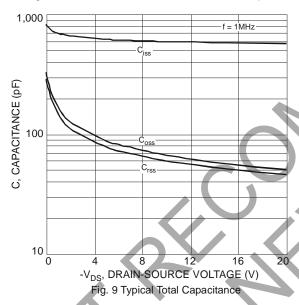
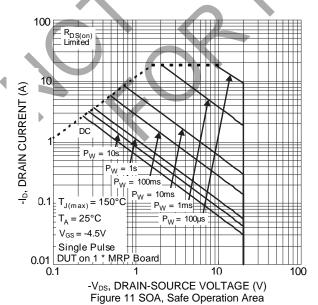
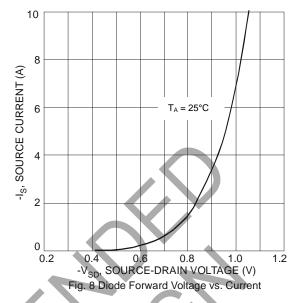


Fig. 7 Gate Threshold Variation vs. Ambient Temperature







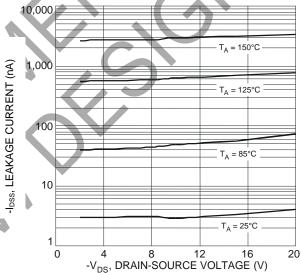
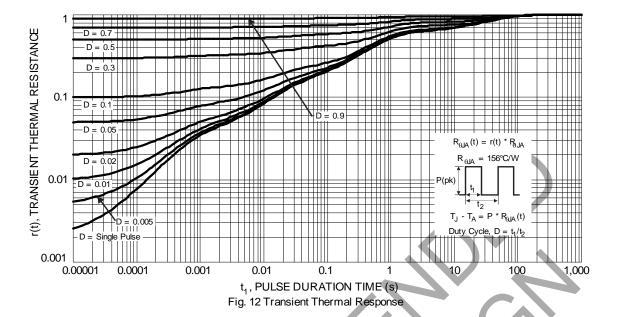


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage



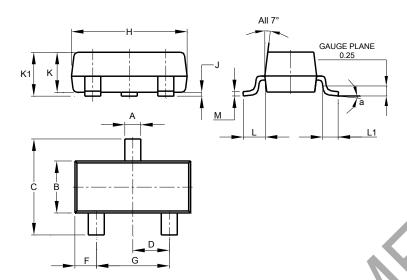




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

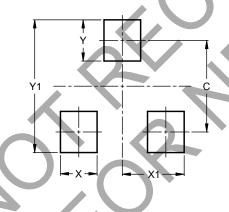


SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
M	0.085	0.150	0.110				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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