

General Description

The SN74LVC1G07 is a high performance single non-inverting buffer with open drain outputs operating from a 1.65 to 5.5V supply. The Output stage is open drain with Over Voltage Tolerance. This allows the SN74LVC1G07 to be used to interface 5.0V circuits to circuits of any voltage between 0 and +7.0V.

Features

- Tiny SC70-5, SOT23-5 and DFN6-1.0mm*1.5mm Packages
- Designed for 1.65V to 5.5V V_{CC} Operation
- 24 mA Output Sink Capability, Pullup may be between 0 and 7.0V
- Near Zero Static Supply Current Substantially Reduces System Power Requirements
- These Devices are Pb-Free and RoHS Compliant

Pin Configuration

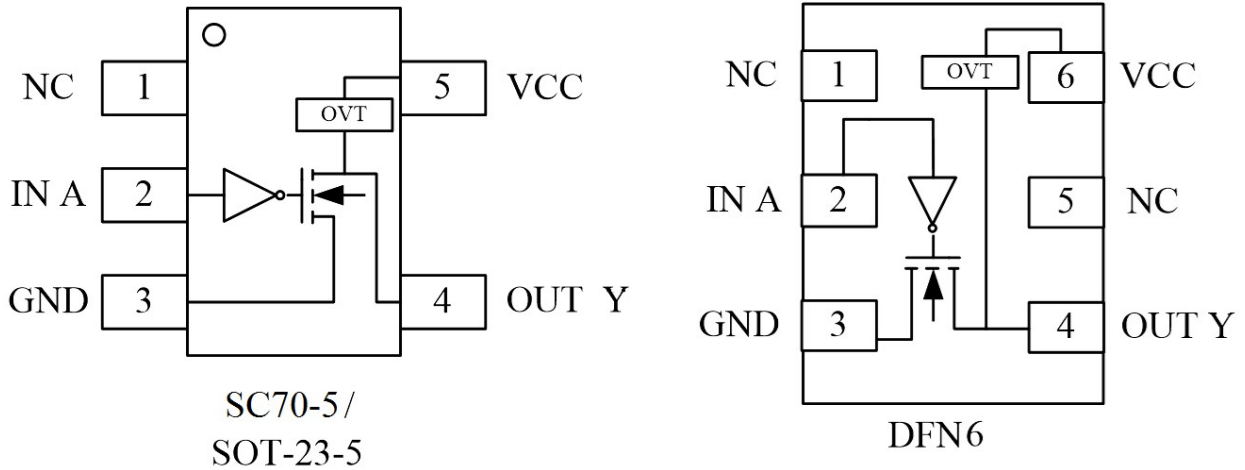


Figure1, Top View

Pin Function

(SC70-5/ SOT23-5)

| PIN | FUNCTION |
|-----|----------|
| 1 | NC |
| 2 | IN A |
| 3 | GND |
| 4 | OUT |
| 5 | VCC |

(DFN6)

| PIN | FUNCTION |
|-----|----------|
| 1 | NC |
| 2 | IN A |
| 3 | GND |
| 4 | OUT |
| 5 | NC |
| 6 | VCC |

Block Diagram

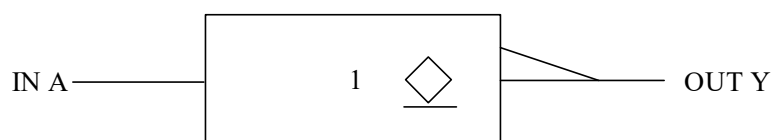


Figure 2. Logic Symbol

Functional Description

Function Table

| Input | Output |
|-------|--------|
| A | Y |
| L | L |
| H | Z |

Absolute Maximum Ratings

| Symbol | Parameter | Value | Unit |
|---------------|--|---------------------------|------|
| V_{CC} | DC Supply Voltage | -0.5 to 7.0 | V |
| V_I | DC Input Voltage | $-0.5 \leq V_I \leq +7.0$ | V |
| V_O | DC Output Voltage Output in Higher or Low State (Note 1) | -0.5 to $V_{CC} + 0.5$ | V |
| I_{IK} | DC Input Diode Current $V_I < GND$ | -50 | mA |
| I_{OK} | DC Output Diode Current $V_O < GND, V_O > V_{CC}$ | ± 50 | mA |
| I_O | DC Output Sink Current | ± 50 | mA |
| I_{CC} | DC Supply Current per Supply Pin | ± 100 | mA |
| I_{GND} | DC Ground Current per Supply Pin | ± 100 | mA |
| T_{STG} | Storage Temperature Range | -65 to 150 | °C |
| T_L | Lead Temperature, 1 mm from Case for 10 Seconds | 260 | °C |
| T_J | Junction Temperature Under Bias | 150 | °C |
| θ_{JA} | Thermal Resistance SC70-6 | 333 | °C/W |
| | SOT23-6 | 225 | |
| | DFN6 | 423 | |
| P_D | Power Dissipation in Still Air at 85°C | 200 | mW |
| MSL | Moisture Sensitivity | Level 1 | |
| ESD | ESD Classification Human Body Model (Note 2) | 2000 | V |
| | Machine Model (Note 3) | 200 | |
| | Charged Device Model (Note 4) | N/A | |
| $I_{Latchup}$ | Latchup Performance Above V_{CC} and Below GND at 125°C (Note 5) | ± 100 | mA |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. IO absolute maximum rating must be observed.
2. Tested to EIA/JESD22-A114-A, rated to EIA/JESD22-A114-B.
3. Tested to EIA/JESD22-A115-A, rated to EIA/JESD22-A115-A.
4. Tested to JESD22-C101-A.
5. Tested to EIA/JESD78.

Recommended Operating Conditions

| Symbol | Parameter | Min | Max | Unit |
|------------|---|------|-----|------|
| V_{CC} | DC Supply Voltage Operating | 1.65 | 5.5 | V |
| | Date Retention | 1.5 | 5.5 | |
| V_{IN} | DC Input Voltage | 0 | 5.5 | V |
| V_{OUT} | DC Output Voltage (High or Low State) | 0 | 5.5 | V |
| T_A | Operating Temperature Range | -40 | 85 | °C |
| t_r, t_f | Input Rise and Fall Time $V_{CC} = 2.5 V \pm 0.2 V$ | 0 | 20 | ns/V |
| | $V_{CC} = 3.0 V \pm 0.3 V$ | 0 | 10 | |
| | $V_{CC} = 5.0 V \pm 0.5 V$ | 0 | 5 | |

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied.

Electrical Characteristics

DC ELECTRICAL CHARACTERISTICS

| Symbol | Parameter | Condition | VCC(V) | TA = 25 °C | | | -40°C ≤TA≤85°C | | Unit |
|------------------|--|---|--------------------------|---|------|---|---|---|------|
| | | | | Min | Typ | Max | Min | Max | |
| V _{IH} | High-Level Input Voltage | | 1.65to1.95 2.3 to 5.5 | 0.75V _{cc} 0.7V _{cc} | | | 0.75V _{cc} 0.7V _{cc} | | V |
| V _{IL} | Low-Level Input Voltage | | 1.65to1.95 2.3 to 5.5 | | | 0.25V _{cc} 0.3V _{cc} | | 0.25V _{cc} 0.3V _{cc} | V |
| V _{OL} | Low-Level Output Voltage V _{IN} = V _{IL} | I _{OL} =100uA | 1.65to5.5 | | 0.0 | 0.1 | | 0.1 | V |
| | | I _{OL} =3mA | 1.65 | | 0.08 | 0.24 | | 0.24 | |
| | | I _{OL} =8mA | 2.3 | | 0.20 | 0.3 | | 0.3 | |
| | | I _{OL} =12mA | 2.7 | | 0.22 | 0.4 | | 0.4 | |
| | | I _{OL} =16mA | 3.0 | | 0.28 | 0.4 | | 0.4 | |
| | | I _{OL} =24mA | 3.0 | | 0.38 | 0.55 | | 0.55 | |
| | | I _{OL} =32mA | 4.5 | | 0.42 | 0.55 | | 0.55 | |
| I _{IN} | Input Leakage Current | V _{IN} = 5.5 V or GND | 0 to 5.5 | | | ±0.1 | | ±1.0 | uA |
| I _{OFF} | Power Off Leakage Current | V _{IN} = 5.5 V or V _{OUT} = 5.5 V | 0 | | | 1 | | 10 | uA |
| I _{CC} | Quiescent Supply Current | V _{IN} = 5.5 V or GND | 5.5 | | | 1 | | 10 | uA |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

AC ELECTRICAL CHARACTERISTICS tr=tf= 2.5ns;

| Symbol | Parameter | Condition | Vcc(V) | TA = 25 °C | | | -40°C ≤TA≤85°C | | Unit |
|------------------|---------------------------------|--|--------|------------|-----|------|----------------|------|------|
| | | | | Min | Typ | Max | Min | Max | |
| t _{pZL} | Propagation Delay (Figure3and4) | R _L = 500Ω C _L = 15 pF | 1.8 | 2.0 | 9.1 | 11.1 | 2.0 | 12.8 | ns |
| | | | 2.5 | 1.5 | 6.1 | 7.5 | 1.5 | 8.5 | |
| | | | 3.3 | 1.2 | 4.9 | 6.0 | 1.5 | 6.9 | |
| | | | 5.0 | 0.8 | 3.9 | 4.9 | 0.8 | 5.5 | |
| t _{pLZ} | Propagation Delay (Figure3and4) | R _L = 500Ω C _L = 15 pF | 1.8 | 2.0 | 9.1 | 11.1 | 2.0 | 12.8 | ns |
| | | | 2.5 | 1.5 | 5.9 | 7.3 | 1.5 | 8.3 | |
| | | | 3.3 | 1.2 | 3.9 | 4.9 | 1.5 | 5.5 | |
| | | | 5.0 | 0.8 | 3.0 | 3.7 | 0.8 | 4.2 | |

CAPACITIVE CHARACTERISTICS

| Symbol | Parameter | Condition | Typical | Unit |
|-----------------|---|--|---------|------|
| C _{IN} | Input Capacitance | V _{CC} = 5.5 V, V _I = 0 V or V _{CC} | >2.5 | pF |
| C _{PD} | Power Dissipation Capacitance (Note 6) | 10 MHz, V _{CC} = 3.3 V, V _I = 0 V or V _{CC} | 26 | pF |
| | | 10 MHz, V _{CC} = 5.5 V, V _I = 0 V or V _{CC} | 30 | |

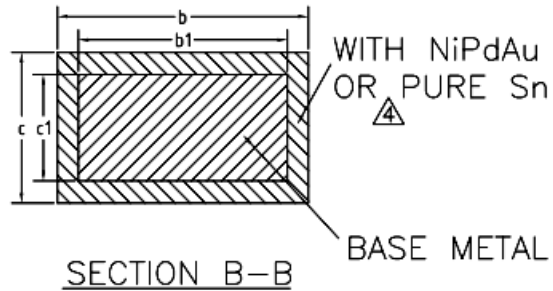
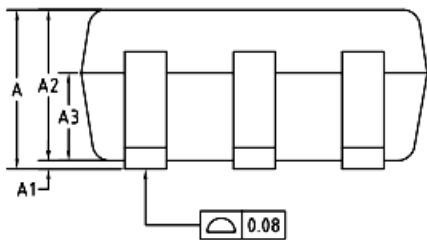
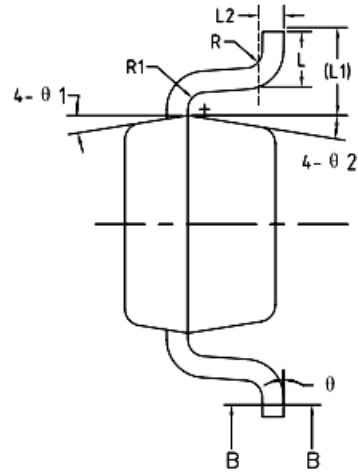
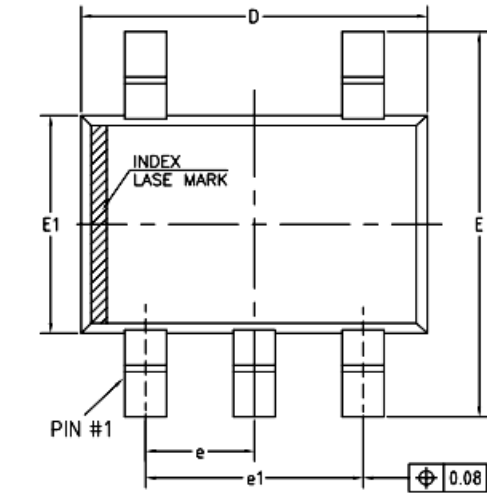
6. CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation:

$ICC(OPR)=CPD*VCC*fin+ICC*CPD$ is used to determine the no-load dynamic power consumption;

$PD=CPD*VCC2*fin+ICC*VCC*Fig$.

Package Dimension

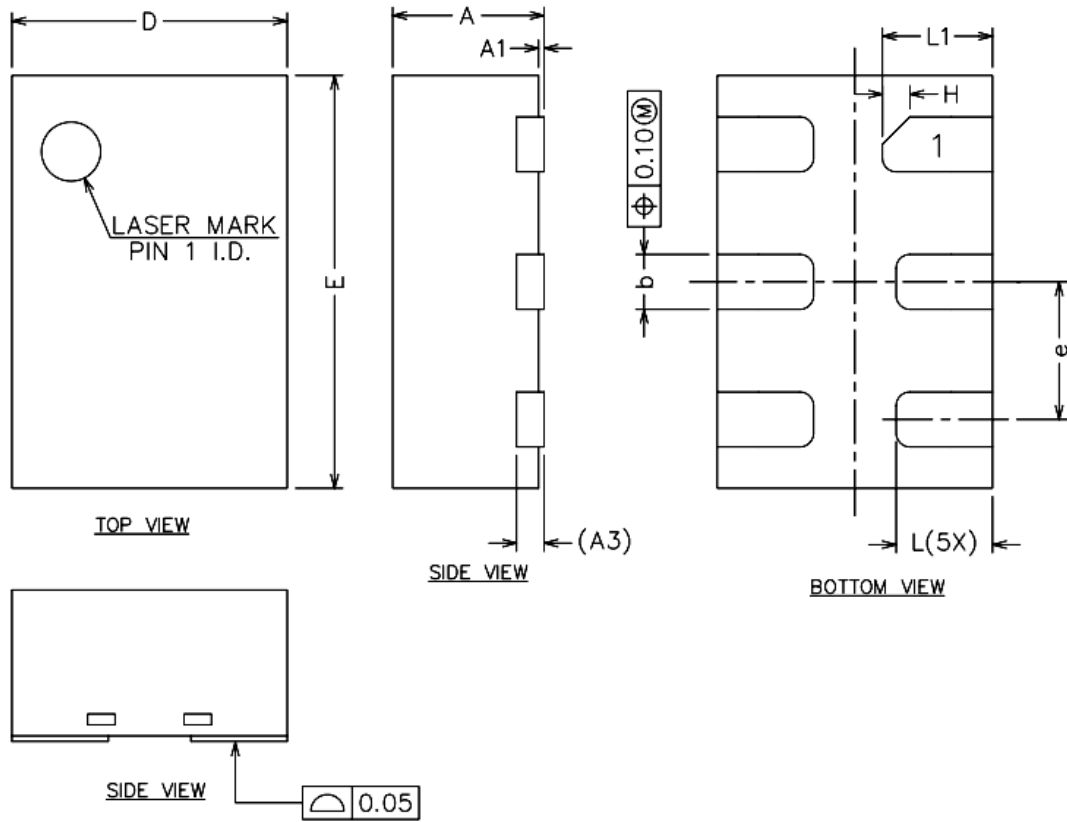
SC70-5



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

| SYMBOL | MIN | NOM | MAX |
|--------|--------------|------|------|
| A | 0.85 | — | 1.05 |
| A1 | 0 | — | 0.10 |
| A2 | 0.80 | 0.90 | 1.00 |
| A3 | 0.47 | 0.52 | 0.57 |
| b | NiPdAu 0.22 | — | 0.29 |
| | PURE Sn 0.23 | — | 0.33 |
| b1 | 0.22 | 0.25 | 0.28 |
| c | NiPdAu 0.115 | — | 0.15 |
| | PURE Sn 0.12 | — | 0.18 |
| c1 | 0.115 | 0.13 | 0.14 |
| D | 2.02 | 2.07 | 2.12 |
| E | 2.20 | 2.30 | 2.40 |
| E1 | 1.25 | 1.30 | 1.35 |
| e | 0.60 | 0.65 | 0.70 |
| e1 | 1.20 | 1.30 | 1.40 |
| L | 0.28 | 0.33 | 0.38 |
| L1 | 0.50REF | | |
| L2 | 0.15BSC | | |
| R | 0.10 | — | — |
| R1 | 0.10 | — | 0.25 |
| θ | 0° | — | 8° |
| θ 1 | 6° | 9° | 12° |
| θ 2 | 6° | 9° | 12° |

DFN6



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

| SYMBOL | MIN | NOM | MAX |
|--------|---------|------|------|
| A | 0.50 | — | 0.60 |
| A1 | 0.00 | 0.02 | 0.05 |
| A3 | 0.10REF | | |
| b | 0.15 | 0.20 | 0.25 |
| D | 0.90 | 1.00 | 1.10 |
| E | 1.40 | 1.50 | 1.60 |
| e | 0.40 | 0.50 | 0.60 |
| H | 0.10REF | | |
| L | 0.30 | 0.35 | 0.40 |
| L1 | 0.35 | 0.40 | 0.45 |

Ordering information

| Order code | Marking code | Package | Baseqty | Deliverymode |
|-------------------|---------------------|----------------|----------------|---------------------|
| SN74LVC1G07DBVR | C075 | SOT23-5 | 3000 | Tape and reel |
| SN74LVC1G07DCKR | CV5 | SC70-5 | 3000 | Tape and reel |
| SN74LVC1G07DRYR | CV | DFN6 | 5000 | Tape and reel |