

# S2M0080120T

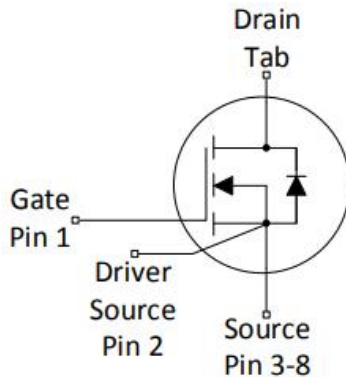
## 1200V SiC POWER MOSFET



### Description

S2M0080120T is single SiC Power MOSFET packaged in TOLL case. The device is a high voltage n-channel enhancement mode MOSFET that has very low total conduction losses and very stable switching characteristics over temperature extremes. The S2M0080120T is ideal for energy sensitive, high frequency applications in challenging environments.

### Circuit Diagram



### Features

- Positive temperature characteristics, easy to parallel.
- Low on-resistance Typ.  $R_{DS(on)} = 77\text{ m}\Omega$  .
- Fast switching speed and low switching losses.
- Very fast and robust intrinsic body diode.
- Process of non-bright Tin electroplatin

### Applications

- EV Fast Charging Modules
- EV On Board Chargers
- Solar Inverters
- Online UPS/Industrial UPS
- SMPS (Switch Mode Power Supplies)
- DC-DC Converters
- ESS (Energy Storage Systems)

### Maximum Ratings(T=25°C unless otherwise specified)

Characteristics	Symbol	Condition	Max.	Units
Drain Source Voltage	$V_{DSS}$	$V_{GS} = 0\text{V}, I_{DS} = 100\mu\text{A}, T_C = 25^\circ\text{C}$	1200	V
Gate Source Voltage	$V_{GSS}$	$T_C = 25^\circ\text{C}$ , Absolute maximum values, AC (f>1Hz)	- 10 to +25	V
Gate Source Voltage	$V_{GSOP}$	$T_C = 25^\circ\text{C}$ Recommended Operational Values	-5 to +20	V
Continuous Drain Current	$I_D$	$V_{GS} = 20\text{V}, T_C = 25^\circ\text{C}$	41	A
	$I_D$	$V_{GS} = 20\text{V}, T_C = 100^\circ\text{C}$	29	A
Pulsed Drain Current	$I_{D,pulse}$	Pulse width $t_P$ limited by $T_{jmax}$	82	A
Power Dissipation	$P_D$	$T_C=25^\circ\text{C}, T_J = 175^\circ\text{C}$	231	W

**Electrical Characteristics(T=25°C unless otherwise specified)**

Characteristics	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 1mA$	1200			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 10mA$	2.0	2.8	4.0	V
		$V_{DS} = V_{GS}, I_D = 10mA, T_J = 175^\circ C$		1.8		V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 1200V, V_{GS} = 0V$		0.1	1.0	$\mu A$
	$I_{DSS}$	$V_{DS} = 1200V, V_{GS} = 0V, T_J = 175^\circ C$		1		$\mu A$
Gate Source Leakage Current	$I_{GSS+}$	$V_{GS} = 20V, V_{DS} = 0V$		10	100	nA
	$I_{GSS-}$	$V_{GS} = -5V, V_{DS} = 0V$		-10	-100	nA
Drain Source On-State Resistance	$R_{DS(on)}$	$V_{GS} = 20V, I_D = 20A$		77	100	m $\Omega$
		$V_{GS} = 20V, I_D = 20A, T_J = 175^\circ C$		137		m $\Omega$
Transconductance	gfs	$V_{DS} = 20V, I_D = 20A$		10.5		S
		$V_{DS} = 20V, I_D = 20A, T_J = 175^\circ C$		8		S
Input Capacitance	$C_{ISS}$	$V_{GS} = 0V,$		1324		pF
Output Capacitance	$C_{OSS}$	$V_{DS} = 1000V$		74		
Reverse Transfer Capacitance	$C_{RSS}$	$V_{AC} = 25mV$		3.4		
Coss Stored Energy	$E_{OSS}$	$f = 200kHz$		37		
Turn-On Switching Energy	$E_{ON}$	$V_{DS} = 800V, V_{GS} = -5/20V$		290		$\mu J$
Turn-Off Switching Energy	$E_{OFF}$	$I_D = 20A, R_{G(ext)} = 2.5\Omega$		20		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 800V, V_{GS} = -5/20V$		20		ns
Rise Time	$t_r$	$I_D = 20A, R_{G(ext)} = 2.5\Omega, L = 975\mu H$		11		
Turn-Off Delay Time	$t_{d(off)}$	FWD=S2M0080120K		20		
Fall Time	$t_f$			7.8		
Internal Gate Resistance	$R_{G(int)}$	$f = 1MHz, V_{AC} = 25mV, D-S$ short		3.3		$\Omega$
Gate to Source Charge	$Q_{gs}$	$V_{DS} = 800V, V_{GS} = -5/20V$		23		nC
Gate to Drain Charge	$Q_{gd}$	$I_D = 20A$		14		
Total Gate Charge	$Q_g$			54		

**Reverse Diode Characteristics:**

Characteristics	Symbol	Condition	Typ.	Max.	Units
Diode Forward Voltage	$V_{SD}$	$V_{GS} = -5V, I_{SD} = 10A$	4.0		V
	$V_{SD}$	$V_{GS} = -5V, I_{SD} = 10A, T_J = 175^\circ C$	3.5		V
Continuous Diode Forward Current	$I_S$	$V_{GS} = -5V, T_C = 25^\circ C$		41	A
Reverse Recovery Time	$t_{rr}$	$V_{GS} = -5V, I_{SD} = 20A, T_J = 25^\circ C$	25		ns
Reverse Recovery Charge	$Q_{rr}$		$V_R = 800V$	102	
Peak Reverse Recovery Current	$I_{mm}$	$diff/dt = 1950A/\mu s$	6.7		A

**Thermal-Mechanical Specifications:**

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	$T_J$	-	-55 to +175	$^\circ C$
Storage Temperature	$T_{stg}$	-	-55 to +175	$^\circ C$
Typical Thermal Resistance Junction to Case	$R_{\theta JC}$	DC operation	0.65	$^\circ C/W$

**Ordering Information:**

Device	Package	Shipping
S2M0080120T	TOLL	1800pcs/reel

**Marking Diagram**


Where XXXXX is YYWWL

S2M = Device Type  
 0080 =  $R_{DS(on)}$   
 120 = Reverse Voltage (1200V)  
 T = Package  
 SSG = SSG  
 YY = Year  
 WW = Week  
 L = Lot Number

**Cautions:** Molding resin  
 Epoxy resin UL:94V-0

**Ratings and Characteristics Curves**

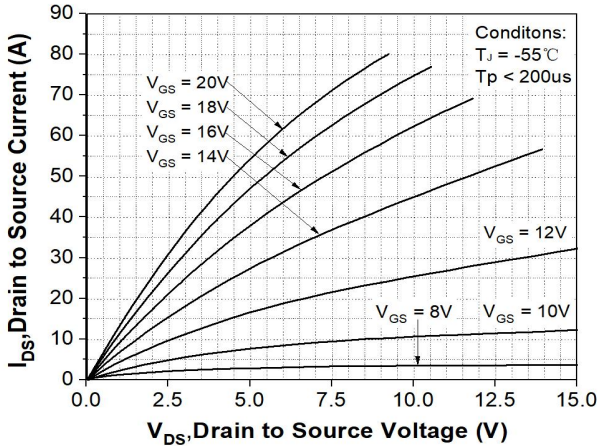


Figure 1. Output Characteristics  $T_J = -55^\circ\text{C}$

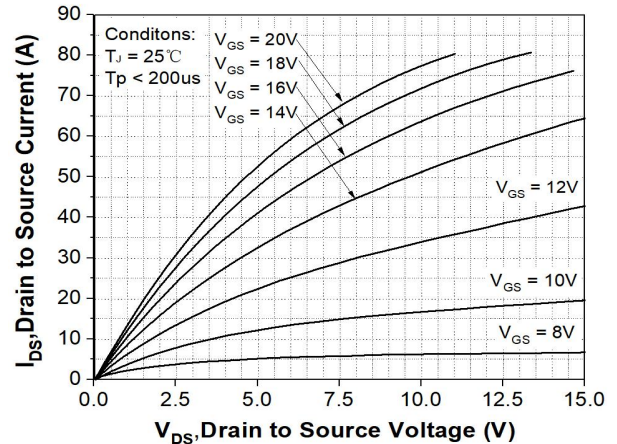


Figure 2. Output Characteristics  $T_J = 25^\circ\text{C}$

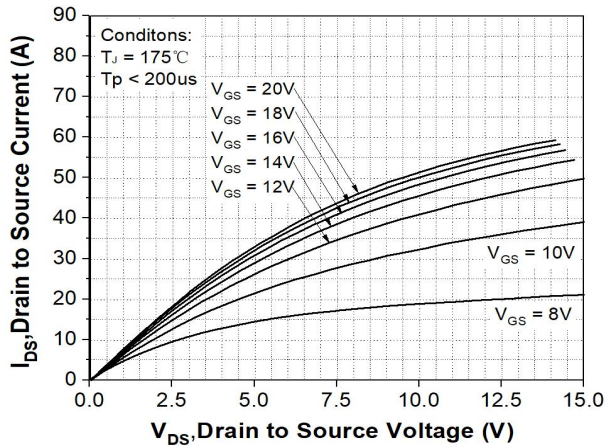


Figure 3. Output Characteristics  $T_J = 175^\circ\text{C}$

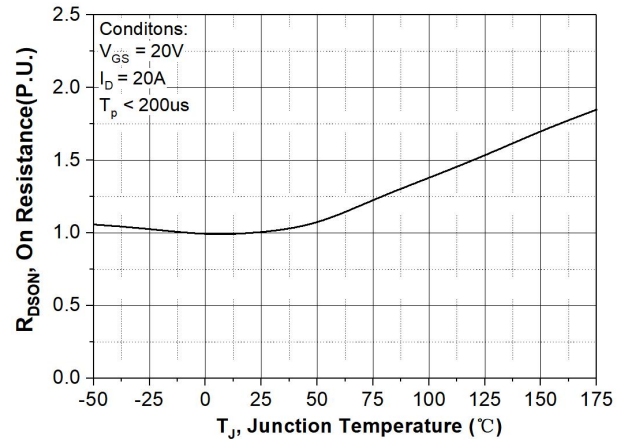


Figure 4. Normalized On-Resistance vs. Temperature

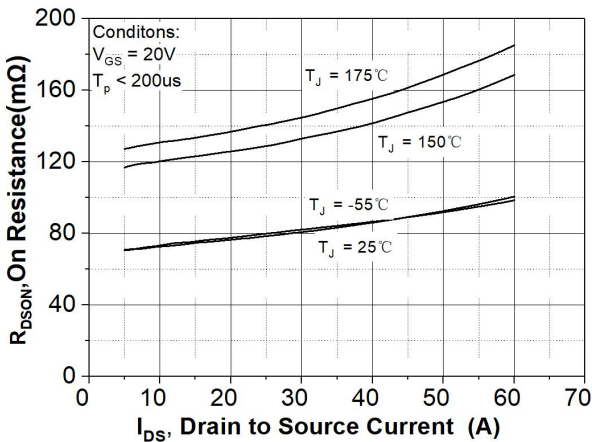


Figure 5. On-Resistance vs. Drain Current For Various Temperatures

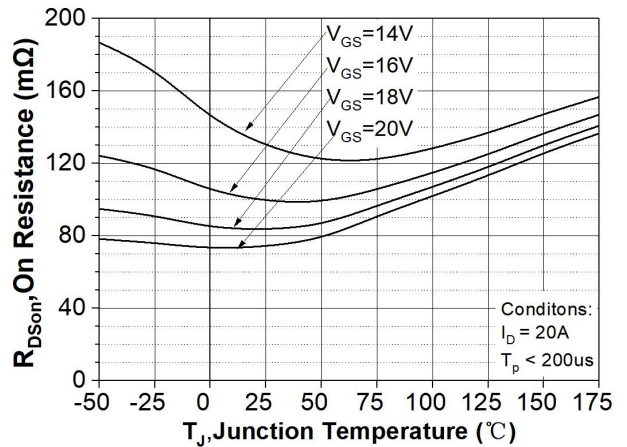


Figure 6. On-Resistance vs. Temperature For Various Gate Voltage

Technical Data  
Data Sheet N2750, REV.-

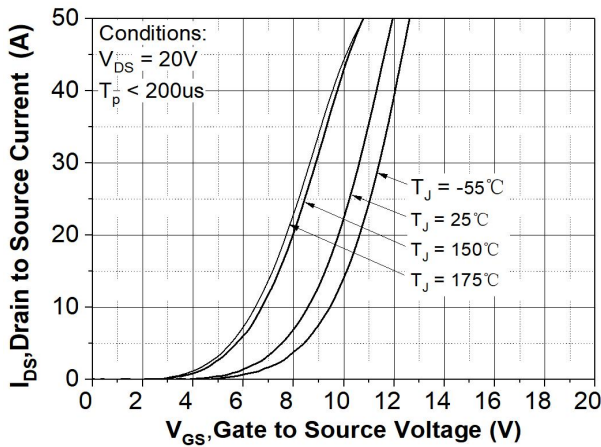


Figure 7. Transfer Characteristic for Various Junction Temperatures

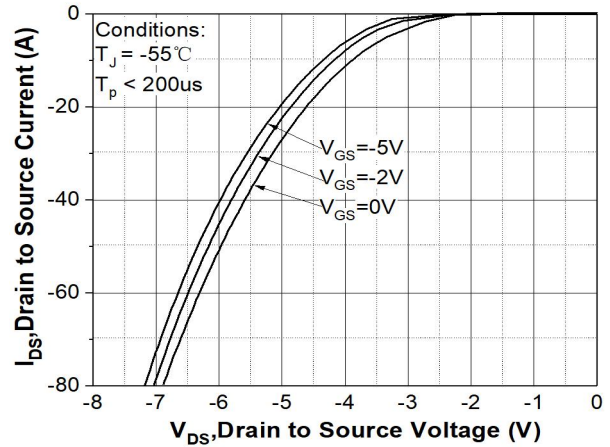


Figure 8. Body Diode Characteristic at  $T_J = -55^\circ\text{C}$

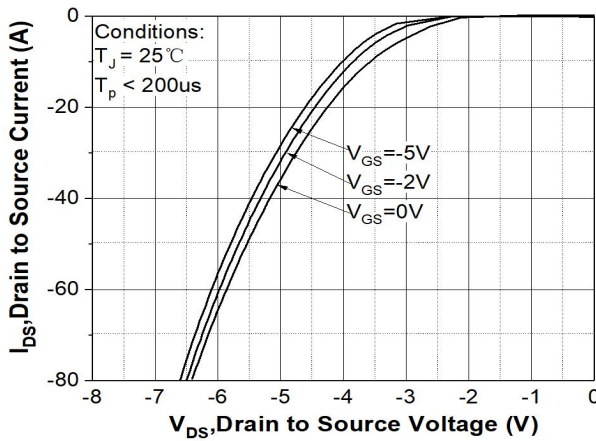


Figure 9. Body Diode Characteristic at  $T_J = 25^\circ\text{C}$

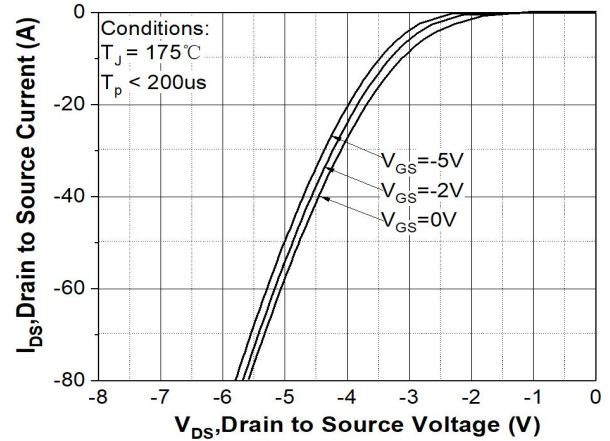


Figure 10. Body Diode Characteristic at  $T_J = 175^\circ\text{C}$

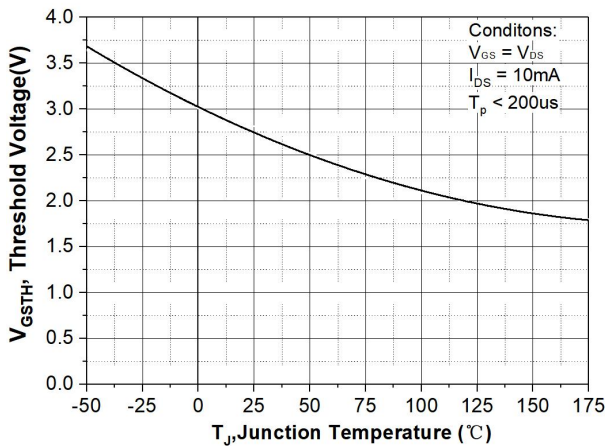


Figure 11. Threshold Voltage vs. Temperature

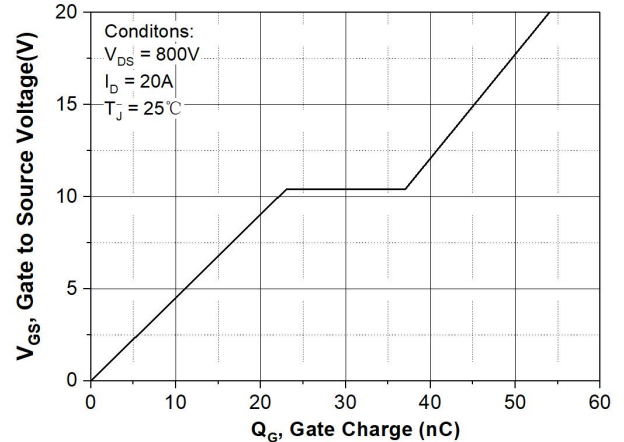


Figure 12. Gate Charge Characteristic



**Technical Data**  
Data Sheet N2750, REV.-

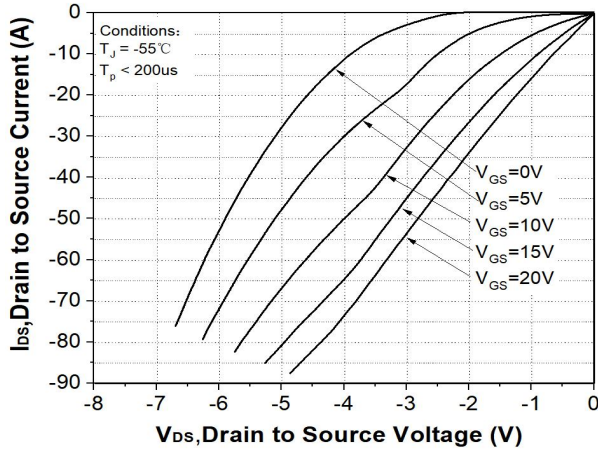


Figure 13. 3rd Quadrant Characteristic at  $T_J = -55^\circ\text{C}$

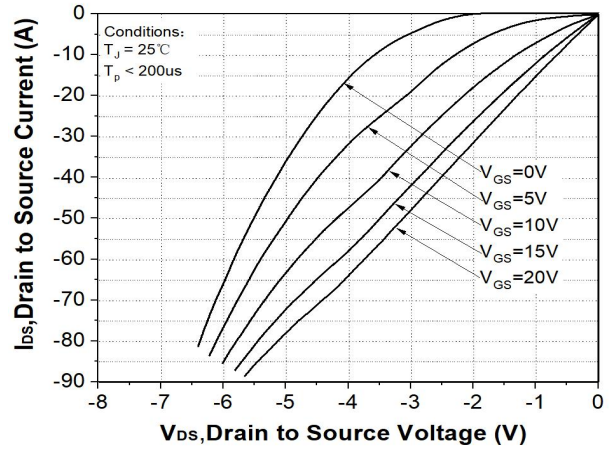


Figure 14. 3rd Quadrant Characteristic at  $T_J = 25^\circ\text{C}$

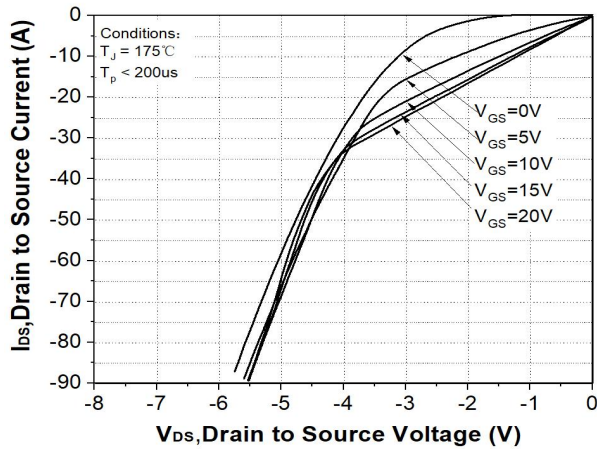


Figure 15. 3rd Quadrant Characteristic at  $T_J = 175^\circ\text{C}$

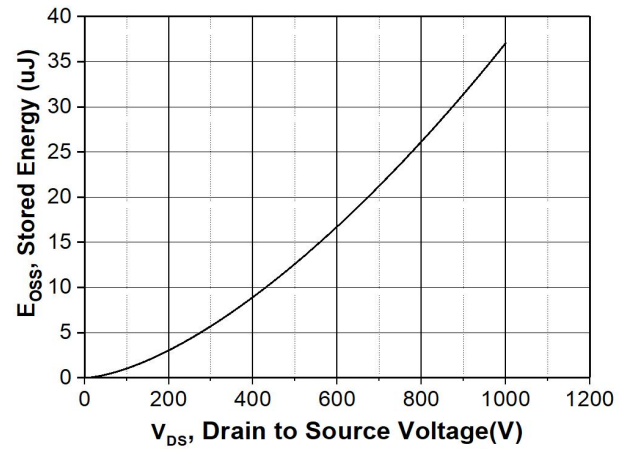


Figure 16. Output Capacitor Stored Energy

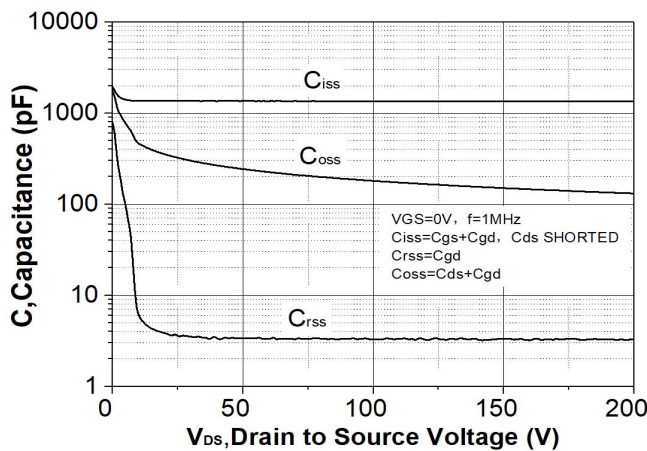


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200V)

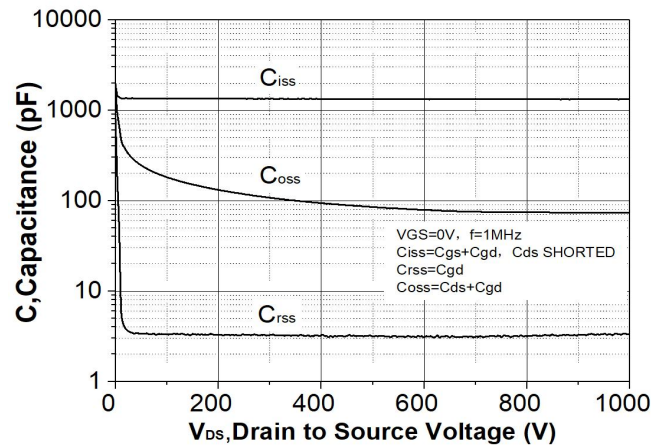


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1000V)

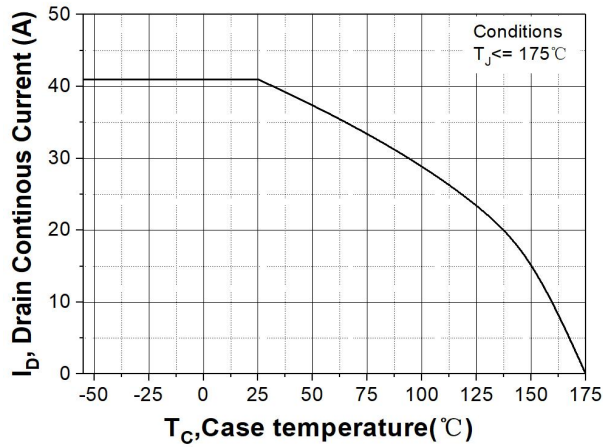


Figure 19. Continuous Drain Current Derating vs. Case Temperature

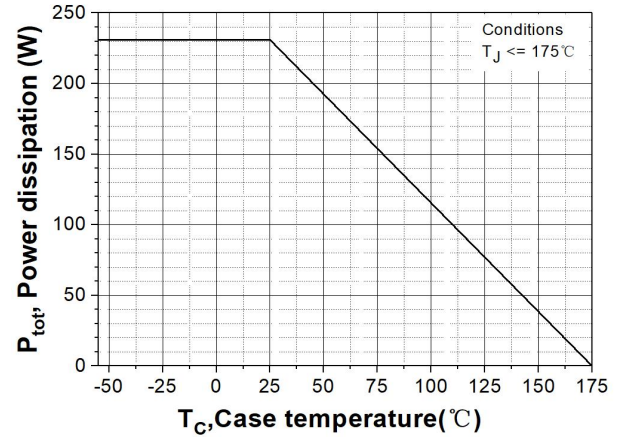


Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

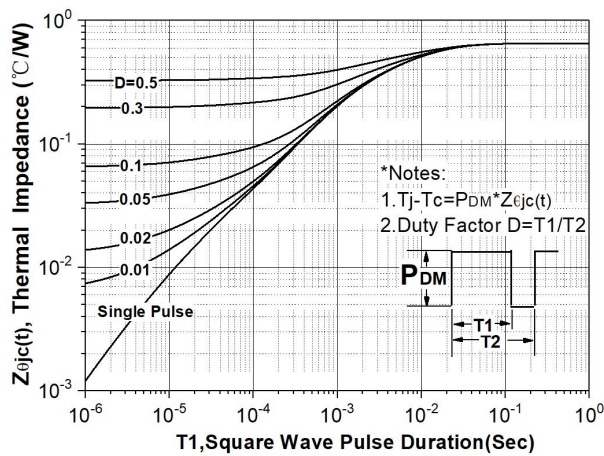


Figure 21. Transient Thermal Impedance (Junction - Case)

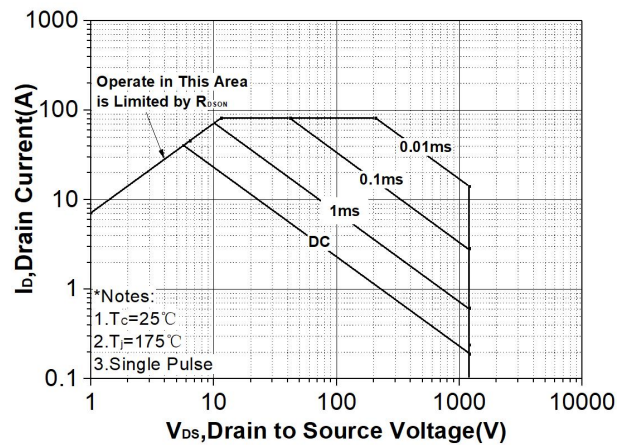


Figure 22. Safe Operating Area

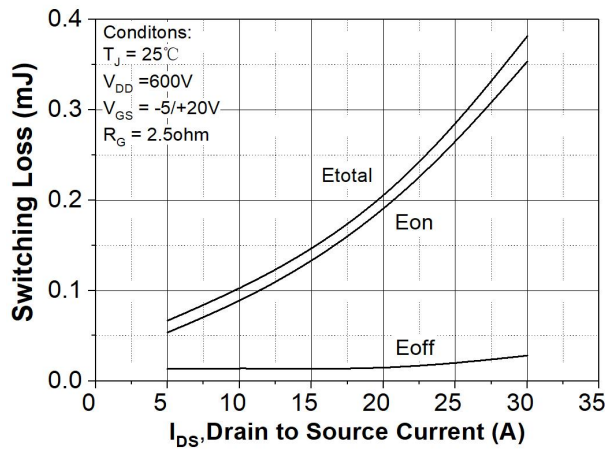


Figure 23. Clamped Inductive Switching Energy vs. Drain Current ( $V_{DD} = 600V$ )

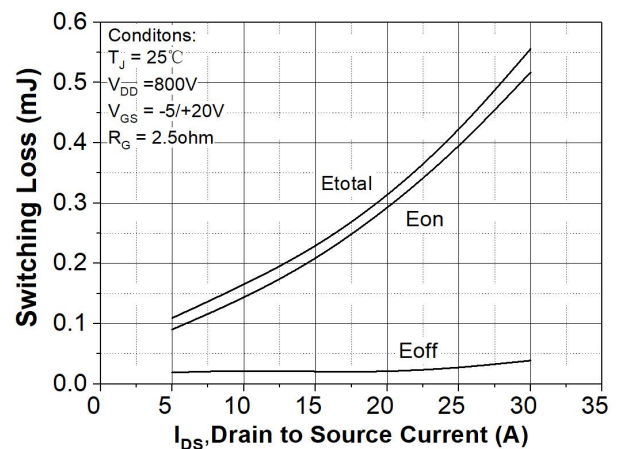


Figure 24. Clamped Inductive Switching Energy vs. Drain Current ( $V_{DD} = 800V$ )

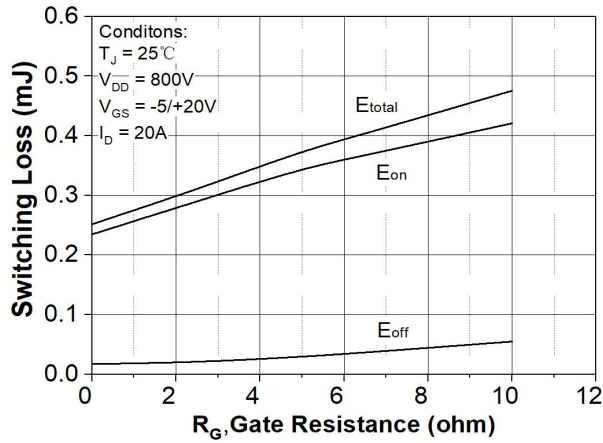


Figure 25. Clamped Inductive Switching Energy vs.  $R_{G(\text{ext})}$

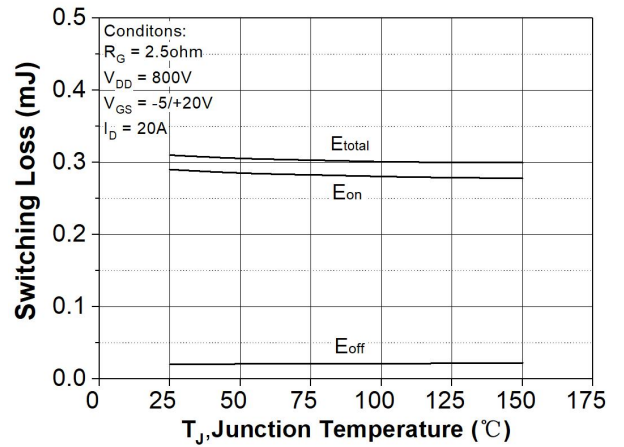


Figure 26. Clamped Inductive Switching Energy vs. Temperature

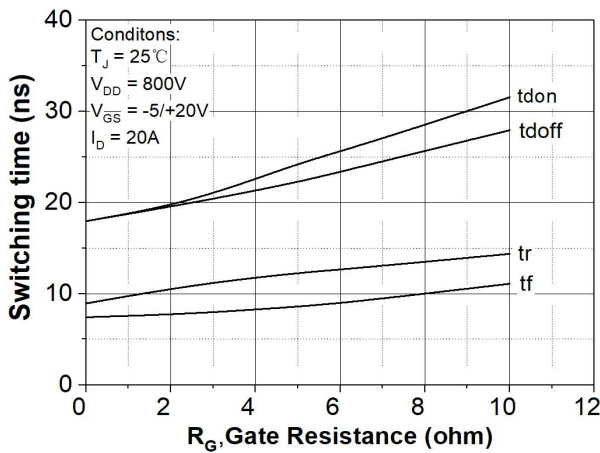


Figure 27. Switching Times vs.  $R_{G(\text{ext})}$

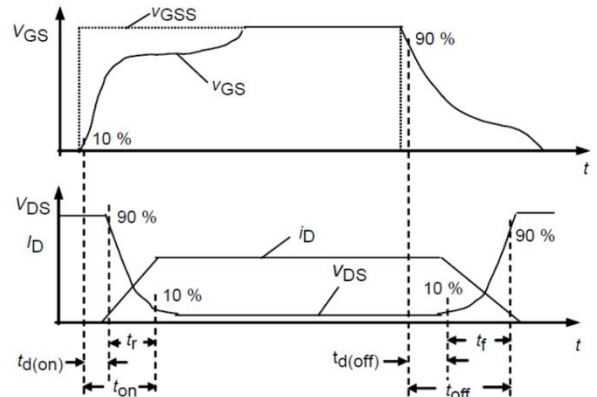
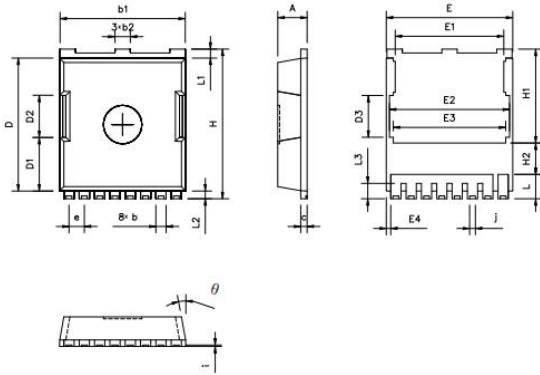


Figure 28. Switching Times Definition



**Mechanical Dimensions TOLL**



SYMBOL	Dimensions In Millimeters		
	Min.	NOM.	Max.
A	2.20	2.30	2.40
b	0.70	0.80	0.90
b1	9.70	9.80	9.90
b2	1.20 REF.		
c	0.40	0.50	0.60
D	10.28	10.38	10.48
D1	4.08	4.18	4.28
D2	3.20	3.30	3.40
D3	3.16	3.26	3.36
E	9.80	9.90	10.00
E1	8.40	8.50	8.60
E2	9.30	9.40	9.50
E3	8.8 REF.		
E4	0.25	0.35	0.45
e	1.20 BASIC		
H	11.58	11.68	11.78
H1	7.23	7.33	7.43
H2	2.45 REF.		
i	0.10	-	-
j	0.45 REF.		
L	1.60	1.90	2.10
L1	0.60	0.70	0.80
L2	0.50	0.60	0.70
L3	1.05	1.20	1.30
θ	10° REF.		

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