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MCCOG240160C6W-FPTLW	240 x 160		LCD Module	
Specification				
Version: 1		Date: 07/01/201	2	
	Re	vision		
1 05/01/2012	2			

Display F	- eatures		
Resolution	240 x 160		
Appearance	Black on White		
Logic Voltage	3.3V		1
Interface	Parallel		), HC
Font Set	N/A		oHS ompliant
Display Mode	Transflective	CC	ompliant
LC Type	FSTN		
Module Size	93.00 x 64.20 x 9.80 <sup>(MAX)</sup> mm		
Operating Temperature	-20°C ~ +70°C		
Construction	COG	Box Quantity	Weight / Display
LED Backlight	White		

### **DESIGN • MANUFACTURE • SUPPLY**

Display Accessories				
Part Number	Description			

Optional Variants			
Appearances	Voltage		
White on Blue			
Black on Yellow/Green			
Black on RGB			

## **General Specification**

The Features of the Module is description as follow:

■ Module dimension: 93.0 x 64.2 x 9.8(MAX) mm

View area: 78.5 x 47.5 mm2

Active area: 69.58 x 43.98 mm2

■ Number of Characters: 240 x 160 dots

■ Dot size: 0.27 x 0.255 mm2

■ Dot pitch: 0.29 x 0.275 mm2

■ LCD type: FSTN Positive Transflective

■ Duty: 1/160DUTY,1/12BIAS

■ View direction: 6 o'clock

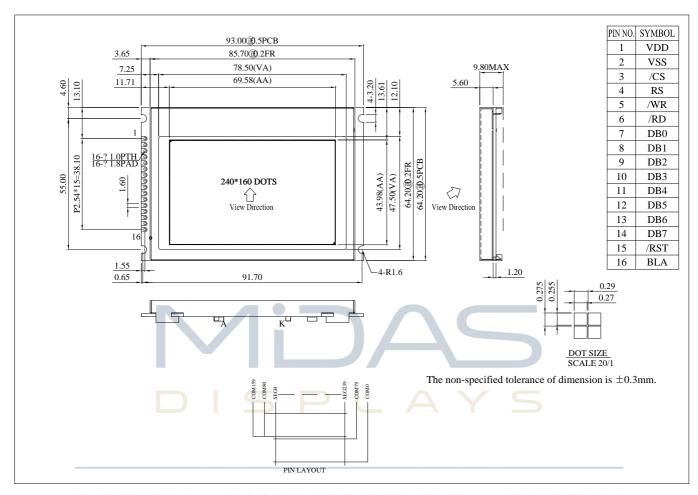
■ Backlight Type: LED, White

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## **Interface Pin Function**

Pin No.	Symbol	Level	Description
1	VDD		Power supply (+3.3V)
2	VSS		Power supply
3	/CS	L	Chip enable active " L "
4	RS	H/L	H : Instruction , L : Data
5	WR	L	Write enable input,active Low
6	/RD	L	Read enable input,active Low
7	DB0	H/L	Data bus line
8	DB1	H/L	Data bus line
9	DB2	H/L	Data bus line
10	DB3	H/L	Data bus line
11	DB4	H/L	Data bus line
12	DB5	H/L	Data bus line
13	DB6	H/L	Data bus line
14	DB7	H/L	Data bus line
15 _	/RST		Reset input pin
n	nesia		/RST =Low, initialization is executed /RST =High, Normal
16	BLA	Power	Backlight Positive Power Supply

### **Outline Dimension**



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# **Absolute Maximum Ratings**

Item	Symbol	Min	Тур	Max	Unit
Operating Temperature	T <sub>OP</sub>	-20		+70	$^{\circ}\!\mathbb{C}$
Storage Temperature	T <sub>ST</sub>	-30	_	+80	$^{\circ}\!\mathbb{C}$
Supply voltage for Logic	$V_{DD-}V_{SS}$	-0.5	_	4.0	V
Output voltage	Vo	-0.5		V <sub>DD</sub> +0.5	V

## **Electrical Characteristics**

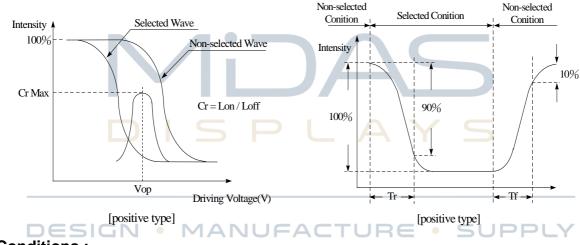
Item	Symbol	Condition	Min	Тур	Max	Unit
Supply Voltage For Logic	V <sub>DD</sub> -V <sub>SS</sub>	7	3.0	3.3	3.6	V
		Ta=-20°C	_	_	_	V
Supply Voltage For LCD	V <sub>0</sub> -V <sub>SS</sub>	Ta=25°C	A	14.5	5 -	V
		Ta=70°C	_		_	V
Input High Volt.	V <sub>IH</sub>	_	$0.7 V_{DD}$	_	$V_{DD}$	V
Input Low Volt.	· MANU	JFĀCI	USS E	• 5	0.3V <sub>DD</sub>	V
Output High Volt.	V <sub>OH</sub>	_	V <sub>DD</sub> -0.4	_	_	V
Output Low Volt.	V <sub>OL</sub>	_	V <sub>SS</sub>	_	0.4	V
Supply Current(No include LED Backlight)	I <sub>DD</sub>	V <sub>DD</sub> =3.3V	_	4.8	_	mA

## **Optical Characteristics**

Item	Symbol	Condition	Min	Тур	Max	Unit
View Angle	(V)θ	CR≧2	30	_	60	deg
View / trigic	(Η)φ	CR≧2	-45	_	45	deg
Contrast Ratio	CR	_		5		
Response Time	T rise	_	_	200	300	ms
	T fall	_	_	200	300	ms

### **Definition of Operation Voltage (Vop)**

#### **Definition of Response Time (Tr, Tf)**



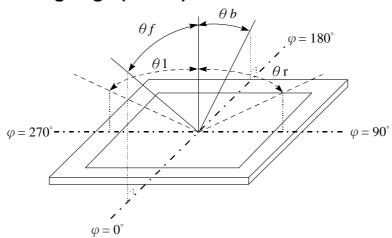
#### **Conditions:**

Operating Voltage: Vop

Viewing Angle( $\theta$ ,  $\phi$ ):  $0^{\circ}$ ,  $0^{\circ}$ 

Frame Frequency: 64 HZ Driving Waveform: 1/N duty, 1/a bias

### **Definition of viewing angle(CR≥2)**



## **Timing Characteristic**

#### PLEASE TO CONSULT ST7529 SPEC

## **Backlight Information**

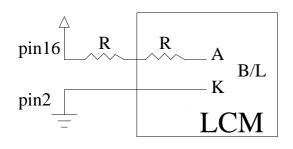
### **Specification**

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Supply Current	ILED	76.8	96.0	120	mA	V=3.5V
Supply Voltage	V	3.4	3.5	3.6	V	
Reverse Voltage	VR		_	5	V	
Luminous						
Intensity (Without LCD)	IV	640.0	800.0	/	CD/M <sup>2</sup>	ILED=96mA
LED Life Time	_	_	50K		Hr.	ILED≦96mA
Color	White	4.0.0.1				

Note: The LED of B/L is drive by current only; driving voltage is only for reference To make driving current in safety area (waste current between minimum and maximum).

Note1:50K hours is only an estimate for reference.

Drive from pin16,pin2



# Reliability

### Content of Reliability Test (wide temperature, -20°C~70°C)

	Environmental Test						
Test Item	Content of Test	Condition	Note				
High Temperature storage	Endurance test applying the high storage temperature for a long time.	200hrs	2				
Low Temperature storage	Endurance test applying the high storage temperature for a long time.	200hrs	1,2				
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	200hrs	_				
Low Temperature Operation	temperature for a long time.	-20℃ 200hrs	1				
High Temperature/ Humidity Operation	The module should be allowed to stand at 60°C,90%RH max  For 96hrs under no-load condition excluding the polarizer,  Then taking it out and drying it at normal temperature.	60℃,90%RH 96hrs	1,2				
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation  -20°C 25°C 70°C  30min 5min 30min 1 cycle	-20℃/70℃ 10 cycles	-				
DESIGI	• MANUFACTURE • S	fixed amplitude: 15mm Vibration. Frequency:	Y				
Vibration test	Endurance test applying the vibration during transportation and using.	10~55Hz. One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3				
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS= 1.5kΩ CS=100pF 1 time					

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal temperature and humidity after remove from the test chamber.

Note3: Vibration test will be conducted to the product itself without putting it in a container.

# **Inspection specification**

NO	Item			Criterion		AQL
01	Electrical Testing	<ul> <li>1.1 Missing vertical, horizontal segment, segment contrast defect.</li> <li>1.2 Missing character, dot or icon.</li> <li>1.3 Display malfunction.</li> <li>1.4 No function or no display.</li> <li>1.5 Current consumption exceeds product specifications.</li> <li>1.6 LCD viewing angle defect.</li> <li>1.7 Mixed product types.</li> <li>1.8 Contrast defect.</li> </ul>				
02	Black or white spots on LCD (display only)	<ul> <li>2.1 White and black spots on display ≤0.25mm, no more than three white or black spots present.</li> <li>2.2 Densely spaced: No more than two spots or lines within 3mm</li> </ul>				
03	LCD black spots, white spots, contaminatio	3.1 Round type Φ=( x + y ) /	P [	wing drawing  A  TURE	SUPPLY	2.5
	n (non-display)	3.2 Line type :	(As follow Length	ring drawing) Width	Acceptable Q TY	
		→ L +-		W≦0.02	Accept no dense	2.5
			L≦3.0 L≦2.5	0.02 <w≦0.03 0.03<w≦0.05< td=""><td>2</td><td></td></w≦0.05<></w≦0.03 	2	
				0.05 <w< td=""><td>As round type</td><td></td></w<>	As round type	
04	Polarizer bubbles	If bubbles are valudge using blasspecifications, leasy to find, mucheck in specification.	nck spot not ust	Size Φ $ Φ \le 0.20 $ $ 0.20 < Φ \le 0.50 $ $ 0.50 < Φ \le 1.00 $ $ 1.00 < Φ $ Total Q TY	Acceptable Q TY Accept no dense 3 2 0 3	2.5

NO	Item	Criterion	AQL
05	Scratches	Follow NO.3 LCD black spots, white spots, contamination	
06	Chipped glass	Symbols Define: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length: 6.1 General glass chip: 6.1.1 Chip on panel surface and crack between panels:  Z≤1/2t Not over viewing area 1/2t <z≤2t 1="" 2="" 3k="" 6.1.2="" 8a="" are="" chip.="" chips,="" corner="" crack:<="" each="" exceed="" if="" is="" length="" more="" not="" of="" or="" td="" there="" total="" x="" x≤1=""><td>2.5</td></z≤2t>	2.5
		$ \begin{array}{ c c c c c c c c c c c c c c c c c c c$	
		1/2t $<$ z $\le$ 2t  Not exceed 1/3k  x $\le$ 1/8a	
		⊙ If there are 2 or more chips, x is the total length of each chip.	
<u> </u>	<u> </u>	ı	1

k: Seal width t: 0 L: Electrode pad length	Glass thickness a: LCD side length ninal:		
6.2.1 Chip on electrode  y: Chip width y≤0.5mm	x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length 6.2 Protrusion over terminal : 6.2.1 Chip on electrode pad :  y: Chip width x: Chip length z: Chip thickness		
ITO must remai terminal specific ⊙ If the product w alignment mark	x: Chip length $x = 1/8a$ $x \le 1/8a$		

NO	Item	Criterion	AQL		
07	Cracked glass	The LCD with extensive crack is not acceptable.			
08	Backlight elements	<ul> <li>8.1 Illumination source flickers when lit.</li> <li>8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards.</li> <li>8.3 Backlight doesn't light or color wrong.</li> </ul>	0.65 2.5 0.65		
09	Bezel	<ul><li>9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.</li><li>9.2 Bezel must comply with job specifications.</li></ul>			
10	PCB · COB	<ul> <li>10.1 COB seal may not have pinholes larger than 0.2mm or contamination.</li> <li>10.2 COB seal surface may not have pinholes through to the IC.</li> <li>10.3 The height of the COB should not exceed the height indicated in the assembly diagram.</li> <li>10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places.</li> <li>10.5 No oxidation or contamination PCB terminals.</li> <li>10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts.</li> <li>10.7 The jumper on the PCB should conform to the product characteristic chart.</li> <li>10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down.</li> <li>10.9 The Scraping testing standard for Copper Coating of PCB</li> <li>X * Y&lt;=2mm²</li> </ul>	2.5 2.5 0.65 2.5 2.5 0.65 2.5 2.5		
11	Soldering	<ul> <li>11.1 No un-melted solder paste may be present on the PCB.</li> <li>11.2 No cold solder joints, missing solder connections, oxidation or icicle.</li> <li>11.3 No residue or solder balls on PCB.</li> <li>11.4 No short circuits in components on PCB.</li> </ul>	2.5 2.5 2.5 0.65		

NO	Item	Criterion		
12	General appearance	<ul> <li>12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.</li> <li>12.2 No cracks on interface pin (OLB) of TCP.</li> <li>12.3 No contamination, solder residue or solder balls on product.</li> <li>12.4 The IC on the TCP may not be damaged, circuits.</li> <li>12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it causes the interface pin to sever.</li> <li>12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.</li> <li>12.7 Sealant on top of the ITO circuit has not hardened.</li> <li>12.8 Pin type must match type in specification sheet.</li> <li>12.9 LCD pin loose or missing pins.</li> <li>12.10 Product packaging must the same as specified on packaging specification sheet.</li> <li>12.11 Product dimension and structure must conform to product specification sheet.</li> </ul>	2.5 0.65 2.5 2.5 2.5 2.5 0.65 0.65 0.65 0.65	

## DISPLAYS

### **Precautions in use of LCD Modules**

### **DESIGN • MANUFACTURE • SUPPLY**

- 1. Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- 2. Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- 3. Don't disassemble the LCM.
- 4. Don't operate it above the absolute maximum rating.
- 5. Don't drop, bend or twist LCM.
- 6. Soldering: only to the I/O terminals.
- 7. Storage: please storage in anti-static electricity container and clean environment.
- 8. Midas have the right to change the passive components (Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.)
- 9. Midas have the right to change the PCB Rev.

### **Material List of Components for RoHs**

1. MIDAS Components Ltd. hereby declares that all of or part of products, including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

Exhibit A: The Harmful Material List

Material	(Cd)	(Pb)	(Hg)	(Cr6+)	PBBs	PBDEs	
Limited Value	100 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	
Above limited value is set up according to RoHS.							

- 2. Process for RoHS requirement:
  - (1) Use the Sn/Ag/Cu soldering surface; the surface of Pb-free solder is rougher than we used before.
  - (2) Heat-resistance temp. :

Reflow: 250°C, 30 seconds Max.;

Connector soldering wave or hand soldering : 320°C, 10 seconds max.

(3) Temp. curve of reflow, max. Temp. : 235±5°€;

Recommended customer's soldering temp. of connector : 280°C, 3 seconds.

### Recommendable storage

- 1. Place the panel or module in the temperature 25°C±5℃ and the humidity below 65% RH
- 2. Do not place the module near organics solvents or corrosive gases.
- 3. Do not crush, shake, or jolt the module