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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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CR05AM-16

Thyristor

Low Power Use

REJ03G0356-0100 Rev.1.00 Aug.20.2004

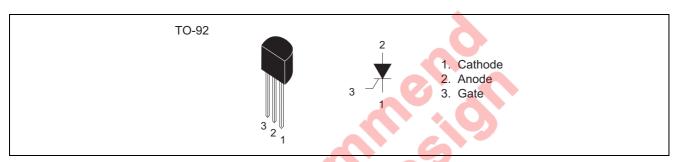
Features

 $\begin{array}{ll} \bullet & I_{T \, (AV)} : 0.3 \; A \\ \bullet & V_{DRM} : 800 \; V \\ \bullet & I_{GT} : 100 \; \mu A \end{array}$

Non-Insulated Type

Glass Passivation Type

Outline



Applications

Leakage protector, timer, and gas igniter

Maximum Ratings

Parameter	Symbol	Voltage class	Unit
Farameter	Symbol	16	Onit
Repetitive peak reverse voltage	V_{RRM}	800	V
Non-repetitive peak reverse voltage	V_{RSM}	960	V
DC reverse voltage	$V_{R(DC)}$	640	V
Repetitive peak off-state voltage ^{Note1}	V_{DRM}	800	V
Non-repetitive peak off-state voltage ^{Note1}	V _{DSM}	960	V
DC off-state voltage ^{Note1}	$V_{D(DC)}$	640	V

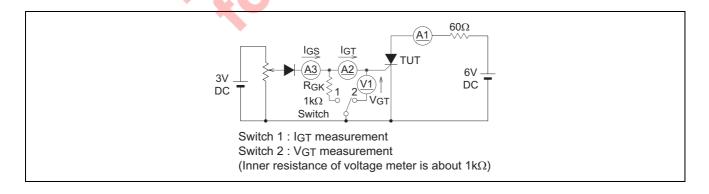
Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T (RMS)}	0.47	Α	
Average on-state current	I _{T (AV)}	0.3	А	Commercial frequency, sine half wave 180° conduction, Ta = 47°C
Surge on-state current	I _{TSM}	10	А	60Hz sine half wave 1 full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	0.4	A ² s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	P _{GM}	0.5	W	
Average gate power dissipation	P _{G (AV)}	0.1	W	
Peak gate forward voltage	V_{FGM}	6	V	
Peak gate reverse voltage	V_{RGM}	6	V	
Peak gate forward current	I_{FGM}	0.3	Α	
Junction temperature	Tj	- 40 to +110	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	_	0.23	g	Typical value

Notes: 1. With gate to cathode resistance $R_{GK} = 1 \text{ k}\Omega$.

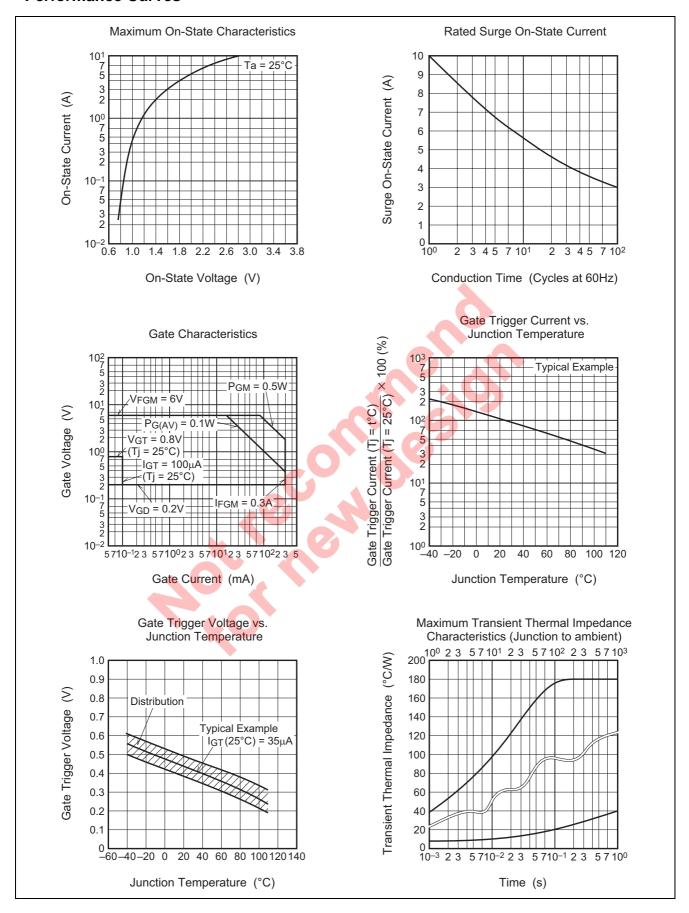
Electrical Characteristics

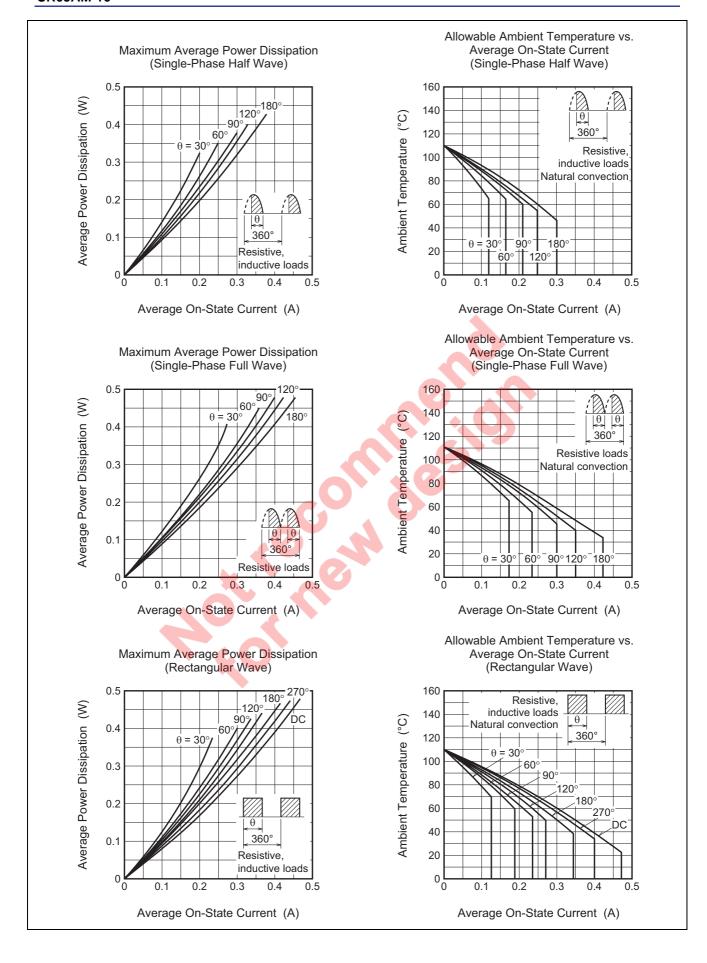
Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak reverse current	I _{RRM}	_	_	0.1	mA	Tj = 110°C, V _{RRM} applied
Repetitive peak off-state current	I _{DRM}	_	-	0.1	mA	$T_j = 110$ °C, V_{DRM} applied, $R_{GK} = 1 kΩ$
On-state voltage	V _{TM}	1		1.8	V	Ta = 25°C, I _{TM} = 4 A, instantaneous value
Gate trigger voltage	V _{GT}			0.8	V	$Tj = 25$ °C, $V_D = 6$ V, $I_T = 0.1$ A ^{Note2}
Gate non-trigger voltage	V _{GD}	0.2	Ī	<u> </u>	V	$Tj = 110^{\circ}C, V_D = 1/2 V_{DRM},$ $R_{GK} = 1 k\Omega$
Gate trigger current	I _{GT}	1	1	100	μΑ	$Tj = 25$ °C, $V_D = 6$ V, $I_T = 0.1$ A ^{Note2}
Holding current	lμ	7	1.5	3	mA	$Tj = 25$ °C, $V_D = 12$ V, $R_{GK} = 1$ k Ω
Thermal resistance	R _{th (j-a)}		_	180	°C/W	Junction to ambient

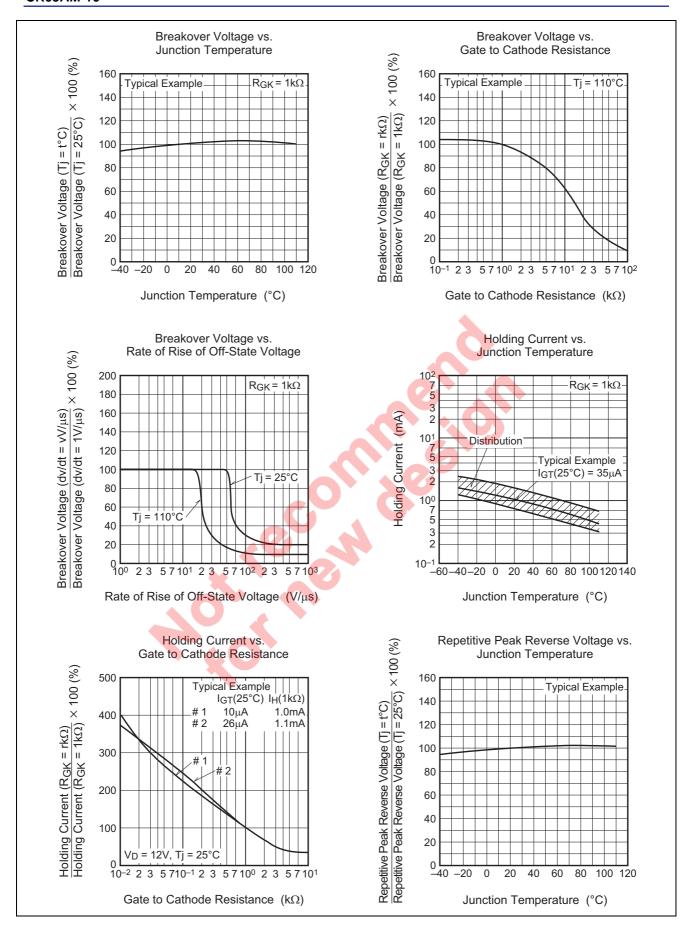
Notes: 2. I_{GT}, V_{GT} measurement circuit.

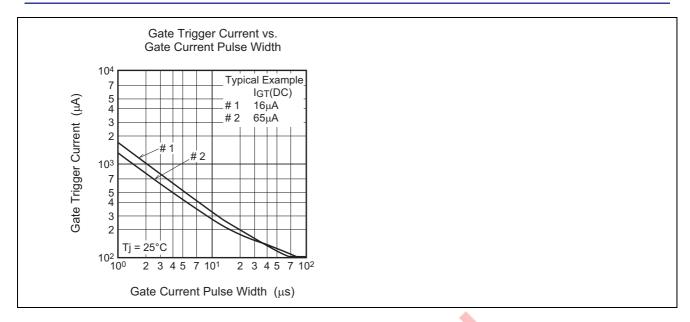


Performance Curves



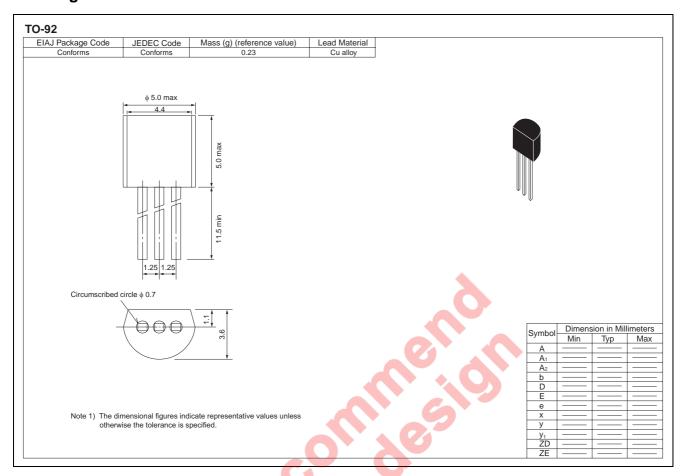








Package Dimensions



Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Straight type	Vinyl sack	500	Type name	CR05AM-16
Lead form	Vinyl sack	500	Type name – Lead forming code	CR05AM-16-A6
Form A8	Taping	2000	Type name – TB	CR05AM-16-TB

Note: Please confirm the specification about the shipping in detail.

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