

## **Customer Information Notification**

2023100111: Errata update for LPC54018JxM, LPC54S018JxM, LPC540xx, LPC54S0xx, LPC5410x, LPC5411x and LPC546xx

**Note:** This notice is NXP Company Proprietary.

Issue Date: Feb 03, 2024 Effective date: Feb 04, 2024

**Management summary** 

Errata update for LPC54018JxM, LPC54S018JxM, LPC540xx, LPC54S0xx, LPC5410x, LPC5411x and LPC546xx

### **Change Category**

[]Wafer Fab Process	[]Assembly Process	[]Product Marking	[]Test Process	[]Design
[ ]Wafer Fab Materials	[ ]Assembly Materials	[]Mechanical Specification	[]Test Equipment	[X]Errata
[ ]Wafer Fab Location	[]Assembly Location	[]Packing/Shipping/Labeling	[]Test Location	[]Electrical spec./Test coverage
[]Firmware	[]Other			

## **PCN Overview**

## **Description**

NXP Semiconductors announces that the errata for LPC54018JxM, LPC54S018JxM, LPC540xx, LPC54S0xx, LPC5410x, LPC5411x and LPC546xx have been updated.

Changes in the new revision:

Added a section for PLL - P-divider set to 4 could generate the wrong output frequency from the PLL.

The updated documents can be found at:

https://www.nxp.com/docs/en/errata/ES LPC540XX LPC54S0XX.pdf

https://www.nxp.com/docs/en/errata/LPC54018JxM\_LPC54S018JxM.pdf

https://www.nxp.com/docs/en/errata/ES LPC5410X.pdf

https://www.nxp.com/docs/en/errata/ES LPC5411X.pdf

https://www.nxp.com/docs/en/errata/ES LPC546XX.pdf

#### Reason

One P-divider value in the LPC54xxx PLL can sometimes cause the P-divider to jump into the wrong division state during startup of the PLL, the issue affects both the System and Audio PLL. A work-around is provided in the errata.

# Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

### Additional information

Additional documents: view online

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For specific questions on this notice or the products affected please contact our specialist directly:

Name NXP Technical support

e-mail address tech.support@nxp.com

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NXP Quality Management Team.

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