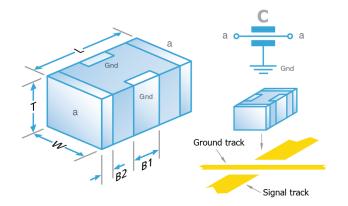


# **Multilayer Ceramic Chip Capacitor**

**Part Number:** 1206J0500223MXTE07

**Description:** 1206 50Vdc 22nF ±20% X7R (2R1)

A range of ceramic MLCC feedthrough 'C' filters with enhanced current carrying capabilities. Internal electrodes conduct signals through the MLCC body, with the capacitance formed to ground pads on the side of the chip, providing low inductance and high performance. Available with a variety of termination options including FlexiCap<sup>TM</sup> (on X7R), the world's first commercially available flexible termination.



# **Mechanical Specification**

Size Code

Length (L1) in mm (")

Width (W) in mm (")

Thickness (T) in mm (")

Termination Bands (B2) in mm (")
Center (Ground) Band (B1) in mm (")

**Termination Material** 

Solderability Packaging 1206

 $3.2 \pm 0.30 \ (0.126 \pm 0.012)$ 

 $1.6 \pm 0.20 \ (0.063 \pm 0.008)$ 

1.1 ± 0.2 (0.043 ± 0.008)

 $0.50 \pm 0.25 \ (0.02 \pm 0.01)$ 

 $0.95 \pm 0.30 \ (0.037 \pm 0.012)$ 

Nickel Barrier, Sn Plated Solder (RoHS compliant)

IEC-60068-2-58

7" Reel Horizontal Orientation, 2500 per reel

# **General Electrical Specification**

Rated Voltage

Rated DC Current

DC Resistance

Nominal Capacitance Value

Capacitance Tolerance

Tangent of Loss Angle (Tan  $\delta$ )

Capacitance and Tan  $\delta$  Test Conditions

Voltage Proof

(Voltage applied for 5 secs max. @ 50mA max. charge current)

Min Insulation Resistance (IR)

Dielectric Classification

Rated Temperature Range

Maximum Capacitance Change over Temperature Range

Climatic Category (IEC)

Ageing Characteristic

50Vdc

2A

 $0.06\Omega$ 

22nF

±20%

≤0.025

1.0Vrms @ 1kHz

125Vdc

45.45GOhm @ 50Vdc

X7R (2R1)

-55°C / +125°C

No DC Voltage ±15%

Rated DC Voltage -

55/125/56

<2% per decade (nominal capacitance is 1000 hour value)

#### **Knowles Precision Devices - Sales**

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www.knowlescapacitors.com

This datasheet is for a standard item and is confirmed valid on the date generated, the latest published data for this part may differ and is available at http://www.knowlescapacitors.com or by contacting us.

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Data is correct to the best of our knowledge, errors and

omissions excepted.

Date: Tuesday, January 16, 2024

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# **Multilayer Ceramic Chip Capacitor**

#### **Environmental**

RoHS Compliant to 2011/65/EC as amended by 2015/863/EU

Compliant

**REACH Compliant** 

235 compliant

California Proposition 65

No exposure risk

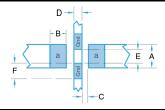
### **Board Layout**

Knowles' conventional 3-terminal chip capacitors should be mounted using the pad design supplied.

It has been developed in conjunction with our customers over the years and has been shown to yield successful soldering results. It incorporates factors that have been shown to reduce mechanical stress, such as reducing the pad width to less than the chip width, but the position of the chip on the board should also be considered.

Note that for optimum noise rejection the ground pads should be placed on the circuit board ground plane, or connected to the ground plane by the shortest and widest route possible.

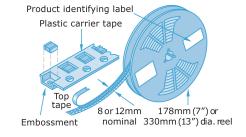
	1206	
Α	1.20mm	0.047"
В	0.90mm	0.035"
С	0.60mm	0.024"
D	0.80mm	0.031"
Е	1.00mm	0.039"
F	0.70mm	0.028"



## **Packaging**

Tape packaging information for tape-and-reel parts:

Tape and reel packing of surface mounting chip capacitors for automatic placement are in accordance with IEC60286-3.



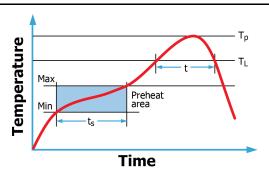
## Soldering

Reflow solder in accordance with IPC-A-610. Recommended reflow profile as laid down in IPC/JEDEC J-STD-020.

Wave soldering is also possible, but care must be taken for case sizes 1210 and larger and component thickness >1.0mm. Trials are encouraged.

DLI

Hand soldering is not recommended and can lead to component damage through thermal shock.



Application notes with mounting and handling guidance are available on request.

### Knowles Precision Devices - Sales

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Compex

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Johanson MFG

Novacap

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