650-V Direct WBG Diode

Key Features:

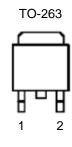
- SiC performance
- Easy paralleling
- · High current carrying capability
- · Very low junction capacitance
- Highly stable V_F and Q_{RR} at elevated temperatures

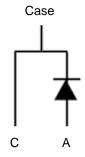
Typical Applications:

- Soft switching topologies
- · Secondary side rectification

PRODUCT SUMMARY					
V _{BR} (V)	$V_F(V)$	IF(AV) (A)			
650	1.8	10			







ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)						
Parameter			Limit	Units		
Cathode-Anode Voltage		V_{BR}	650	V		
Diode Forward Current a	T _C =25°C	I _{F(AV)}	10	Α		
Single Pulse Forward Current ^b	T _C =25°C	I _{FSM}	50	Α		
Joule Integral		i ² t	12	A²·s		
Power Dissipation ^a	T _C =25°C	P_D	37	W		
Storage Temperature Range		T _{stg}	-55 to 175	°C		
Operating Junction Temperature		T_J	-40 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Maximum	Units			
Maximum Junction-to-Ambient °	$R_{\theta JA}$	40	°C/W			
Maximum Junction-to-Case	$R_{ heta JC}$	3.7	C/VV			

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Notes

- a. Package Limited
- b. Pulse width limited by maximum junction temperature
- c. Surface Mounted on 1" x 1" FR4 Board.

Electrical Characteristics

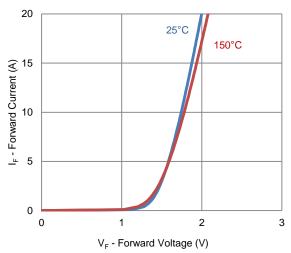
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit			
Static									
Famurand Valtage 8	V_{F}	I _F = 10 A		1.8		V			
Forward Voltage ^a	V _F	$I_F = 10 \text{ A}, T_J = 150^{\circ}\text{C}$		1.84					
Repetitive Peak Reverse Voltage	V_{RRM}	$T_J = -40$ °C to 150°C	650			V			
Junction Capacitance	CJ	$V_R = 200 \text{ V}, V_{\text{sine}} = 0.6 \text{ V}_{\text{eff}},$ f = 100 kHz		6.3		pF			
Dougrap Laglage Current	1	V _R = 650 V			2	uA			
Reverse Leakage Current	I _R	V _R = 650 V, T _J = 150°C			10	uA			
Dynamic ^b									
Reverse Recovery Time	T _{rr}	L = 10 A dl/dt = 100 A/uc		76		ns			
Reverse Recovery Charge	Q_{rr}	$I_F = 10 \text{ A}, \text{ dI/dt} = 100 \text{ A/us},$ $V_R = 400 \text{ V}, T_{\text{LI}} = 25^{\circ}\text{C}$		149		nC			
Peak Recovery Current	I _{RRM}	VR = 400V, 13 = 23 O		3.3		Α			
Reverse Recovery Time	T _{rr}	L = 10 A dl/dt = 100 A/uc		71		ns			
Reverse Recovery Charge	Q_{rr}	$I_F = 10 \text{ A, dI/dt} = 100 \text{ A/us,}$ $V_R = 400 \text{V, T}_J = 150 ^{\circ}\text{C}$		121		nC			
Peak Recovery Current	I _{RRM}	VR = 400 V, 1j = 100 C		2.8		Α			
Reverse Recovery Time	T _{rr}	$I_F = 10 \text{ A}, dI/dt = 500 \text{ A/us},$		30		ns			
Reverse Recovery Charge	Q_{rr}	$V_R = 400V, T_A = 25^{\circ}C$		204		nC			
Peak Recovery Current	I _{RRM}	VR = 400V, 1j = 23 O		11.2		Α			
Reverse Recovery Time	T _{rr}	I _F = 10 A, dl/dt = 500 A/us,		30		ns			
Reverse Recovery Charge	Q_{rr}	$V_R = 400V, T_1 = 150$ °C	_	183		nC			
Peak Recovery Current	I _{RRM}	VR = 400 V, 1j = 100 O		9.9		Α			

Notes

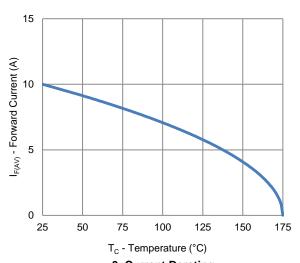
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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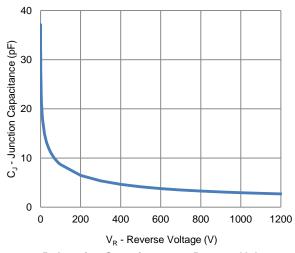
Typical Electrical Characteristics



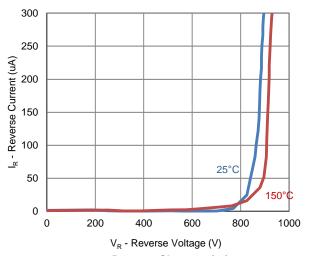
1. Forward Characteristics



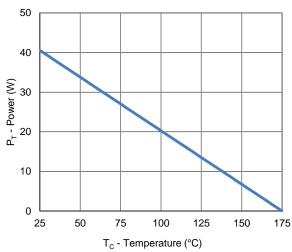
3. Current Derating



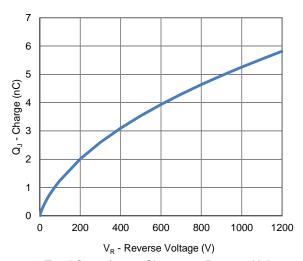
5. Junction Capacitance vs. Reverse Voltage



2. Reverse Characteristics

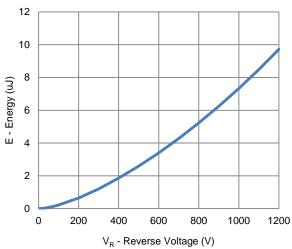


4. Power Derating

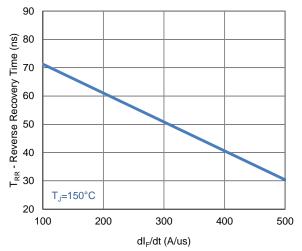


6. Total Capacitance Charge vs. Reverse Voltage

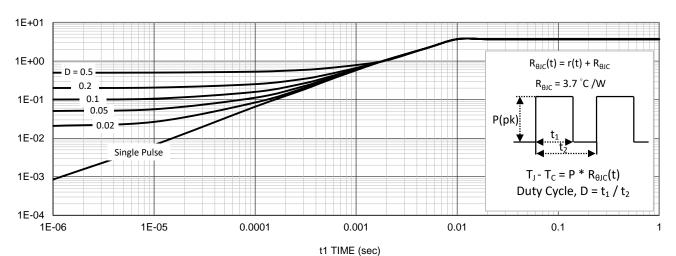
Typical Electrical Characteristics





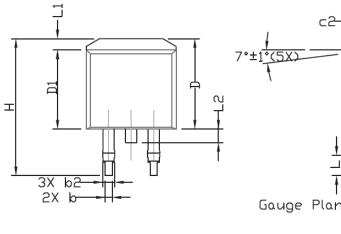


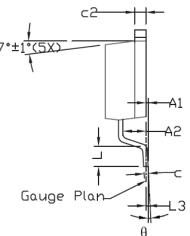
8. Reverse Recovery Time vs. dl_F/dt

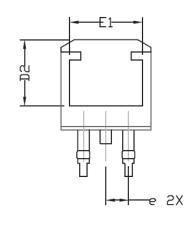


9. Thermal Transient Junction to Ambient

Package Information







CVAADEI	DIMENSIONAL REQMTS			INCHES REQMTS			
SYMBOL	MIN	NDM	MAX	MIN	NDM	MAX	
Α	4,30	4.57	4,72	0.169	0.180	0.186	
A1	0		0,25	0		0.010	
A2	2,47	2.57	2,67	0.097	0.101	0.105	
b	0.69	0,813	0.94	0.027	0.032	0.037	
b2	1.17	1.27	1,45	0.046	0.050	0.057	
С	0.48	0,50	0.60	0.019	0.020	0.024	
c2	1,17	1.27	1.37	0,046	0.050	0,054	
D	9.80	10.05	10,30	0.386	0,396	0.406	
D1	8,64	8.78	9,65	0,340	0.346	0,380	
D2	7.12	7.37	7,62	0.280	0.290	0.300	
E	9,70	10.15	10.54	0,382	0.400	0.415	
E1	8,00	8,20	8,40	0.315	0,323	0.331	
е	2.54 BSC			0.100 BSC			
H	14,99	15,24	15,49	0.590	0.600	0.610	
L	1,78	2.29	2.79	0.070	0.090	0.110	
L1	1.02	1.27	1.52	0.040	0.050	0,060	
L2			1.75			0.069	
L3		0,254			0.010		
θ	0.		8*	0°		8°	