# 650-V Direct WBG Diode

### **Key Features:**

- SiC performance
- Easy paralleling
- · High current carrying capability
- · Very low junction capacitance
- Highly stable V<sub>F</sub> and Q<sub>RR</sub> at elevated temperatures

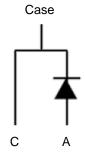
## **Typical Applications:**

- · Soft switching topologies
- · Secondary side rectification

PRODUCT SUMMARY				
VBR (V)	$V_F(V)$	I <sub>F(AV)</sub> (A)		
650	1.8	10		







ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C UNLESS OTHERWISE NOTED)					
Parameter			Limit	Units	
Cathode-Anode Voltage		$V_{BR}$	650	V	
Diode Forward Current a	T <sub>C</sub> =25°C	I <sub>F(AV)</sub>	10	Α	
Single Pulse Forward Current <sup>b</sup>	T <sub>C</sub> =25°C	I <sub>FSM</sub>	50	Α	
Joule Integral		i <sup>2</sup> t	12	A²·s	
Power Dissipation <sup>a</sup>	T <sub>C</sub> =25°C	$P_D$	50	W	
Storage Temperature Range		T <sub>stg</sub>	-55 to 175	°C	
Operating Junction Temperature		$T_J$	-40 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Maximum	Units		
Maximum Junction-to-Ambient °	$R_{\theta JA}$	40	°C/W		
Maximum Junction-to-Case	$R_{\theta JC}$	3	C/VV		

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#### Notes

- a. Package Limited
- b. Pulse width limited by maximum junction temperature
- c. Surface Mounted on 1" x 1" FR4 Board.

### **Electrical Characteristics**

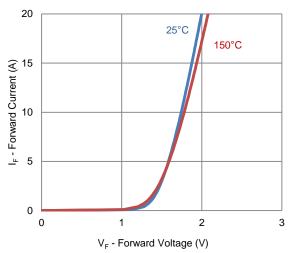
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
		Static				
[	V <sub>F</sub>	$I_F = 10 \text{ A}$ $I_F = 10 \text{ A}, T_J = 150^{\circ}\text{C}$		1.8		V
Forward Voltage <sup>a</sup>	V <sub>F</sub>			1.84		V
Repetitive Peak Reverse Voltage	$V_{RRM}$	$T_J = -40^{\circ}\text{C to } 150^{\circ}\text{C}$ 650				V
Junction Capacitance	CJ	$V_R = 200 \text{ V}, V_{\text{sine}} = 0.6 \text{ V}_{\text{eff}},$ f = 100  kHz		6.3		pF
		V <sub>R</sub> = 650 V			2	uA
Reverse Leakage Current	I <sub>R</sub>	V <sub>R</sub> = 650 V, T <sub>J</sub> = 150°C			10	uA
	•	Dynamic <sup>b</sup>				•
Reverse Recovery Time	$T_{rr}$	1 10 A dl/dt 100 A/vo		76		ns
Reverse Recovery Charge	Q <sub>rr</sub>	$I_F = 10 \text{ A}, \text{ dI/dt} = 100 \text{ A/us},$ $V_R = 400 \text{V}, T_J = 25 ^{\circ}\text{C}$		149		nC
Peak Recovery Current	I <sub>RRM</sub>	VR = 400V, 1J = 25 C		3.3		Α
Reverse Recovery Time	T <sub>rr</sub>	1 10 A d1/dt 100 A/up		71		ns
Reverse Recovery Charge	$Q_{rr}$	$I_F = 10 \text{ A}, \text{ dI/dt} = 100 \text{ A/us},$ $V_R = 400 \text{ V}, T_J = 150 ^{\circ}\text{ C}$		121		nC
Peak Recovery Current	I <sub>RRM</sub>	VR = 400 V, 1j = 100 O		2.8		Α
Reverse Recovery Time	T <sub>rr</sub>	1 10 A d1/dt 500 A/us		30		ns
Reverse Recovery Charge	Q <sub>rr</sub>	$I_F = 10 \text{ A}, \text{ dI/dt} = 500 \text{ A/us},$ $V_R = 400 \text{ V}, T_{.1} = 25 ^{\circ}\text{ C}$		204		nC
Peak Recovery Current	I <sub>RRM</sub>	VR = 400V, 1j = 25 O		11.2		Α
Reverse Recovery Time	T <sub>rr</sub>	L = 10 A dl/dt = 500 A/uc		30		ns
Reverse Recovery Charge	Q <sub>rr</sub>	$I_F = 10 \text{ A, dI/dt} = 500 \text{ A/us,}$ $V_R = 400 \text{ V, T}_J = 150 ^{\circ} \text{ C}$		183		nC
Peak Recovery Current	I <sub>RRM</sub>	v <sub>R</sub> = ∓00 v, 1j = 100 0		9.9		Α

#### Notes

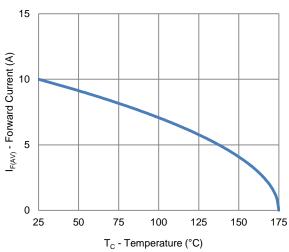
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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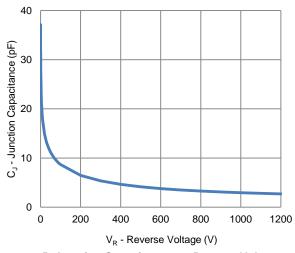
## **Typical Electrical Characteristics**



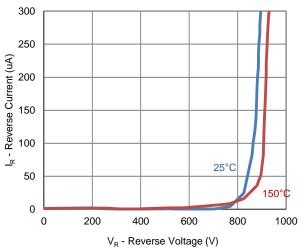
#### 1. Forward Characteristics



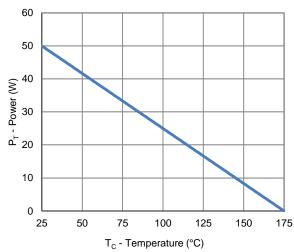
3. Current Derating



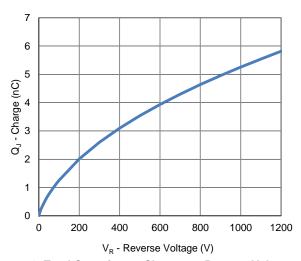
5. Junction Capacitance vs. Reverse Voltage



2. Reverse Characteristics

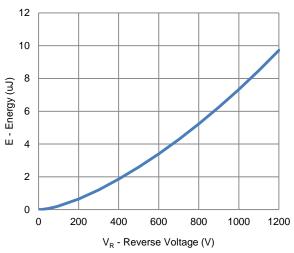


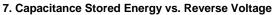
4. Power Derating

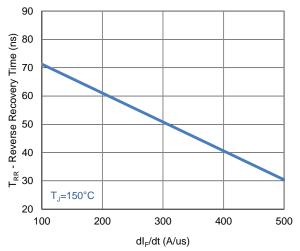


6. Total Capacitance Charge vs. Reverse Voltage

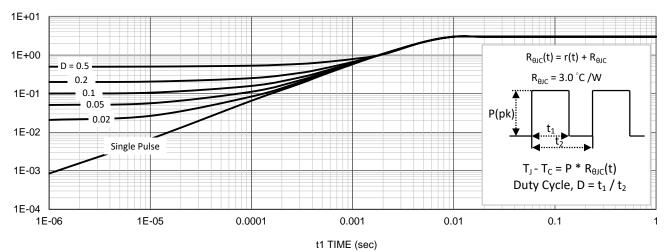
# **Typical Electrical Characteristics**





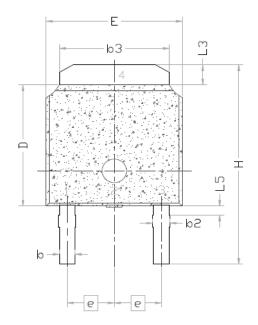


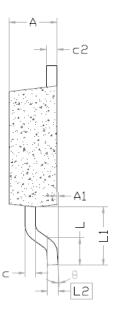
8. Reverse Recovery Time vs. dl<sub>F</sub>/dt

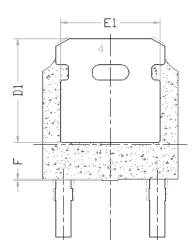


9. Thermal Transient Junction to Ambient

# **Package Information**







02400	DIMENS:		REQMTS	
SYMBOL	MIN	NDM	MAX	
E	6.40	6.60	6.731	
L	1.40	1.52	1.77	
L1	2.743 REF			
L2	0.	.508 BS		
L3	0.89		1.27	
L5				
D	6.00	6.10	6.223	
I	9.40	10.00	10.40	
2	0,64	0,76	0.88	
b2	0.77	0.84	1.14	
9	5.21	5.34	5.46	
۵		286 BS		
A	2.20	2.30	2.38	
A1	0		0.127	
$\cup$	0.46	0.50	0.60	
2	0,46	0,50	0.58	
D1	5.21			
E1 F	4.40			
F			0.45	
θ	0*		10°	

### Note:

- 1. All Dimension Are In mm.
- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.