1200-V Direct WBG Diode

Key Features:

- SiC performance
- Easy paralleling
- · High current carrying capability
- · Very low junction capacitance
- Highly stable V_F and Q_{RR} at elevated temperatures

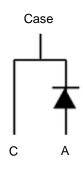
Typical Applications:

- · Soft switching topologies
- Secondary side rectification

PRODUCT SUMMARY				
V _{BR} (V)	$V_F(V)$	I _{F(AV)} (A)		
1200	1.85	20		







ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Units	
Cathode-Anode Voltage		V_{BR}	1200	V	
Diode Forward Current a	T _C =25°C	I _{F(AV)}	20	Α	
Single Pulse Forward Current ^b	T _C =25°C	I _{FSM}	90	Α	
Joule Integral		i ² t	40	A²·s	
Power Dissipation ^a	T _C =25°C	P_D	104	W	
Storage Temperature Range		T _{stg}	-55 to 175	°C	
Operating Junction Temperature		T_J	-40 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Maximum	Units		
Maximum Junction-to-Ambient °	$R_{\theta JA}$	40	°C/W		
Maximum Junction-to-Case	$R_{\theta JC}$	1.45	C/VV		

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Notes

- a. Package Limited
- b. Pulse width limited by maximum junction temperature
- c. Surface Mounted on 1" x 1" FR4 Board.

Electrical Characteristics

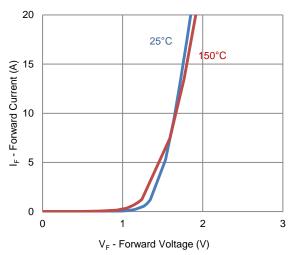
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Forward Voltage ^a	V _F	I _F = 20 A		1.85		V	
	V F	$I_F = 20 \text{ A}, T_J = 150^{\circ}\text{C}$		1.92		V	
Repetitive Peak Reverse Voltage	V_{RRM}	$T_J = -40$ °C to 150°C	1200			V	
Junction Capacitance	CJ	$V_R = 200 \text{ V}, V_{\text{sine}} = 0.6 \text{ V}_{\text{eff}},$ f = 100 kHz		12		pF	
Poverse Leekage Current		V _R = 1200 V			10	uA	
Reverse Leakage Current	I _R	V _R = 1200 V, T _J = 120°C			60	uA	
	Dynamic ^b						
Reverse Recovery Time	T _{rr}	I _F = 20 A, dl/dt = 100 A/us,		84		ns	
Reverse Recovery Charge	Q_{rr}	$T_J = 25$ °C		213		nC	
Peak Recovery Current	I _{RRM}	1j = 25 O		4.3		Α	
Reverse Recovery Time	T _{rr}	$I_F = 20 \text{ A}, dI/dt = 100 \text{ A/us},$		82		ns	
Reverse Recovery Charge	Q_{rr}	$T_{ij} = 20 \text{ A}$, divid = 100 A/ds, $T_{ij} = 150 \text{ °C}$		197		nC	
Peak Recovery Current	I _{RRM}	15 = 100 0		3.9		Α	
Reverse Recovery Time	T _{rr}	$I_F = 20 \text{ A}, \text{ dI/dt} = 500 \text{ A/us},$		47		ns	
Reverse Recovery Charge	Q_{rr}	$T_{\rm I} = 20^{\circ} \text{A}$, divid = 500 Avds, $T_{\rm I} = 25^{\circ} \text{C}$		482		nC	
Peak Recovery Current	I _{RRM}	1 J = 20 O		17.9		Α	
Reverse Recovery Time	T_{rr}	L = 20 A dl/dt = 500 A/vs		45		ns	
Reverse Recovery Charge	Q _{rr}	$I_F = 20 \text{ A}, \text{ dI/dt} = 500 \text{ A/us},$ $T_{.I} = 150^{\circ}\text{C}$		435		nC	
Peak Recovery Current	I _{RRM}	1,1 = 100 0		15.9		Α	

Notes

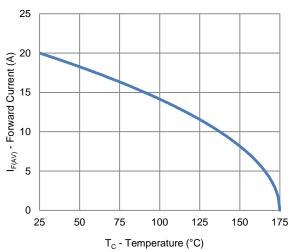
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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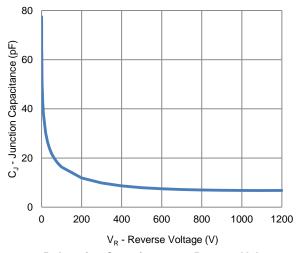
Typical Electrical Characteristics



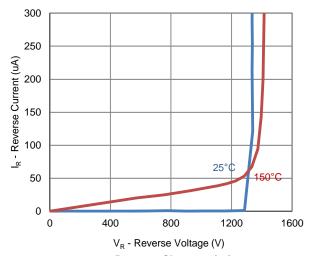
1. Forward Characteristics



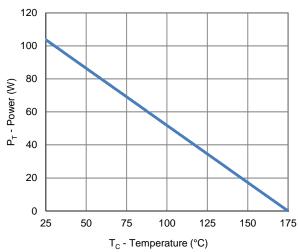
3. Current Derating



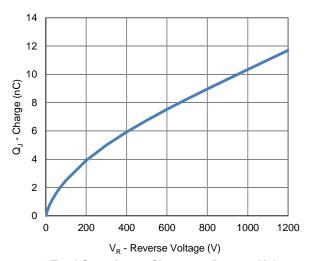
5. Junction Capacitance vs. Reverse Voltage



2. Reverse Characteristics

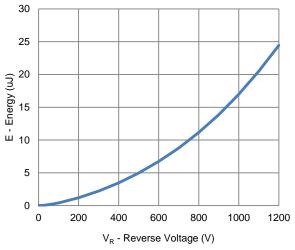


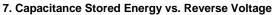
4. Power Derating

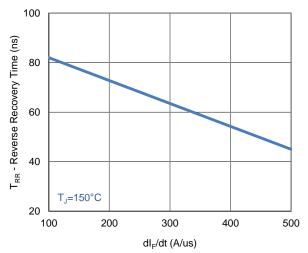


6. Total Capacitance Charge vs. Reverse Voltage

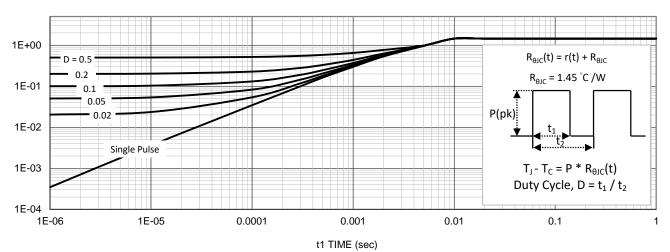
Typical Electrical Characteristics





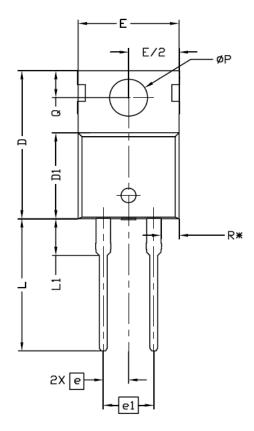


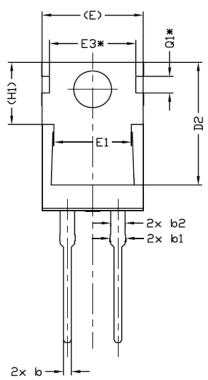
8. Reverse Recovery Time vs. dl_F/dt

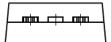


9. Thermal Transient Junction to Ambient

Package Information







SYMBOL	DIMENSIONS			
SYMBOL	MIN.	NOM.	MAX.	
Α	4,24	4.44	4,64	
A1	1.15	1.27	1.40	
A2	2.30	2.48	2.70	
b	0.70	0.80	0.90	
b1	1.20	1.55	1.75	
b2	1,20	1.45	1.70	
С	0.40	0.50	0.60	
D	14.70	15.37	16.00	
D1	8.82	8.92	9,02	
D2	12.43	12.73	12.83	
E	9.96	10.16	10.36	
E1	6,86	7,77	8,89	
E3*	8.70REF.			
е	2,54BSC			
e1	5.08BSC			
H1	6.30	6.45	6.60	
L	13.47	13.72	13.97	
L1	3.60	3.80	4.00	
ØP	3.75	3.84	3,93	
Q	2,60	2.80	3,00	
Q1*	1.73REF.			
R*	1.82REF.			