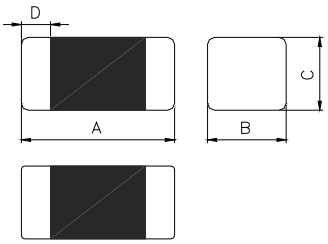
High Current Ferrite Chip Inductor (Lead Free) FCH160808SF-SERIES

1. Features

1.Low DC resistance structure of electrode to prevent wasteful electric power consumption.
2.Suitable for reflow soldering.
3.Excellent solder ability and heat resistance.
4.100% Lead(Pb) & Halogen-Free and RoHS compliant.
5.Operating Temperature:-55~+105℃(Including self-temperature rise)

2. Dimensions

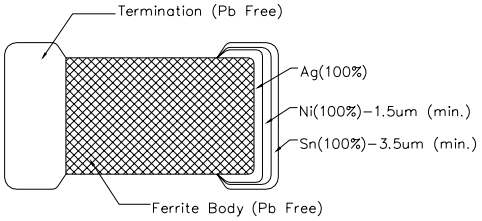


Chip Size				
Series	A(mm)	B(mm)	C(mm)	D(mm)
160808	1.60±0.15	0.80±0.15	0.95 max.	0.30±0.20

3. Part Numbering

FCH	160808	S	F	-	4R7	M
A	B	C	D		E	F

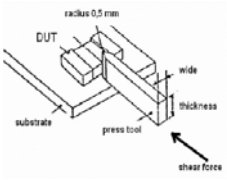
A: Series
B: Dimension
C: Category Code
D: Material Lead Free Material
E: Inductance 4R7=4.7uH
F: Inductance Tolerance M=±20%



4. Specification

Tai-Tech Part Number	Inductance (uH)	Test Frequency (Hz)	Rated Current (mA) max.	DCR (Ω) max.
FCH160808SF-1R0M	1.0±20%	1M / 60mV	1700	0.08
FCH160808SF-2R2M	2.2±20%	1M / 60mV	1300	0.13
FCH160808SF-4R7M	4.7±20%	1M / 60mV	1000	0.20

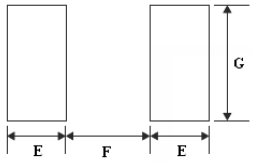
- Irms:DC current that causes temperature rise(ΔT 40℃) from 25℃ ambient.
- In compliance with EIA 595

Item	Performance	Test Condition
Solderability	More than 95% of the terminal electrode should be covered with solder.	a. Method B, 4 hrs @155°C dry heat @235°C±5°C Test time:5 +0/-0.5 seconds. b. Method D category 3. (steam aging 8hours ± 15 min)@ 260°C±5°C
Resistance to Soldering Heat	Appearance：No damage. Inductance：within±10% of initial value Q：Shall not exceed the specification value. RDC：within ±15% of initial value and shall not exceed the specification value	Number of heat cycles: 1 Temperature (°C) / Time (s) / Temperature ramp/immersion and emersion rate 260 ±5 (solder temp) / 10 ±1 / 25mm/s ±6 mm/s Depth: completely cover the termination
Terminal strength	Appearance：No damage. Inductance：within±10% of initial value Q：Shall not exceed the specification value. RDC：within ±15% of initial value and shall not exceed the specification value 	Preconditioning: Run through reflow for3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles) Component mounted on a PCB apply a force >0805inch(2012mm):1kg <=0805inch(2012mm):0.5kg to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to shock the component being tested.

6. Soldering and Mounting

6-1. Recommended PC Board Pattern

Chip Size						Land Patterns For Reflow Soldering		
Serie	**Type**	**A(mm)**	**B(mm)**	**C(mm)**	**D(mm)**	**E(mm)**	**F(mm)**	**G(mm)**
FCH	160808	1.60±0.15	0.80±0.15	0.95 max.	0.30±0.20	0.80	0.85	0.95



PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

6-2. Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

6-2.1 Soldering Reflow::

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

5. Reliability and Test Condition

<table>
<tr><th>Item</th><th>Performance</th><th>Test Condition</th></tr>
<tr><td>Operating Temperature</td><td>-55~+105℃
(Including self-temperature rise)</td><td>--</td></tr>
<tr><td>Transportation
Storage Temperature</td><td>-55~+105℃
(on board)</td><td>For long storage conditions, please see the Application Notice</td></tr>
<tr><td>Inductance (Ls)</td><td rowspan="3">Refer to standard electrical characteristics list</td><td>Agilent4291
Agilent E4991
Agilent4287
Agilent16192</td></tr>
<tr><td>DC Resistance</td><td>Agilent 4338</td></tr>
<tr><td>Rated Current</td><td>DC Power Supply
Over Rated Current requirements, there will be some risk</td></tr>
<tr><td>Temperature Rise Test</td><td>ΔT 40℃Max</td><td>1. Applied the allowed DC current.
2. Temperature measured by digital surface thermometer.</td></tr>
<tr><td>Life test</td><td rowspan="2">Appearance: no damage.

Inductance: within±10%of initial value.
Q：Shall not exceed the specification value.
RDC：within ±15% of initial value and shall not exceed the specification value</td><td>Preconditioning: Run through reflow for3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles)
Temperature: 105±2℃
Applied current: rated current.
Duration: 1000±12hrs.
Measured at room temperature after placing for 24±2 hrs.</td></tr>
<tr><td>Load Humidity</td><td>Preconditioning: Run through IR reflow for3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles)
Humidity: 85±2%R.H.
Temperature: 85±2℃.
Duration: 1000hrs Min.
Bead: with 100% rated current.
Inductance: with 10% rated current.
Measured at room temperature after placing for 24±2 hrs.</td></tr>
<tr><td>Thermal shock</td><td>Appearance: no damage.

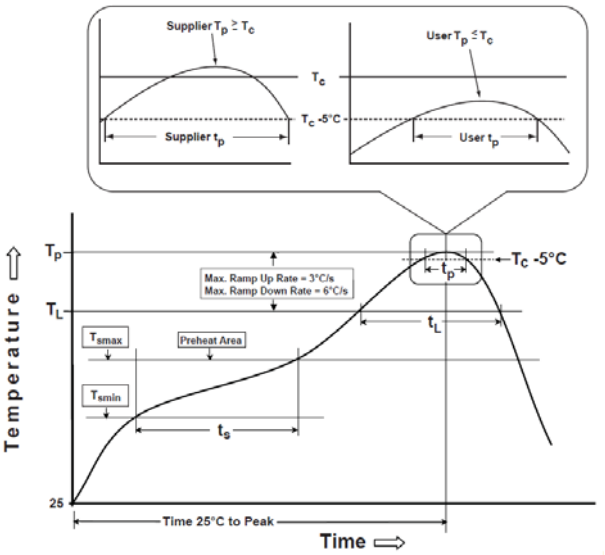
Inductance: within±10%of initial value.
Q：Shall not exceed the specification value.
RDC：within ±15% of initial value and shall not exceed the specification value</td><td>Preconditioning: Run through reflow for3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles)
Condition for 1 cycle
Step1: -55±2℃ 30±5 min.
Step2: 25±2℃ ≦0.5min
Step3: +105±2℃ 30±5min.
Number of cycles: 500
Measured at room temperature after placing for 24±2 hrs.</td></tr>
<tr><td>Vibration</td><td>Appearance：No damage.
Inductance：within±10% of initial value
Q：Shall not exceed the specification value.
RDC：within ±15% of initial value and shall not exceed the specification value</td><td>Preconditioning: Run through reflow for3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles)
Oscillation Frequency: 10Hz～2KHz～10Hz for 20 minutes
Equipment： Vibration checker
Total Amplitude:10g
Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations)。</td></tr>
<tr><td>Bending</td><td>Appearance：No damage.
Inductance：within±10% of initial value
Q：Shall not exceed the specification value.
RDC：within ±15% of initial value and shall not exceed the specification value</td><td>Shall be mounted on a FR4 substrate of the following dimensions:
>=0805inch(2012mm):40x100x1.2mm
<0805inch(2012mm):40x100x0.8mm
Bending depth:
>=0805inch(2012mm):1.2mm
<0805inch(2012mm):0.8mm
Duration of 10 sec for a min.</td></tr>
<tr><td>Shock</td><td>Appearance：No damage.
Inductance：within±10% of initial value
Q：Shall not exceed the specification value.
RDC：within ±15% of initial value and shall not exceed the specification value</td><td>Test condition:
<table><tr><th>Type</th><th>Peak Value (g's)</th><th>Normal duration (D) (ms)</th><th>Wave form</th><th>Velocity change (Vi)ft/sec</th></tr><tr><td>SMD</td><td>50</td><td>11</td><td>Half-sine</td><td>11.3</td></tr><tr><td>Lead</td><td>50</td><td>11</td><td>Half-sine</td><td>11.3</td></tr></table></td></tr>
</table>

6-2.2 Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. (Figure 2.)

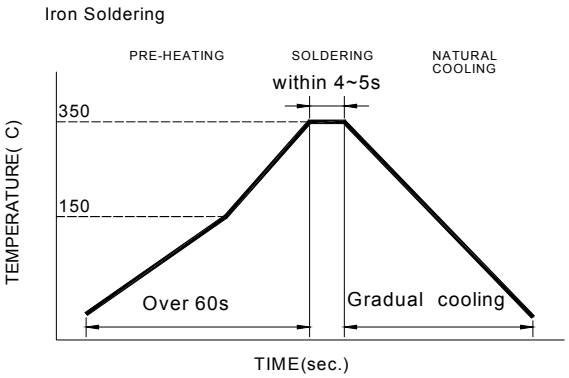
- Preheat circuit and products to 150℃
- 350℃ tip temperature (max)
- Never contact the ceramic with the iron tip
- 1.0mm tip diameter (max)
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- Limit soldering time to 4~5sec.

Fig.1 Soldering Reflow



Reflow times: 3 times max

Fig.2 Iron soldering temperature profiles



Iron Soldering times：1 times max

Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat -Temperature Min(T_{smin}) -Temperature Max(T_{smax}) -Time(t_s)from(T_{smin} to T_{smax})	 150℃ 200℃ 60-120seconds
Ramp-up rate($T_L$to T_p)	3℃/second max.
Liquidus temperature(T_L) Time(t_L)maintained above T_L	217℃ 60-150 seconds
Classification temperature(T_c)	See Table (1.2)
Time(t_p) at Tc- 5℃ (Tp should be equal to or less than Tc.)	< 30 seconds
Ramp-down rate(T_p to T_L)	6℃ /second max.
Time 25℃ to peak temperature	8 minutes max.

Tp: maximum peak package body temperature, **Tc**: the classification temperature.

For user (customer) **Tp** should be equal to or less than **Tc.**

Table (1.2) Package Thickness/Volume and Classification Temperature (T_c)

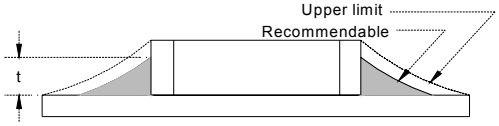
	Package Thickness	Volume mm^3 <350	Volume mm^3 350-2000	Volume mm^3 >2000
PB-Free Assembly	<1.6mm	260℃	260℃	260℃
	1.6-2.5mm	260℃	250℃	245℃
	≥2.5mm	250℃	245℃	245℃

Reflow is referred to standard IPC/JEDEC J-STD-020E。

6-2.3 Solder Volume:

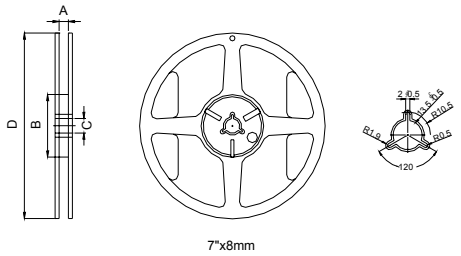
Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in right side:

Minimum fillet height = soldering thickness + 25% product height



7. Packaging Information

7-1. Reel Dimension

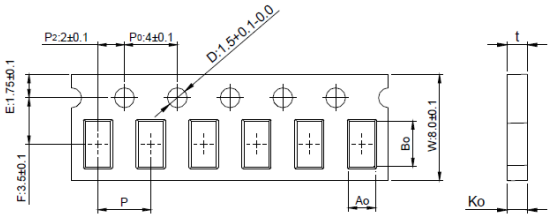


7"x8mm

Type	A(mm)	B(mm)	C(mm)	D(mm)
7"x8mm	9.0±0.5	60±2	13.5±0.5	178±2.0

7-2 Tape Dimension / 8mm

■Material of taping is paper

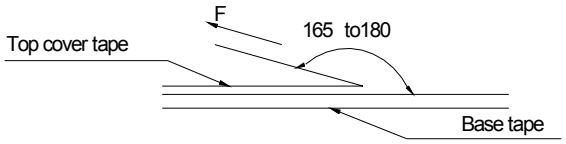


Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
160808	1.90±0.05	1.10±0.05	0.95±0.05	4.0±0.10	0.95±0.05

7-3. Packaging Quantity

Chip size	160808
Reel	4000
Inner box	20000
Middle box	100000
Carton	200000

7-4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp. (℃)	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min
5~35	45~85	860~1060	300

Application Notice

- Storage Conditions(component level)
  To maintain the solder ability of terminal electrodes:
  1. TAI-TECH products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
  2. Temperature and humidity conditions: 40℃ and 60% RH.
  3. Recommended products should be used within 12 months from the time of delivery.
  4. The packaging material should be kept where no chlorine or sulfur exists in the air.

- Transportation
  1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
  2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
  3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

Typical Inductance v.s. Frequency Curve