



PCN Number:	20230920000.2			PCN Date:	September 20, 2023																														
Title:	Qualification of MLA as an additional Assembly/Test site & BOM change for Select Devices																																		
Customer Contact:	Change Management Team		Dept:	Quality Services																															
Proposed 1st Ship Date:	Mar 18, 2024		Sample Requests accepted until:	Oct 20, 2023*																															
*Sample requests received after Oct 20, 2023 will not be supported.																																			
Change Type:																																			
<input checked="" type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design	<input type="checkbox"/>	Wafer Bump Material																														
<input checked="" type="checkbox"/>	Assembly Process	<input type="checkbox"/>	Data Sheet	<input type="checkbox"/>	Wafer Bump Process																														
<input checked="" type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change	<input type="checkbox"/>	Wafer Fab Site																														
<input type="checkbox"/>	Mechanical Specification	<input checked="" type="checkbox"/>	Test Site	<input type="checkbox"/>	Wafer Fab Material																														
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process	<input type="checkbox"/>	Wafer Fab Process																														
PCN Details																																			
Description of Change:																																			
<p>Texas Instruments Incorporated is announcing the qualification of MLA as an additional Assembly site & Test site for set of devices listed below. Construction differences are as follows:</p> <p>Group 1 Device BOM Table Comparison:</p> <table border="1"> <thead> <tr> <th></th> <th>HNT</th> <th>HNT New</th> <th>MLA</th> </tr> </thead> <tbody> <tr> <td>Mount Compound</td> <td>SID#400180</td> <td>SID#400180</td> <td>4224264</td> </tr> <tr> <td>Mold Compound</td> <td>SID#450179</td> <td>SID#450179</td> <td>4224264</td> </tr> <tr> <td>Die Coat</td> <td>none</td> <td>PI</td> <td>None or PI</td> </tr> <tr> <td>Die Thickness</td> <td>15 mil</td> <td>15 mil</td> <td>10.5 mil</td> </tr> <tr> <td>Bond wire composition, diameter</td> <td>Au, 1.0 mil</td> <td>Au, 1.0 mil</td> <td>Cu, 1.0 mil</td> </tr> </tbody> </table> <p>Group 2 Device BOM Table Comparison:</p> <table border="1"> <thead> <tr> <th></th> <th>Current</th> <th>New</th> </tr> </thead> <tbody> <tr> <td>Die Coat</td> <td>none</td> <td>PI</td> </tr> </tbody> </table> <p>Test coverage, insertions, conditions will remain consistent with current testing and verified with test MQ</p>							HNT	HNT New	MLA	Mount Compound	SID#400180	SID#400180	4224264	Mold Compound	SID#450179	SID#450179	4224264	Die Coat	none	PI	None or PI	Die Thickness	15 mil	15 mil	10.5 mil	Bond wire composition, diameter	Au, 1.0 mil	Au, 1.0 mil	Cu, 1.0 mil		Current	New	Die Coat	none	PI
	HNT	HNT New	MLA																																
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Die Coat	none	PI																																	
Reason for Change:																																			
Supply continuity																																			
Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):																																			
None																																			
Impact on Environmental Ratings																																			
<p>Checked boxes indicate the status of environmental ratings following implementation of this change. If below boxes are checked, there are no changes to the associated environmental ratings.</p> <table border="1"> <thead> <tr> <th>RoHS</th> <th>REACH</th> <th>Green Status</th> <th>IEC 62474</th> </tr> </thead> <tbody> <tr> <td><input checked="" type="checkbox"/> No Change</td> <td><input checked="" type="checkbox"/> No Change</td> <td><input checked="" type="checkbox"/> No Change</td> <td><input checked="" type="checkbox"/> No Change</td> </tr> </tbody> </table>						RoHS	REACH	Green Status	IEC 62474	<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change																						
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<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change																																

Changes to product identification resulting from this PCN:			
Assembly Site	Assembly Site Origin (22L)	Assembly Country Code (23L)	Assembly City
Hana	HNT	THA	Ayutthaya
MLA	MLA	MYS	Kuala Lumpur

Sample product shipping label (not actual product label)

(1P) SN74LS07NSR

(Q) 2000 (D) 0336

(31T) LOT: 3959047MLA

(4W) TKY (1T) 7523483SI2

(P)

(2P) REV: (V) 0033317

(20L) CS0: SHE (21L) CCO:USA

(22L) AS0: MLA (23L) ACO: MYS

Product Affected:

Group 1 Device list (MLA as an additional Assembly/Test site + BOM Changes):

TPA6211A1TDGNRQ1

Group 2 Device list (Addition of die coat only):

SN016211TDGNRQ1

TI Information
Selective Disclosure

Automotive New Product Qualification Summary
(As per AEC-Q100 and JEDEC Guidelines)

TPA6211A1TDGNRQ1 MLA PI and non-PI Offload
Approve Date 17-AUGUST -2023

Product Attributes

Attributes	Qual Device: TPA6211A1TDGNRQ1	QBS Reference: SN65HVD1040AQDRQ1	QBS Reference: UCC27624QDGNRQ1	QBS Reference: TPA6211A1TDGNRQ1	QBS Reference: TPA6211A1TDNVRQ1
Automotive Grade Level	Grade 2	Grade 1	Grade 1	Grade 2	Grade 2
Operating Temp Range (C)	-40 to 105	-40 to 125	-40 to 125	-40 to 105	-40 to 105
Product Function	Signal Chain	Interface	Power Management	Signal Chain	Signal Chain
Wafer Fab Supplier	MH8	DL-LIN	DMOS6	MH8	MH8
Assembly Site	MLA	MLA	MLA	ASESHAT	MLA
Package Group	VSSOP	-	VSSOP	VSSOP	QFN
Package Designator	DGN	D	DGN	DGN	DNV
Pin Count	8	8	8	8	8

- QBS: Qual By Similarity
- Qual Device TPA6211A1TDGNRQ1 is qualified at MSL2 260C

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: TPA6211A1TDGNRQ1	QBS Reference: SN65HVD1040AQDRQ1	QBS Reference: UCC27624QDGNRQ1	QBS Reference: TPA6211A1TDGNRQ1	QBS Reference: TPA6211A1TDNVRQ1
Test Group A - Accelerated Environment Stress Tests												
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL1 260C	-	-	3/Pass	3/Pass	-	-

PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL2 260C	-	3/Pass	-	-	3/0/0	-
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL3 260C	-	-	-	-	-	3/0/0
HAST	A2	JEDEC JESD22-A110	3	77	Biased HAST	110C/85%RH	264 Hours	-	-	-	-	1/77/0
HAST	A2	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	3/231/1 ²	3/231/0	3/231/0	3/231/0	-
AC/UHAST	A3	JEDEC JESD22-A102/JEDEC JESD22-A118	3	77	Autoclave	121C/15psig	96 Hours	-	-	3/231/0	3/231/0	-
AC/UHAST	A3	JEDEC JESD22-A102/JEDEC JESD22-A118	3	77	Unbiased HAST	130C/85%RH	96 Hours	3/231/0	-	-	-	3/231/0
TC	A4	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-55C/125C	1000 Cycles	3/231/0	-	-	-	3/231/0
TC	A4	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	-	3/231/0	3/231/0	3/231/0	-
TC-BP	A4	MIL-STD883 Method 2011	1	5	Post Temp Cycle Bond Pull	-	-	-	-	1/5/0	1/5/0	1/5/0
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	150C	1000 Hours	-	3/135/0	3/231/0	-	-
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	150C	500 Hours	1/45/0	-	-	1/45/0	1/45/0
Test Group B - Accelerated Lifetime Simulation Tests												
HTOL	B1	JEDEC JESD22-A108	1	77	Life Test	125C	1000 Hours	-	-	-	3/231/0	-
ELFR	B2	AEC Q100-008	1	77	Early Life Failure Rate	125C	24 Hours	-	-	-	-	2/1600/0
ELFR	B2	AEC Q100-008	1	77	Early Life Failure Rate	125C	48 Hours	-	-	-	-	1/798/0 ¹
Test Group C - Package Assembly Integrity Tests												

WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	-	3/90/0	1/30/0	3/90/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	3/90/0	3/90/0	1/30/0	3/90/0
SD	C3	JEDEC J-STD-002	1	15	PB Solderability	>95% Lead Coverage	-	-	1/15/0	-	1/15/0	-
SD	C3	JEDEC J-STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	1/15/0	1/15/0	-	1/15/0	-
PD	C4	JEDEC JESD22-B100 and B108	1	10	Physical Dimensions	Cpk>1.67	-	3/30/0	3/30/0	3/30/0	3/30/0	3/30/0
Test Group D - Die Fabrication Reliability Tests												
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDDb	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
NBTI	D4	-	-	-	Negative Bias Temperature Instability	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
Test Group E - Electrical Verification Tests												
ESD	E2	AEC Q100-002	1	3	ESD HBM	-	2000 Volts	1/3/0	-	-	-	1/3/0
ESD	E2	AEC Q100-002	1	3	ESD HBM	-	3000 Volts	-	-	-	1/3/0	-
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	1500 Volts	-	-	-	1/3/0	-
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	500 Volts	1/3/0	-	-	-	-
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	750 Volts	-	-	-	-	1/3/0
LU	E4	AEC Q100-004	1	6	Latch-Up	Per AEC Q100-004	-	1/6/0	-	-	1/6/0	1/6/0
ED	E5	AEC Q100-009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	1/30/0	3/90/0	1/30/0	3/90/0	3/90/0
Additional Tests												
LI	C6	JEDEC JESD22-B105	1	5	Lead Integrity	10 leads from each of 5 parts	-	-	-	-	1/6/0	-
MSL	-	JEDEC J-STD20	3	12	Moisture Classification	-	-	3/36/0	-	-	-	-

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

Orderable Part Numbers

The following table contains a list of all TI Orderable Part Numbers (OPNs) released by this qualification per Product Qualification Family definition (AEC Q100 Appendix 1). Group E results shown above cover all part numbers listed here.

TPA6211A1TDGNRQ1

Ambient Operating Temperature by Automotive Grade Level:

- Grade 0 (or E) : -40C to +150C
- Grade 1 (or Q) : -40C to +125C
- Grade 2 (or T) : -40C to +105C
- Grade 3 (or I) : -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

- Room/Hot/Cold : HTOL, ED
- Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU
- Room : ACU/HAST

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

TI Qualification ID: R-CHG-2211-016

¹ Lost in handler

² Inserted in handler incorrectly causing die crack.

Automotive New Product Qualification Summary
(As per AEC-Q100, AEC-Q006, and JEDEC Guidelines)

1mil Cu Wire at MLA
Approve Date 17-AUGUST -2023

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: TPA6211A1TDGNRQ1	QBS Package Reference: SN65HVD1040AQDRQ1
Test Group A - Accelerated Environment Stress Tests									
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL1 260C	-	-	3/ Pass
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL2 260C	-	3/Pass	-
PC	A1.1	-	3	22	SAM Precon Pre	Review for delamination	-	-	3/66/0
PC	A1.2	-	3	22	SAM Precon Post	Review for delamination	-	-	3/66/0
HAST	A2.1	JEDEC JESD22-A110	3	77	Biased HAST	130C	96 Hours	-	3/231/0
HAST	A2.1.2	-	3	1	Cross Section, post bHAST, 1X	Post stress cross section	Completed	-	-

HAST	A2.1.3	-	3	3	Wire Bond Shear, post bHAST, 1X	Post stress	Wires	-	2/6/0 ²
HAST	A2.1.4	-	3	3	Bond Pull over Stitch, post bHAST, 1X	Post stress	Wires	-	2/6/0 ²
HAST	A2.1.5	-	3	3	Bond Pull over Ball, post bHAST, 1X	Post stress	Wires	-	2/6/0 ²
HAST	A2.2	JEDEC JESD22-A110	3	70	Biased HAST	130C	192 Hours	-	3/209/0 ¹
HAST	A2.2.1	-	3	22	SAM Analysis, post bHAST 2X	Review for delamination	Completed	-	3/66/0
HAST	A2.2.2	-	3	1	Cross Section, post bHAST, 2X	Post stress cross section	Completed	-	3/3/0
HAST	A2.2.3	-	3	3	Wire Bond Shear, post bHAST, 2X	Post stress	Wires	-	3/9/0
HAST	A2.2.4	-	3	3	Bond Pull over Stitch, post bHAST, 2X	Post stress	Wires	-	3/9/0
HAST	A2.2.5	-	3	3	Bond Pull over Ball, post bHAST, 2X	Post stress	Wires	-	3/9/0
TC	A4.1	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-55C/125C	2000 Cycles	3/231/0	-
TC	A4.1	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	1000 Cycles	-	3/210/0
TC	A4.1	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	-	3/231/0
TC	A4.1.1	-	3	22	SAM Analysis, post TC 1X	Review for delamination	Completed	-	3/66/0
TC	A4.1.2	-	3	1	Cross Section, post TC, 1X	Post stress cross section	Completed	-	3/3/0
TC	A4.1.3	-	3	3	Wire Bond Shear, post TC, 1X	Post stress	Wires	-	3/9/0
TC	A4.1.4	-	3	3	Bond Pull over Stitch, post TC, 1X	Post stress	Wires	-	3/9/0

TC	A4.1.5	-	3	3	Bond Pull over Ball, post TC, 1X	Post stress	Wires	-	3/9/0
TC	A4.2	JEDEC JESD22-A104 and Appendix 3	3	70	Temperature Cycle	-55C/125C	2000 Cycles	3/231/0	-
TC	A4.2.1	-	3	22	SAM Analysis, post TC, 2X	Review for delamination	Completed	3/66/0	3/66/0
TC	A4.2.2	-	3	1	Cross Section, post TC, 2X	Post stress cross section	Completed	3/3/0	3/3/0
TC	A4.2.3	-	3	3	Wire Bond Shear, post TC, 2X	Post stress	Wires	3/9/0	3/9/0
TC	A4.2.4	-	3	3	Bond Pull over Stitch, post TC, 2X	Post stress	Wires	3/9/0	3/9/0
TC	A4.2.5	-	3	3	Bond Pull over Ball, post TC, 2X	Post stress	Wires	3/9/0	3/9/0
HTSL	A6.1	JEDEC JESD22-A103	3	45	High Temperature Storage Life	150C	1000 Hours	-	3/135/0
HTSL	A6.1.1	-	3	1	Cross Section, post HTSL, 1X	Post stress cross section	Completed	-	-
HTSL	A6.2	JEDEC JESD22-A103	3	44	High Temperature Storage Life	150C	2000 Hours	-	3/135/0
HTSL	A6.2.1	-	3	1	Cross Section, post HTSL, 2X	Post stress cross section	Completed	-	3/3/0
Test Group C - Package Assembly Integrity Tests									
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	3/90/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	3/90/0

- QBS: Qual By Similarity
- Qual Device TPA6211A1TDGNRQ1 is qualified at MSL2 260C
- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

Ambient Operating Temperature by Automotive Grade Level:

- Grade 0 (or E): -40C to +150C
- Grade 1 (or Q): -40C to +125C
- Grade 2 (or T): -40C to +105C
- Grade 3 (or I): -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

- Room/Hot/Cold : HTOL, ED
- Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU
- Room : AC/uHAST

Notes:

1 - Lost Unit

2 - Wire integrity tests not performed on 1 lot

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

TI Qualification ID: R-CHG-2211-016

Automotive New Product Qualification Summary (As per AEC-Q100 and JEDEC Guidelines)

TPA6211A1TDGNRQ1/SN016211TDGNRQ1 HNA Add PI - MIHO8/HNA - Grade 2 Q100 Rev H
Approve Date 25-JULY -2023

Product Attributes

Attributes	Qual Device: TPA6211A1TDGNRQ1	Qual Device: SN016211TDGNRQ1	QBS Package/Product Reference: TPA6211A1TDGNRQ1
Automotive Grade Level	Grade 2	Grade 2	Grade 2
Operating Temp Range (C)	-40 to 105	-40 to 105	-40 to 105
Product Function	Signal Chain	Signal Chain	Signal Chain
Wafer Fab Supplier	MH8	MH8	MH8
Assembly Site	HNA	HNA	ASESHAT
Package Group	VSSOP	VSSOP	VSSOP
Package Designator	DGN	DGN	DGN
Pin Count	8	8	8

- QBS: Qual By Similarity
- Qual Device TPA6211A1TDGNRQ1 is qualified at MSL3 260C

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: TPA6211A1TDGNRQ1	Qual Device: SN016211TDGNRQ1	QBS Reference: TPA6211A1TDGNRQ1
Test Group A - Accelerated Environment Stress Tests										
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL2 260C	-	-	-	3/Pass
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL3 260C	-	3/Pass	-	-
HAST	A2	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	-	-	3/231/0
AC/UHAST	A3	JEDEC JESD22-A102/JEDEC JESD22-A118	3	77	Autoclave	121C/15psig	96 Hours	-	-	3/231/0
AC/UHAST	A3	JEDEC JESD22-A102/JEDEC JESD22-A118	3	77	Unbiased HAST	130C/85%RH	96 Hours	3/231/0	-	-
TC	A4	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-55C/125C	1000 Cycles	3/231/0	-	-
TC	A4	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	-	-	3/231/0
TC-BP	A4	MIL-STD883 Method 2011	1	5	Post Temp Cycle Bond Pull	-	-	1/5/0	-	1/5/0

HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	150C	500 Hours	-	-	1/45/0
Test Group B - Accelerated Lifetime Simulation Tests										
HTOL	B1	JEDEC JESD22-A108	1	77	Life Test	125C	1000 Hours	-	-	3/231/0
Test Group C - Package Assembly Integrity Tests										
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	-	1/30/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	-	1/30/0
SD	C3	JEDEC J-STD-002	1	15	PB Solderability	>95% Lead Coverage	-	-	-	1/15/0
SD	C3	JEDEC J-STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	-	-	1/15/0
PD	C4	JEDEC JESD22-B100 and B108	1	10	Physical Dimensions	Cpk>1.67	-	1/10/0	-	3/30/0
Test Group D - Die Fabrication Reliability Tests										
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	-	Completed Per Process Technology Requirements
TDDb	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	-	Completed Per Process Technology Requirements
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	-	Completed Per Process Technology Requirements
NBTI	D4	-	-	-	Negative Bias Temperature Instability	-	-	Completed Per Process Technology Requirements	-	Completed Per Process Technology Requirements

SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements	-	Completed Per Process Technology Requirements
Test Group E - Electrical Verification Tests										
ESD	E2	AEC Q100-002	1	3	ESD HBM	-	3000 Volts	-	-	1/3/0
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	1500 Volts	-	-	1/3/0
LU	E4	AEC Q100-004	1	6	Latch-Up	Per AEC Q100-004	-	-	-	1/6/0
ED	E5	AEC Q100-009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	-	-	3/90/0

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

Orderable Part Numbers

The following table contains a list of all TI Orderable Part Numbers (OPNs) released by this qualification per Product Qualification Family definition (AEC Q100 Appendix 1). Group E results shown above cover all part numbers listed here.

SN016211TDGNRQ1	TPA6211A1TDGNRQ1
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Ambient Operating Temperature by Automotive Grade Level:

- Grade 0 (or E): -40C to +150C
- Grade 1 (or Q): -40C to +125C
- Grade 2 (or T): -40C to +105C
- Grade 3 (or I): -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

- Room/Hot/Cold : HTOL, ED
- Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU
- Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

TI Qualification ID: R-CHG-2211-017

ZVEI ID: SEM-BD-01, SEM-PA-07, SEM-PA-08, SEM-PA-11, SEM-PA-18, SEM-TF-01

For questions regarding this notice, e-mails can be sent to Change Management team or your local Field Sales Representative.

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