

PLETRONICS 0815005-10.0M OGXO Oscillator

Features

- Pletronics' OCXO Series Ovenized Quartz Crystal High Precision Oscillator
- LVTTL Output
- 5.0V nominal Supply Voltage
- 10.0MHz Nominal Frequency

Applications

SONET / SDH / DWDM Test & Measurement Telecom Transmission & Switching Equipment Base Stations / Picocell Wireless Communication Equipment

OSI5 Series
36.3 x 27.2 x 12.7 mm
5 Pin Metal Package

Electrical Characteristics					
Parameter	Min	Тур	Мах	Unit	Condition
Frequency	-	10	-	MHz	
Initial Calibration			±0.1	ppm	After turn on 15 ± 1 minutes @25°C±1, ≤90 days after date code, Vcontrol = 2.5V ± 0.001V
Frequency Stability vs Temperature	-	-	±3	ppb	-40 to +85°C
Frequency Stability vs Supply	-	-	±0.5	ppb	±5% voltage change
Frequency Stability vs Load	-	-	±0.5	ppb	±5% load change
Warm-up	-	-	+10	ppb	In 10 minutes @ +25°C, referenced to 1 hour
Short Term	-	-	0.05	ppb/g	root Allan variance
	-	-	±0.5	ppb	per day at time of shipment
Aging	-	-	±0.5	ppb	Per day, after 30 days
Aging	-	±50 ppb per year		ppb	per year
	-	±0.3 ppm 10 years		10 years	
Operating Temperature Range	-40	-	+85	°C	Ref to 25°C
Supply Voltage ¹ V _{CC}	4.75	5	5.25	V	
Current	-	-	800	mA	@turn on
Steady State	-	-	1.3	W	@ 25°C
Pullability	±0.5	-	-	ppm	
Control Voltage Vc	0	2.5	5	V	
Linearity	-	-	±10	%	
Input Impedance Vc pin	100	-	-	kΩ	
Phase Noise 1 Hz 10 Hz 100 Hz 100 Hz 1 kHz 10 kHz 100 kHz	-	-95 -125 -140 -148 -156 -168	-90 -120 -135 -145 -155 -155	dBc/Hz	
Storage Temperature Range	-55	-	+105	°C	

Output						
Parameter		Min	Тур	Max	Unit	Condition
Output Waveform			L	VTTL		
Level	Voh Vol	2.6 -	3.3 -	- 0.4	V	
Load		-	15	-	pF	
Duty Cycle		45	-	55	%	@ 1.65V
Rise/Fall Time		-	-	6	ns	10%~90%Vcc
Spurious		-	-	-60	dBc	

Note: ¹ Place a 10nF power supply bypass capacitor next to device for correct operation

Product information is current as of publication date. The product conforms to specifications per the terms of the Pletronics standard warranty. Aug 17, 2022 Rev. A Production processing does not necessarily include testing of all parameters. Copyright © 2022, Pletronics Inc. • 19013 36th Ave. W, Lynnwood, WA 98036 USA • www.pletronics.com • 425-776-1880 Pg 1



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Device Marking

PLE OSI5005 100.0M <i>YMDz</i> S/N: xxx	PLE OSI5005 10.0M YMD z S/N: xxx	 Pletronics Model number/Part number* Frequency (M = MHz) Date code (Year-Month-Day: See Table below) Internal Factory Code Serial number
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* A unique number is assigned for your exact specifications.

Specifications such as part number, frequency stability, supply voltage and operating temperature range, etc. are not identified from marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD (Year Month Day)

Code		2		3		4	ŀ	5	5	6		Co	de	Α		в	C	;	D		E	F	G		Н	J		Κ	L		М
Year	2	2022		202	3	202	24	202	25	202	26	Mor	nth	JAN	N F	ΈB	MA	٨R	APR	M	IAY	JUN	JU	L	AUG	SE	P	ОСТ	NO	V I	DEC
Code	1	2	3	4	5	6	7	8	9	Α	в	С	D	Е	F	G	н	J	к	L	М	Ν	Ρ	R	т	U	v	w	X	Y	z
Day	1	2	3	Δ	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31

Package Labeling

P/N Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII RoHs Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial



RoHS Compliant

2nd LvL Interconnect Category=e3

Max Safe Temp=280C for 15s (Wave solder only)

Pletronics Inc. certifies this device is in accordance with the RoHS (by exemption) and REACH directives. Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Mercury, PBB's, PBDE's Moisture Sensitivity Level: 1 As defined in J-STD-020D Second Level Interconnect code: e3

Environmental

Reliability: Environmental

Parameter	Ref Standard	Condition
Humidity	MIL-STD-202, Method 103, Test Condition A	95% RH@ +40°C, non-condensing, 240 hours
Mechanical Shock (non-operating)	MIL-STD-202, Method 213 Test Cond J	30g, 11ms, half-sine
Vibration (nonoperating)	MIL-STD-202, Method 201	0.06" Total p-p, 10 to 55 Hz



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PIN

1

2

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4 5

0

PIN CONNECTIONS

FUNCTION

+VDC

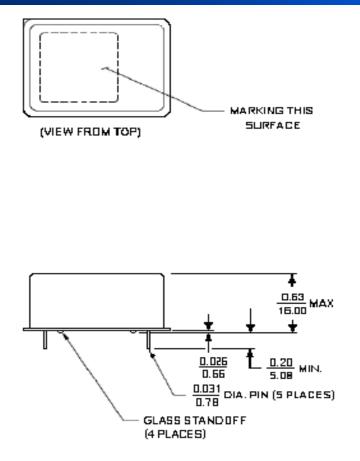
R.F. Output

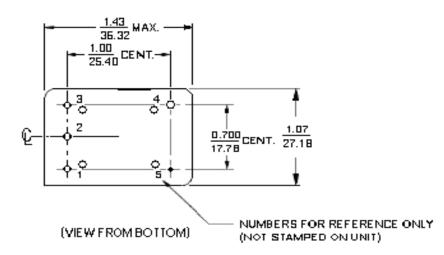
Volts and Case

VC IN

Not Connected

Mechanical Dimensions





For Optimum Jitter Performance, Pletronics recommends:

- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply
- Do not place near piezoelectric buzzers or mechanical fans



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Important Notice

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