

2N7638-GA

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650 V

1.4 V

180 mΩ

8 A

Normally – OFF Silicon Carbide Junction Transistor

Features

- 250 °C maximum operating temperature
- Temperature independent switching performance
- Gate oxide free SiC switch
- Suitable for connecting an anti-parallel diode
- · Positive temperature coefficient for easy paralleling
- Low gate charge

Advantages

• Low switching losses

• Higher efficiency

High temperature operation

· High short circuit withstand capability

· Low intrinsic capacitance

Package RoHS Compliant





SMD0.5 / TO – 276 (Hermetic Package)

Applications

• Down Hole Oil Drilling, Geothermal Instrumentation

V_{DS}

V_{DS(ON)}

R_{DS(ON)}

- Hybrid Electric Vehicles (HEV)
- Solar Inverters
- Switched-Mode Power Supply (SMPS)
- Power Factor Correction (PFC)
- Induction Heating
- Uninterruptible Power Supply (UPS)
- Motor Drives

Maximum Ratings at T_i = 250 °C, unless otherwise specified

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Parameter	Symbol	Conditions	Values	Unit
Drain – Source Voltage	V _{DS}	$V_{GS} = 0 V$	650	V
Continuous Drain Current	I _D	T _C = 158 °C	8	А
Gate Peak Current	I _{GM}		5	А
Reverse Gate – Source Voltage	V _{GS}		30	V
Reverse Drain – Source Voltage	V _{DS}		40	V
Power Dissipation	P _{tot}	T _C = 25 °C	11	W
Operating and Storage Temperature	T _j , T _{stg}		-55 to 250	°C

Electrical Characteristics at T_i = 250 °C, unless otherwise specified

Parameter	Sympol	Conditions	Values		11	
Parameter	Symbol	Conditions	min.	typ.	max.	Unit
On Characteristics						
		I_D = 8 A, I_G = 250 mA, T_j = 25 °C		1.4	2.1	
Drain – Source On Voltage	V _{DS(ON)}	I _D = 8 A, I _G = 500 mA, T _j = 175 °C		2.6	3.7	V
		I _D = 8 A, I _G = 500 mA, T _j = 250 °C		3.9	4.8	
		I _D = 8 A, I _G = 250 mA, T _j = 25 °C		180		
Drain – Source On Resistance	$R_{DS(ON)}$	I _D = 8 A, I _G = 500 mA, T _j = 175 °C		330		mΩ
		I _D = 8 A, I _G = 500 mA, T _j = 250 °C		490		
Gate Forward Voltage	$V_{GS(FWD)}$	I _G = 500 mA, T _j = 25 °C		3		V
		I _G = 500 mA, T _j = 250 °C		2.7		v
DC Current Gain	0	V _{DS} = 5 V, I _D = 10 A, T _j = 25 °C	80	110		
	β	V _{DS} = 5 V, I _D = 10 A, T _i = 250 °C	50	80		

Off Characteristics

		V _R = 650 V, V _{GS} = 0 V, T _j = 25 °C	10	100	
Drain Leakage Current	I _{DSS}	V _R = 650 V, V _{GS} = 0 V, T _j = 175 °C	40	400	μA
		V _R = 650 V, V _{GS} = 0 V, T _j = 250 °C	100	600	

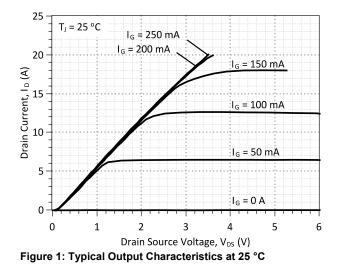


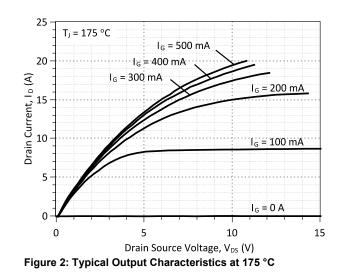
2N7638-GA

Electrical Characteristics at T_j = 250 °C, unless otherwise specified

Parameter	Symphol	Conditions	Values		11		
Parameter	Symbol	Conditions	min.	typ.	max.	Unit	
Dynamic Characteristics							
Input Capacitance	C _{iss}	<u> </u>		720		pF	
Output Capacitance	C _{oss}	V _{DS} = 35 V, V _{GS} = 0 V, f = 1 MHz, T _{vi} = 25 °C		88		pF	
Reverse Transfer Capacitance	C _{rss}	$1 - 1 \text{ Will} 2, 1_{\text{VJ}} - 23 \text{ C}$		88		pF	
Switching Characteristics							
Turn On Delay Time	t _{d(on)}			11		ns	
Rise Time	tr	$V_{DD} = 400 \text{ V}, I_D = 10 \text{ A},$		28		ns	
Turn Off Delay Time	t _{d(off)}	$R_{G(on)} = R_{G(off)} = 32 \Omega,$		76		ns	
Fall Time	t _f	V _{GS} = -8/15 V ,T _i = 175 °C		38		ns	
Turn-On Energy Per Pulse	Eon	Refer to Figure 10 for gate drive current waveforms		34		μJ	
Turn-Off Energy Per Pulse	E _{off}			64		μJ	
Total Switching Energy	E _{ts}			98		μJ	
Turn On Delay Time	t _{d(on)}			12		ns	
Rise Time	t _r	$V_{DD} = 400 \text{ V}, I_D = 10 \text{ A},$		30		ns	
Turn Off Delay Time	t _{d(off)}	$ \begin{array}{c} v_{DD} = 400 \ V, \ b = 10 \ A, \\ R_{G(on)} = R_{G(of)} = 32 \ \Omega, \\ V_{GS} = -8/15 \ V, \ T_i = 250 \ ^{\circ}\text{C} \\ \text{Refer to Figure 10 for gate drive} \\ \text{current waveforms} \end{array} $		73		ns	
Fall Time	t _f			58		ns	
Turn-On Energy Per Pulse	Eon			43		μJ	
Turn-Off Energy Per Pulse	E _{off}			82		μJ	
Total Switching Energy	E _{ts}]		125		μJ	

Thermal resistance, junction - case	R _{thJC}	1	°C/W





GeneSic SEMICONDUCTOR

2N7638-GA

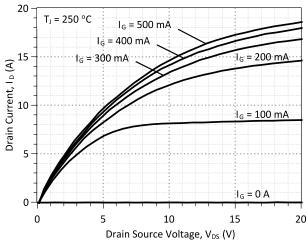


Figure 3: Typical Output Characteristics at 250 °C

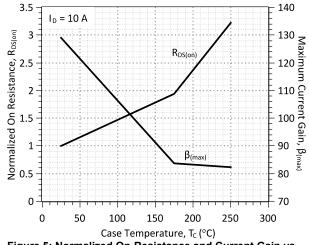


Figure 5: Normalized On-Resistance and Current Gain vs. Temperature

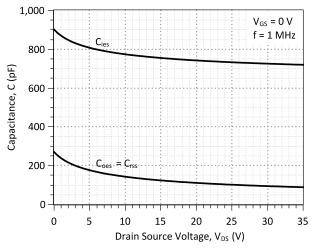


Figure 7: Typical Capacitance vs Drain-Source Voltage

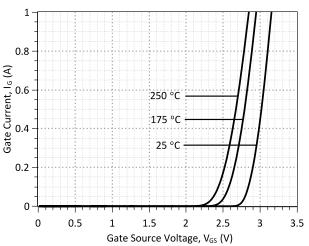


Figure 4: Typical Gate Source I-V Characteristics vs. Temperature

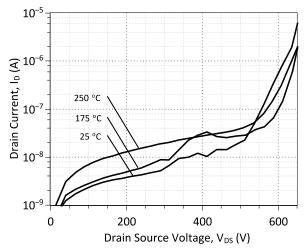
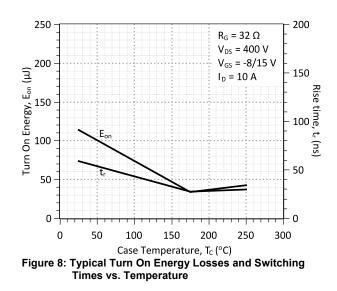
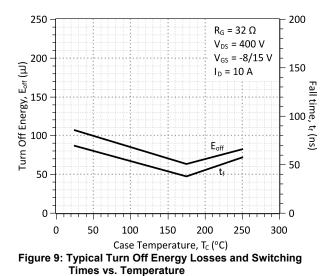


Figure 6: Typical Blocking Characteristics





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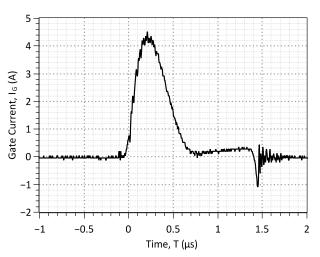
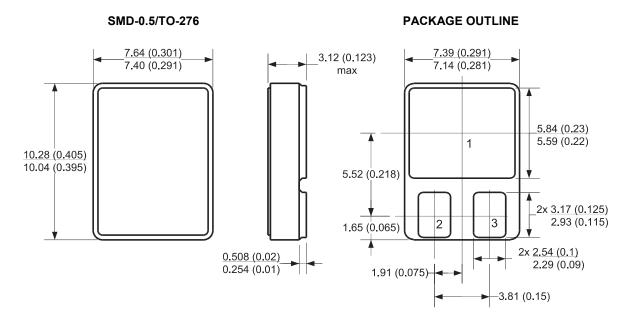


Figure 10: Typical Gate-Source Switching Waveforms

Package Dimensions:



NOTE

CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
 DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS



Revision History					
Date	Revision	Comments	Supersedes		
2013/11/18	1	Updated Electrical Characteristics			
2012/08/24	0	Initial release			

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SPICE Model Parameters

Copy the following code into a SPICE software program for simulation of the 2N7638-GA device.

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*
     MODEL OF GeneSiC Semiconductor Inc.
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     $Revision: 1.0
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*
     $Date: 06-SEP-2013
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*
    GeneSiC Semiconductor Inc.
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*
* These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY
* OF ANY KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED
* TO ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A
* PARTICULAR PURPOSE."
* Models accurate up to 2 times rated drain current.
.model 2N7638 NPN
+ IS
      3.73E-46
+ ISE
          5.50E-28
+ EG
          3.2
+ BF
         103
+ BR
         0.55
         900
+ IKF
+ NF
         1
         2.021
+ NE
+ RB
         0.26
+ RE
         0.1
+ RC
         0.09
         2.77E-10
+ CJC
+ VJC
         3.023103628
+ MJC
          0.460762158
+ CJE
         8.23E-10
+ VJE
         2.945448229
        0.498044294
+ MJE
+ XTI
         3
          -0.35
+ XTB
          1.20E-02
+ TRC1
+ VCEO 650
+ ICRATING 8
+ MFG GeneSiC Semiconductor
* End of 2N7638-GA SPICE Model
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