

# Product/Process Change Notice - PCN 13\_0314 Rev. -

Analog Devices, Inc. Three Technology Way Norwood, Massachusetts 02062-9106

This notice is to inform you of a change that will be made to certain ADI products (see Material Report). Any issues with this PCN or requirements to qualify the change (additional data or samples) must be sent to ADI within 30 days of publication date. ADI contact information is listed below.

PCN Title: ADP5050/ADP5052 Die Revision

Publication Date: 06-Jan-2014

Effectivity Date: 06-Jan-2014 (the earliest date that a customer could expect to receive changed material)

#### **Revision Description:**

Initial Release

### **Description Of Change**

All layer mask change in the SYNC/MODE circuitry.

#### Reason For Change

To improve the functionality when SYNC/MODE (pin 43) is configured as a synchronization output (SYNC/MODE Option 1). This circuitry is not active when the device is operated under the default condition (SYNC/MODE Option 0).

## Impact of the change (positive or negative) on fit, form, function & reliability

No impact on form, fit, function, or reliability.

Product Identification (this section will describe how to identify the changed material)

ADP5050 Date code 1345 ADP5052 Date code 1346

# **Summary of Supporting Information**

Qualification has been performed per ADI0012, Procedure for Qualification of New or Revised Processes. See attached Qualification Report Summary.

#### **Supporting Documents**

Attachment 1: Type: Qualification Report Summary

ADI\_PCN\_13\_0314\_Rev\_-\_ADP5050 Die Revision Qual Results.docx

Rest of Asia: PCN\_ROA@analog.com

Appendix A - Affected ADI Models					
Added Parts On This Revision - Product Family / Model Number (2)					
ADP5050 / ADP5050ACPZ-R7	ADP5062 / ADP5062ACPZ-R7				

Appendix B - Revision History					
Rev	Publish Date	Effectivity Date	Rev Description		
Rev	06-Jan-2014	06-Jan-2014	Initial Release		

Analog Devices, Inc.

Docld:2676 Parent Docld:None Layout Rev:7