

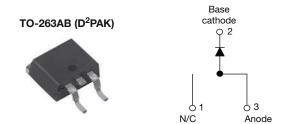
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Vishay Semiconductors

RoHS

COMPLIANT

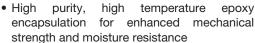
## High Performance Schottky Rectifier, 6 A

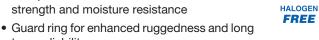


PRODUCT SUMMARY							
Package	TO-263AB (D <sup>2</sup> PAK)						
$I_{F(AV)}$	6 A						
$V_{R}$	35 V, 40 V, 45 V						
V <sub>F</sub> at I <sub>F</sub>	0.53 V						
I <sub>RM</sub>	7 mA at 125 °C						
$T_{J}$ max.	175 °C						
Diode variation	Single die						
E <sub>AS</sub>	8 mJ						

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- High frequency operation
- Low forward voltage drop





- term reliability
  Meets MSL level 1, per J-STD-020, LF maximum peak
- Meets MSL level 1, per J-S1D-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

The VS-6TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL CHARACTERISTICS VALUES									
I <sub>F(AV)</sub>	Rectangular waveform	6	А						
V <sub>RRM</sub>	Range	35 to 45	V						
I <sub>FSM</sub>	t <sub>p</sub> = 5 µs sine	690	А						
V <sub>F</sub>	6 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.53	V						
T <sub>J</sub>	Range	-55 to +175	°C						

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS-6TQ035SPbF	VS-6TQ040SPbF	VS-6TQ045SPbF	UNITS				
Maximum DC reverse voltage	V <sub>R</sub>	35	40	45	V				
Maximum working peak reverse voltage	V <sub>RWM</sub>	35	40	45	V				

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS					
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 164 °C	6					
Maximum peak one cycle non-repetitive surge current	l=a	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	690	Α			
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	140				
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1.20 A, L = 11.10 mH		8	mJ			
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5 \times V_R$ typical		1.20	А			



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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS			
Maximum forward voltage drop See fig. 1		6 A	T <sub>.1</sub> = 25 °C	0.60			
	V <sub>FM</sub> <sup>(1)</sup>	12 A	11=25 0	0.73	v		
	V <sub>FM</sub> (··)	6 A	T 105 %C	0.53			
		12 A	- T <sub>J</sub> = 125 °C	0.64			
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	0.8	- mA		
See fig. 2		T <sub>J</sub> = 125 °C	$v_R = Rateu v_R$	7			
Threshold voltage	V <sub>F(TO)</sub>	T T maying um					
Forward slope resistance	r <sub>t</sub>	ı j = ıj maximum	$T_J = T_J$ maximum		mΩ		
Maximum junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal rang	400	pF			
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mi	8.0	nH			
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs			

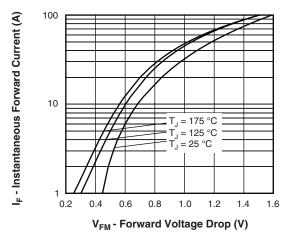
#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width  $<300~\mu s,$  duty cycle <2~%

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	PARAMETER		TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C			
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation See fig. 4		°C/W			
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased		O/ VV			
Approximate weight	A			2	g			
Approximate weight				0.07	OZ.			
Mounting torque	minimum			6 (5)	kgf · cm			
Mounting torque maximum				12 (10)	(lbf · in)			
Marking device				6TQ035S				
			Case style D <sup>2</sup> PAK	6TQ040S				
				6TQ04	15S			

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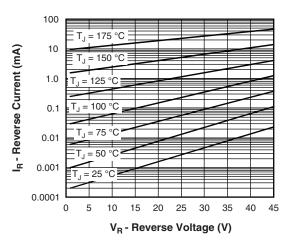


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

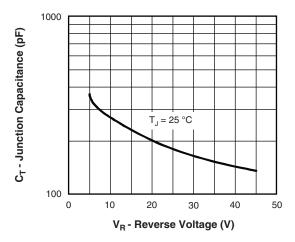


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

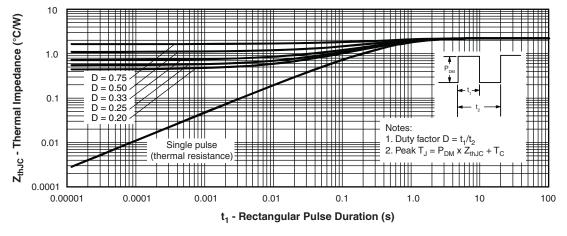


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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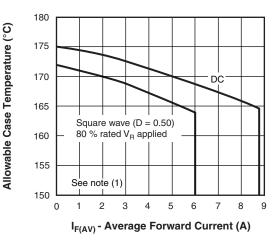


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

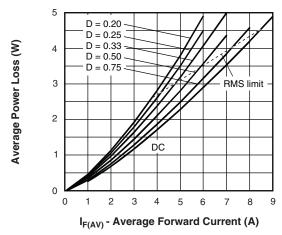


Fig. 6 - Forward Power Loss Characteristics

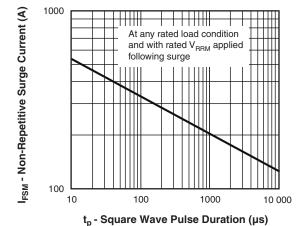


Fig. 7 - Maximum Non-Repetitive Surge Current

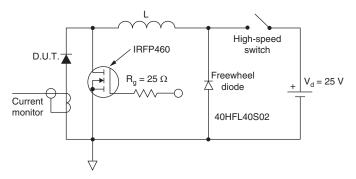


Fig. 8 - Unclamped Inductive Test Circuit

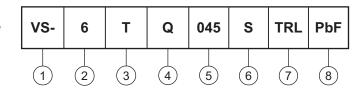
### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \\ \end{array}$ 

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#### **ORDERING INFORMATION TABLE**

**Device code** 



Vishay Semiconductors product

Current rating (6 A)

3 Package: T = TO-220

Schottky "Q" series

035 = 35 V 040 = 40 VVoltage ratings 045 = 45 V

 $S = D^2PAK$ 

• None = tube (50 pieces)

• TRL = tape and reel (left oriented)

• TRR = tape and reel (right oriented)

8 PbF = lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-6TQ035SPBF	50	1000	Antistatic plastic tubes						
VS-6TQ035STRRPBF	800	800	13" diameter plastic tape and reel						
VS-6TQ035STRLPBF	800	800	13" diameter plastic tape and reel						
VS-6TQ040SPBF	50	1000	Antistatic plastic tubes						
VS-6TQ040STRRPBF	800	800	13" diameter plastic tape and reel						
VS-6TQ040STRLPBF	800	800	13" diameter plastic tape and reel						
VS-6TQ045SPBF	50	1000	Antistatic plastic tubes						
VS-6TQ045STRRPBF	800	800	13" diameter plastic tape and reel						
VS-6TQ045STRLPBF	800	800	13" diameter plastic tape and reel						

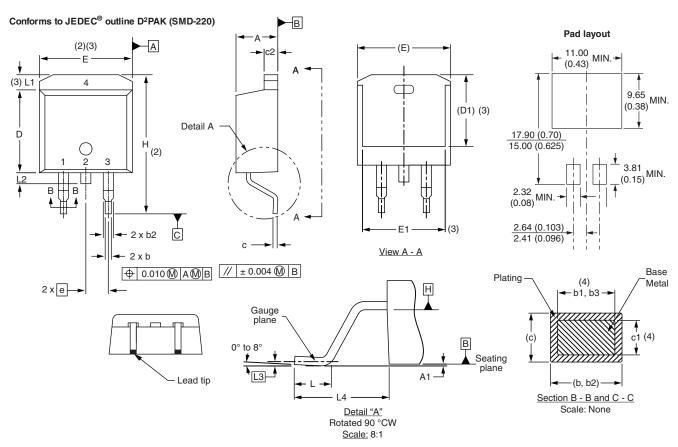
LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95046				
Part marking information	www.vishay.com/doc?95054				
Packaging information	www.vishay.com/doc?95032				



### Vishay Semiconductors

### D<sup>2</sup>PAK

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		ES NOTES		SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB



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