

Features

- ESD/Surge Protection for 1 Line with Bidirectional.
- Provide ESD protection for each line to IEC 61000-4-2 (ESD) ±18kV (air / contact) IEC 61000-4-4 (EFT) 40A (5/50ns) IEC 61000-4-5 (Lightning) 5A (8/20µs)
- Suitable for, 24V and below, operating voltage applications
- Low capacitance : 1.8pF typical
- High surge protection
- Fast turn-on and low clamping voltage
- Solid-state silicon-avalanche and active circuit triggering technology
- Green Part

Applications

- xDSL Line Protection
- WAN/LAN Device
- 10/100/1000 Ethernet
- Power Supply Protection
- USB Interface Protection
- RF Interface Protection
- Peripherals

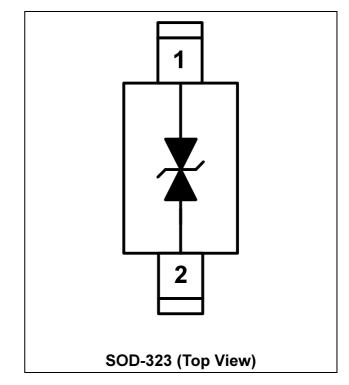
Description

AZ4624-01L is a design which includes a bi-directional ESD rated clamping cell to protect one power line, or one control line, or one high speed data line in an electronic system. The AZ4624-01L has been specifically designed to protect sensitive components which are connected to data and transmission lines from over-voltage caused by Electrostatic Discharging (ESD), Electrical Fast Transients (EFT), Lightning, and Cable Discharge Event (CDE).

AZ4624-01L is a unique design which includes proprietary clamping cell in a single package. During transient conditions, the proprietary clamping cell prevents over-voltage on the power line or control/data lines, protecting any downstream components.

AZ4624-01L may be used to meet the ESD immunity requirements of IEC 61000-4-2, Level 4 (±15kV air, ±8kV contact discharge).

Circuit Diagram / Pin Configuration



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SPECIFICATIONS

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	RATING	UNITS	
Peak Pulse Current (tp=8/20µs)	I _{PP}	5	А	
Operating Supply Voltage	V _{DC}	±26.4	V	
ESD per IEC 61000-4-2 (Air)	V_{ESD}	±18	kV	
ESD per IEC 61000-4-2 (Contact)		±18		
Lead Soldering Temperature	T _{SOL}	260 (10 sec.)	℃	
Operating Temperature	T _{OP}	-55 to +125	℃	
Storage Temperature	T _{STO}	-55 to +150	℃	

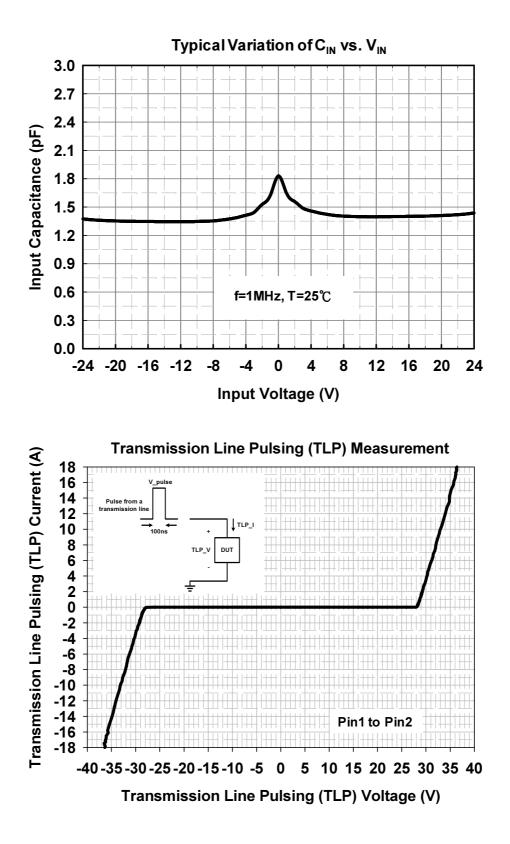
ELECTRICAL CHARACTERISTICS						
PARAMETER	SYMBOL CONDITIONS		MINI	TYP	MAX	UNITS
Reverse Stand-Off Voltage	V _{RWM}	T=25 ℃	-24		24	V
Reverse Leakage Current	I _{Leak}	V _{RWM} = ±24V, T=25 ℃			0.5	μΑ
Reverse Breakdown Voltage	V _{BV}	I _{BV} = 1mA, T=25 ℃	26.5		30	V
Surge Clamping Voltage	V _{CL-surge}	I _{PP} = 5A, tp=8/20μs, T=25 ℃		31		V
ESD Clamping Voltage (Note 1)	V_{clamp}	IEC 61000-4-2, +8kV (I _{TLP} = 16A), Contact mode, T=25 °C		36		V
ESD Dynamic Turn-on Resistance	R _{dynamic}	IEC 61000-4-2, 0~+8kV, T=25 °C, Contact mode		0.45		Ω
Channel Input Capacitance	C _{IN}	V _R = 0V, f = 1MHz, T=25 ℃		1.8	2.5	pF

Note 1: ESD Clamping Voltage was measured by Transmission Line Pulsing (TLP) System.

TLP conditions: Z_0 = 50 Ω , t_p = 100ns, t_r = 1ns.



Typical Characteristics





Applications Information

The AZ4624-01L is designed to protect one line against System ESD/EFT/Lightning pulses by clamping it to an acceptable reference. It provides bidirectional protection.

The usage of the AZ4624-01L is shown in Fig. 1. Protected line, such as data line, control line, or power line, is connected at pin 1. The pin 2 is connected to a ground plane on the board. In order to minimize parasitic inductance in the board traces, all path lengths connected to the pins of AZ4624-01L should be kept as short as possible. In order to obtain enough suppression of ESD induced transient, good circuit board is critical. Thus, the following guidelines are recommended:

- Minimize the path length between the protected lines and the AZ4624-01L.
- Place the AZ4624-01L near the input terminals or connectors to restrict transient coupling.
- The ESD current return path to ground should be kept as short as possible.
- Use ground planes whenever possible.
- NEVER route critical signals near board edges and near the lines which the ESD transient easily injects to.

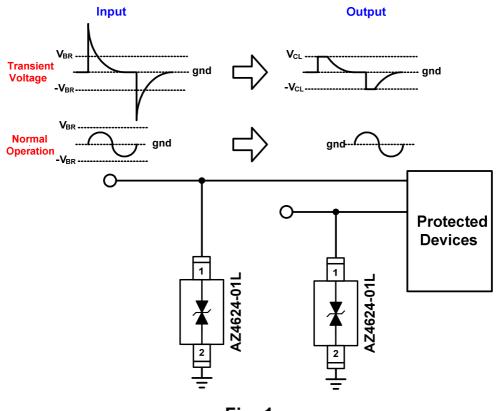
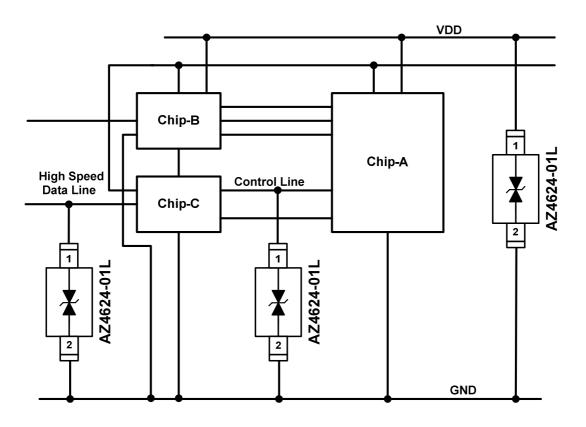






Fig. 2 shows another simplified example of using AZ4624-01L to protect the control line, high

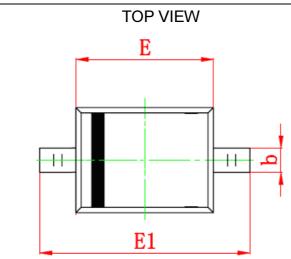
speed data line, and power line from ESD transient stress.



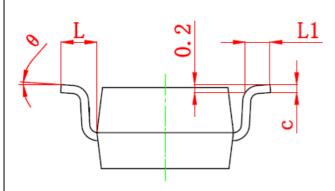




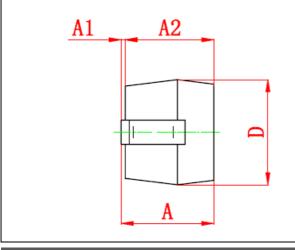
Mechanical Details SOD-323 PACKAGE DIAGRAMS



SIDE VIEW



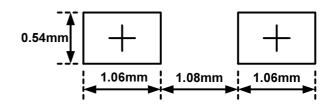
END VIEW



PACKAGE DIMENSIONS

Symbol	Millim	neters	Inches		
	MIN.	MAX.	MIN.	MAX.	
Α	0.8	1.0	0.031	0.039	
A1	0	0.1	0.000	0.004	
A2	0.8	0.9	0.031	0.035	
b	0.25	0.35	0.010	0.014	
С	0.08	0.15	0.003	0.006	
D	1.2	1.4	0.047	0.055	
E	1.6	1.8	0.063	0.071	
E1	2.5	2.7	0.098	0.106	
L	0.475REF		0.019 REF		
L1	0.25	0.4	0.010	0.016	
θ	0°	8°	0°	8°	

LAND LAYOUT

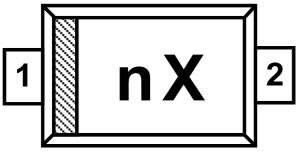


Notes:

This LAND LAYOUT is for reference purposes only. Please consult your manufacturing partners to ensure your company's PCB design guidelines are met.



MARKING CODE



Top View

n = Device Code X = Date Code

Part Number	Marking Code
AZ4624-01L.R7G	nX
(Green Part)	ПХ

Note. Green means Pb-free, RoHS, and Halogen free compliant.

Ordering Information

PN#	Material	Туре	Reel size	MOQ	MOQ/internal box	MOQ/carton
AZ4624-01L.R7G	Green	T/R	7 inch	3,000/reel	4 reels= 12,000/box	6 boxes =72,000/carton

Revision History

Revision	Modification Description		
Revision 2015/12/07	Preliminary Release.		
Revision 2017/05/11	Formal Release.		