

Micro Commercial Components



Micro Commercial Components 20736 Marilla Street Chatsworth CA 91311 Phone: (818) 701-4933 Fax: (818) 701-4939

Features

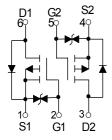
- Halogen free available upon request by adding suffix "-HF"
- Lead Free Finish/RoHS Compliant ("P" Suffix designates RoHS Compliant. See ordering information)
- P-Channel Swith with Low R_{DS(ON)}
- ESD Protected Gate
- Epoxy meets UL 94 V-0 flammability rating
- Moisure Sensitivity Level 1

Marking:3152

Maximum Ratings @ 25°C Unless Otherwise Specified

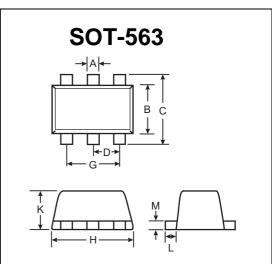
Symbol	Rating	Rating	Unit	
N-MOSF	ĒT		•	
V _{DS}	Drain-Source Voltage -20 V			
V _{GS}	Gate-Source Voltage	±12 V		
I _D	Continuous Drain Current (note1)	-0.66 A		
I _{DM}	Pulsed Drain Current(tp=10us)	-1.0	A	
PD	Power Dissipation (note2)	150	mW	
R_{thJA}	Thermal Resistance from Junction to Ambient(note1)	833 ^o C/W		
ТJ	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	-55~150	°C	
ΤL	Lead Temperature for Soldering Purposes (1/8" from case for 10s)	260	°C	

Equivalent Circuit



MCX3152P

Dual P-Channel MOSFET



	DIMENSIONS				
	INCHES		ММ		
DIM	MIN	MAX	MIN	MAX	NOTE
A	.006	.011	0.15	0.30	
В	.043	.049	1.10	1.25	
С	.061	.067	1.55	1.70	
D	.020		0.50		
G	.035	.043	0.90	1.10	
Н	.059	.067	1.50	1.70	
K	.022	.023	0.56	0.60	
L	.004	.011	0.10	0.30	
М	.004	.007	0.10	0.18	

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MCX3152P



MOSFET ELECTRICAL CHARACTERISTICS(T_a=25℃ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
STATIC PARAMETERS	•	•				
Drain-source breakdown voltage	V(BR)DSS	Vgs = 0V, Id =-250µA	-20			V
Zero gate voltage drain current	IDSS	V _{DS} =-16V,V _{GS} = 0V			-1	μA
Gate-body leakage current	lgss	Vgs =±4.5V, Vds = 0V			±2	uA
Gate threshold voltage (note 2)	VGS(th)	V _{DS} =V _{GS} , I _D =-250µA	-0.4	-0.45	-1	V
Drein aguras en registance/nete 2)	RDS(on)	Vgs =-4.5V, Id =-0.66A		0.45	0.7	Ω
Drain-source on-resistance(note 2)		Vgs =-2.5V, Id =-0.5A		0.55	1	Ω
		Vgs =-1.8V, Id =-0.4A		0.72	1.2	Ω
Forward tranconductance(note 2)	g fs	VDS =-10V, ID =-0.43A		1		S
Diode forward voltage	V _{SD}	I _S =-0.35A, V _{GS} = 0V			-1.2	V
DYNAMIC PARAMETERS(note 4)						
Input Capacitance	C _{iss}			105	175	pF
Output Capacitance	C _{oss}	V _{DS} =-16V,V _{GS} =0V,f =1MHz		15	30	pF
Reverse Transfer Capacitance	C _{rss}			10	20	pF
SWITCHING PARAMETERS (note 4)	•	•				
Turn-on delay time (note 3)	td(on)			10		ns
Turn-on rise time (note 3)	tr	V _{DD} =-4.5V,V _{GS} =-10V,		12		ns
Turn-off delay time (note 3)	td(off)	I_D =-215mA,R _{GEN} =10 Ω		35		ns
Turn-off fall time (note 3)	tr			19		ns
Total Gate Charge(note 3)	Qg			1.7	2.5	nC
Gate Source Charge(note 3)	Q _{gs}	V _{DS} =-10V,V _{GS} =-4.5V,I _D =-215mA		0.3		nC
Gate Drain Charge(note 3)	Q _{gd}			0.4		nC

Notes :

1.Surface mounted on FR4 board using the minimum recommended pad size.

2. Pulse Test : Pulse width=300µs, duty cycle≤2%.

3. Switching characteristics are independent of operating junction temperatures.

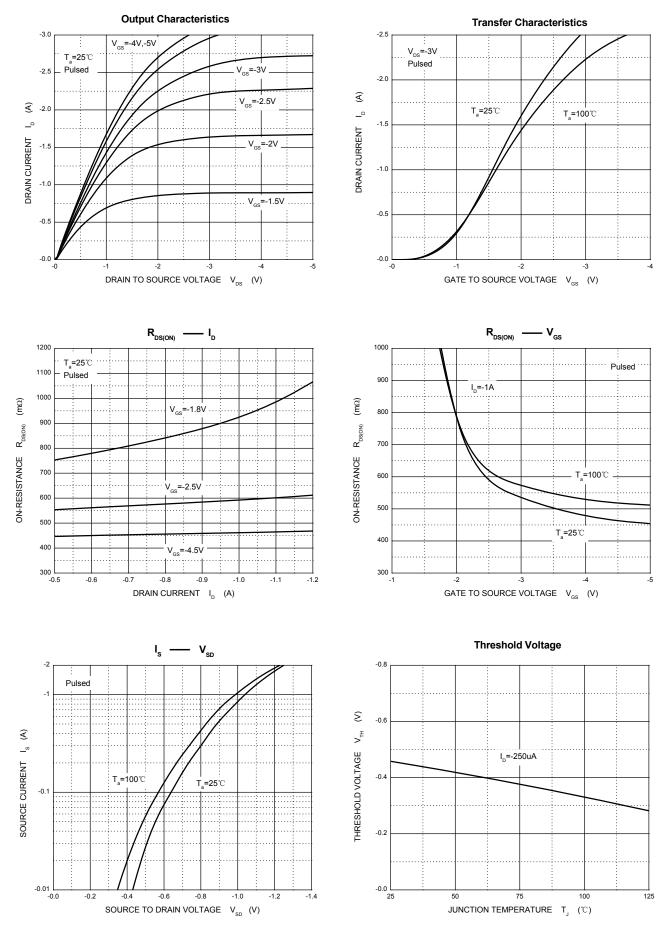
4. Graranted by design, not subject to producting.





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Typical Characteristics



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Ordering Information :

Device	Packing
Part Number-TP	Tape&Reel 3Kpcs/Reel

Note : Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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