



30V N-CHANNEL ENHANCEMENT MODE MOSFET IN SOT23

Product Summary

BV _{DSS}	Max R _{DS(ON)}	I D Max (Note 5) T _A = +25°C
30V	$460m\Omega @ V_{GS} = 4.5V$	0.94A
300	$560m\Omega @ V_{GS} = 2.5V$	0.85A

Description and Applications

This MOSFET has been designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Load switches
- Portable applications
- Power management functions

Features and Benefits

- Low VGS(TH), Can be Driven Directly From a Battery
- Low RDS(ON)
- ESD Protected Gate 2kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

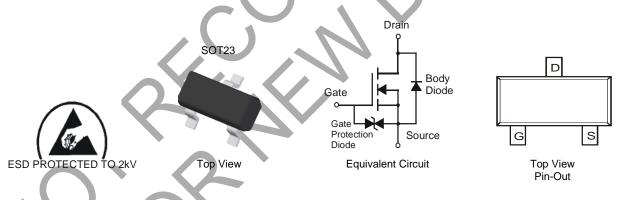
https://www.diodes.com/products/automotive/automotiveproducts/.

This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/guality/product-definitions/

Mechanical Data

- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound.
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish-Matte Tin. (2)
- Weight: 0.08 grams (Approximate)



Ordering Information (Note 4)

Part Number	art Number Package Marking Reel Size (inches) Tape Width (mm)					Packing				
Fait Number	t Number Package		Marking Reel Size (inches)		Qty.	Carrier				
DMN3730U-7	SOT23	N3U	7	8	3,000	Reel				

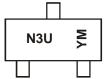
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. Notes: 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and

Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



N3U = Product Type Marking Code YM = Date Code Marking Y = Year (ex: J = 2022) M = Month (ex: 9 = September)

Date	Code	Key

Date Code Key												
Year	2011		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	Y		J	К	L	М	Ν	0	Р	R	S	Т
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Carla	1	2	2	1	5	6	7	8	9	0	N	П
Code	1	2	5	4	5	0	'	0	5	0	IN	

Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Cha	racteristic		Symbol	Value	Unit
Drain-Source Voltage			VDSS	30	V
Gate-Source Voltage			Vgss	±8	V
Continuous Drain Current	Steady State	$T_A = +25^{\circ}C$ (Note 6) $T_A = +85^{\circ}C$ (Note 6) $T_A = +25^{\circ}C$ (Note 5)	Ib	0.94 0.68 0.75	A
Pulsed Drain Current (Note 7)			Ідм	10	А

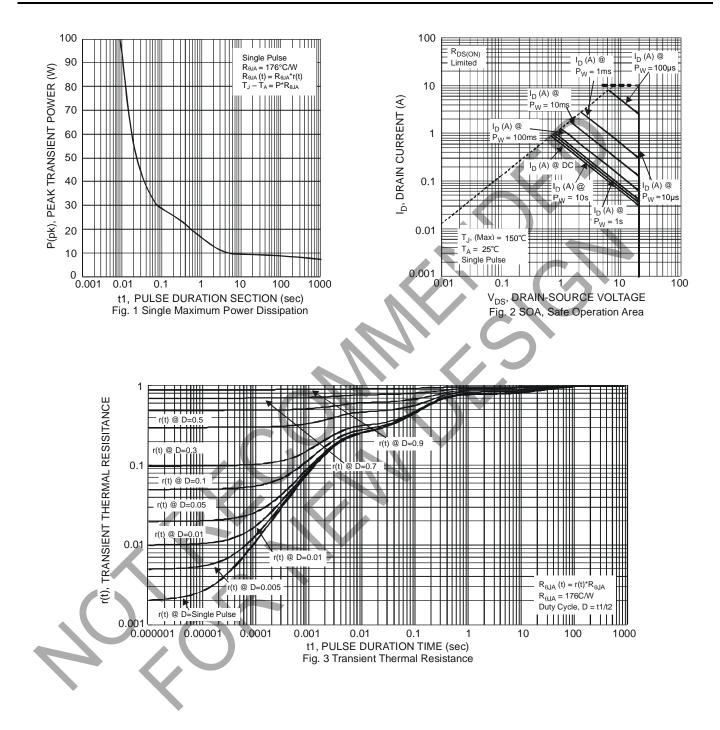
Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Power Dissipation	(Note 5)	P	0.45	W
	(Note 6)	PD	0.71	W
Thermel Desistance, Junction to Ambient	(Note 5)		275	°C/W
Thermal Resistance, Junction to Ambient	(Note 6)	Reja	177	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on 25mm x 25mm square copper plate with FR-4 substrate PC board, 2oz copper.
Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%. Notes:



Thermal Characteristics



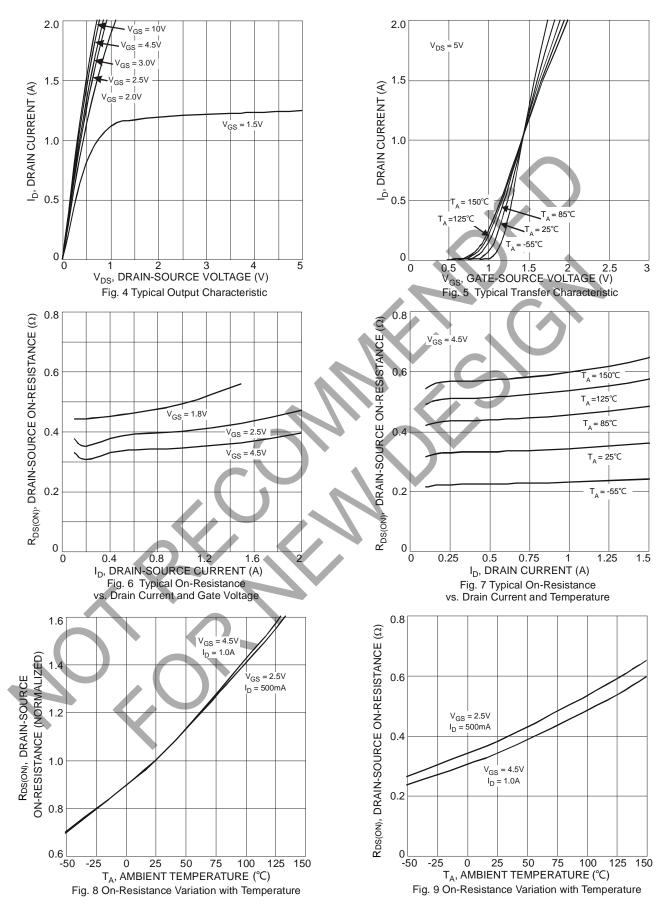


Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

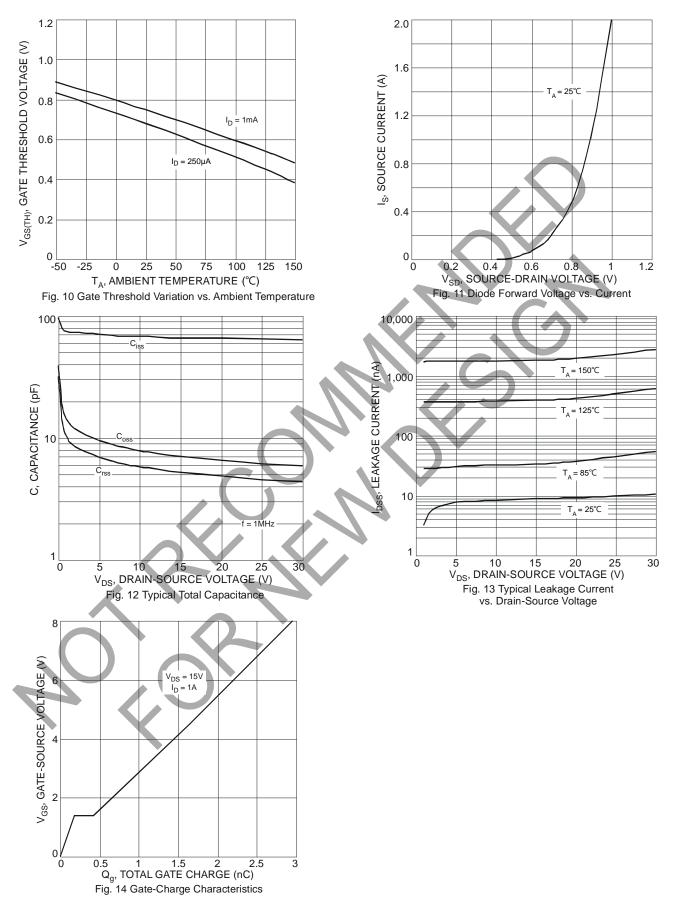
			-			
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS		1	r			
Drain-Source Breakdown Voltage	BVDSS	30	—	_	V	$V_{GS} = 0V, I_{D} = 10\mu A$
Zero Gate Voltage Drain Current	IDSS			1	μA	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	IGSS			3	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	0.45	—	0.95	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
				460		V _{GS} = 4.5V, I _D = 200mA
Static Drain-Source On-Resistance (Note 8)	R _{DS(ON)}	—		560	mΩ	$V_{GS} = 2.5V, I_D = 100mA$
				730		$V_{GS} = 1.8V, I_D = 75mA$
Forward Transfer Admittance	Yfs	40		_	mS	$V_{DS} = 3V$, $I_D = 10mA$
Diode Forward Voltage (Note 8)	Vsd	_	0.7	1.2	V	Vgs = 0V, Is = 300mA
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss	_	64.3	-	рF	
Output Capacitance	Coss	_	6.1		pF	Vps = 25V, Vgs = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	4.5		pF	
Gate Resistance	Rg	—	70		Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge	Qg	_	1.6		nC	
Gate-Source Charge	Qgs		0.2	—	nC	VGs = 4.5V, VDs = 15V ID = 1A
Gate-Drain Charge	Q _{gd}		0.2		nC	
Turn-On Delay Time	tD(ON)	+	3.5		ns	
Turn-On Rise Time	t _R	_	2.8	_	ns	$V_{DS} = 10V, I_D = 1A$
Turn-Off Delay Time	td(off)	-	38		ns	VGs = 10V, RG = 6Ω
Turn-Off Fall Time	t⊧		13		ns	

 Measured under pulsed conditions to minimize self-heating effect. Pulse width ≤ 300µs; duty cycle ≤ 2%.
For design aid only, not subject to production testing. Notes:





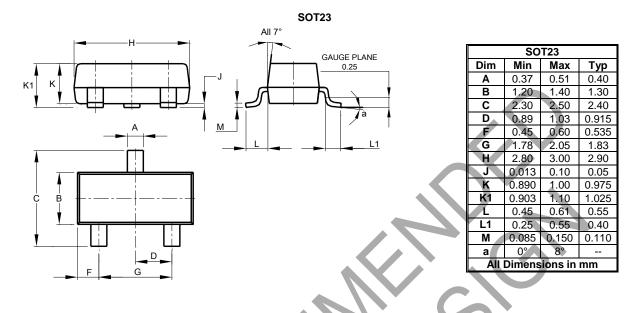






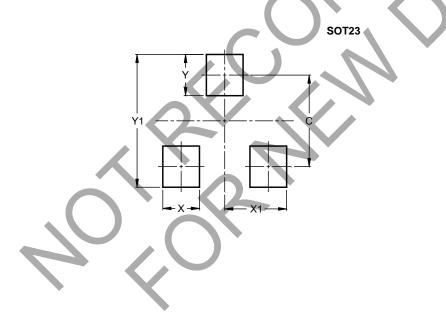
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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