		ımber:		304004		PCN Date:	Marc	h 6 20	19
Tit	le:	Datasheet fo	r DS90U	B929-Q1	and DS9	0UH929-Q1			_
Cus	stom	ner Contact:	PCN Ma	nager			De	pt:	Quality Services
Cha	ange	е Туре:							
		sembly Site			Design			Wafe	r Bump Site
Ī		sembly Process			Data Shee	et			r Bump Material
П		embly Material	S			er change			r Bump Process
П		chanical Specifi			Test Site				r Fab Site
		king/Shipping/			Test Proce	ess		Wafe	r Fab Materials
		<u> </u>						Wafe	r Fab Process
				No	tificatio	on Details			
De	scrip	otion of Chang	je:						
Tex	as Ir	nstruments Inc	orporate	d is anno	uncing ar	information of	only no	otificati	on.
The	pro	duct datasheet	(s) is bei	ng updat	ed as sun	nmarized belo	w.		
The	follo	owing change h	nistory pr	ovides fu	ırther det	ails.			
	₹Ü.	ŢEXAS							DS90UB929-Q1
	•	INSTRUMENTS					SNI 94574	A _ NOVEME	BER 2014-REVISED MARCH 2019
							SINESHOTA	K-NOVEME	DER 2014-REVISED MARCH 2018
C	hang	es from Original (N	ovember 2	014) to Rev	ision A				Page
_	Chr	angod all references	of HDMLC	look to TMD	C Clock				3
•		_							
•									5
•									6
•		Added RX_5V parameter to the Recommended Operating Conditions							
•									7
•		Changed the TMDS jitter specification in the AC Electrical Characteristics table							
•			_						21
•		Deleted Auto Soft Sleep mode from the MODE_SEL[1:0] Settings table							
•									28
•									29
•									30
•			-						32
•	Add	ded registers 0x00, 0)x13, 0x15,	0x5B, 0xC0	, 0xC2, 0xC3	3, 0xC6, 0xC8, 0xC	E, and 0	xD0 to d	efault list32
•	Cha	anged information al	out GPIO0	modes x00	and x10				36
	Cha	anged information al	out GPIO1	modes x00	and x10				36
	Add	led reset information	to register	0x15					40
	Changed the register 0x1A information					41			
	Added Registers 0x40, 0x41, and 0x42					46			
•	Deleted Rev A1 silicon information								
	Added 'Set to 0' test to the 0x5B register description					51			
	Changed register 0x5C[4:3] information. 51								
	Added Page 0x10 Register5					57			
	Added Page 0x14 Register								
•	Cha 2.6-	anged graph caption -Gbps Serial Line Ra	from: 1080 ate, Single l	p60 Video a Lane FPD-L	at 2.6 Gbps S ink III Output	Serial Line Rate (O	ne of Tw	o Lanes)	to: 720p60 Video at62
				-					
•	Char	nged Power-Up Req	uirements s	section					63
Ī									



CI	anges from Original (November 2014) to Revision A	Page
	Changed all references of HDMI Clock to TMDS Clock	3
•	Changed the VTERM pin description	5
•	Changed V _{DD11} maximum from: 1.7 V to: 1.32 V	6
•	Added RX_5V parameter to the Recommended Operating Conditions	6
•	Added T _{CLH1/2} and T _{CHL1/2} parameters to the Recommended Operating Conditions	7
•	Changed the TMDS jitter specification in the AC Electrical Characteristics table	10
•	Added information about using I2S with the DS90UH926-Q1 in the Audio Modes section	21
•	Deleted Auto Soft Sleep mode from the MODE_SEL[1:0] Settings table	27
•	Added Frequency Detection Circuit section	28
•	Added 5% resistor information to the Serial Control Bus section	29
•	Added information to Multi-Master Arbitration Support section	30
•	Added additional information to register 0x01	32
•	Added registers 0x00, 0x13, 0x15, 0x5B, 0xC0, 0xC2, 0xC3, 0xC6, 0xC8, 0xCE, and 0xD0 to default list	32
•	Changed information about GPIO0 modes x00 and x10	36
•	Changed information about GPIO1 modes x00 and x10	36
•	Added reset information to register 0x15	40
•	Changed the register 0x1A information	41
•	Added Registers 0x40, 0x41, and 0x42	46
•	Deleted Rev A1 silicon information	50
•	Added 'Set to 0' test to the 0x5B register description	51
•	Changed register 0x5C[4:3] information.	51
•	Added Page 0x10 Register	62
•	Added Page 0x14 Register	62
•	Changed graph caption from: 1080p60 Video at 2.6 Gbps Serial Line Rate (One of Two Lanes) to: 720p60 Video at 2.6-Gbps Serial Line Rate, Single Lane FPD-Link III Output	67
(Changed Power-Up Requirements section	68

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Device Family	Change From:	Change To:
DS90UB929-Q1	SNLS457	SNLS457A
DS90UH929-Q1	SNLS458	SNLS458A

These changes may be reviewed at the datasheet links provided.

http://www.ti.com/product/DS90UB929-Q1

http://www.ti.com/product/DS90UH929-Q1

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

DS90UB929TRGCRQ1	DS90UB929TRGCTQ1	DS90UH929TRGCRQ1	DS90UH929TRGCTQ1

For questions regarding this notice, e-mails can be sent to the contacts shown below or your local Field Sales Representative.

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