



### DMN26D0UFB4

#### **Product Summary**

BV <sub>DSS</sub>	RDS(on)	I <sub>D</sub> TA = +25°C
2014	$3.0\Omega @ V_{GS} = 4.5V$	240mA
20V	6.0Ω @ V <sub>GS</sub> = 1.8V	180mA

#### Description

This new generation MOSFET is designed to minimize the on-state resistance ( $R_{DS(ON)}$ ) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

## Applications

- DC-DC Converters
- Power Management Functions

### **Features and Benefits**

- N-Channel MOSFET
- Low On-Resistance
- Very Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package, 0.4mm Maximum Package Height

N-CHANNEL ENHANCEMENT MODE MOSFET

- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotiveproducts/.

This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

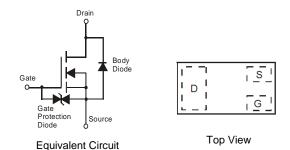
### **Mechanical Data**

- Case: X2-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.001 grams (Approximate)





Bottom View



#### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN26D0UFB4-7	X2-DFN1006-3	3,000/Tape & Reel
DMN26D0UFB4-7B	X2-DFN1006-3	10,000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Notes:



# **Marking Information**

DMN26D0UFB4-7	From date code 1527 (YYWW), Top View Dot Denotes Drain Side Top View Dot Denotes Drain Side Top View Bar Denotes Gate and Source Side Top View Bar Denotes Gate and Source Side
DMN26D0UFB4-7B	$\mathbf{M} = \operatorname{Part} \operatorname{Marking} \operatorname{Code}$ $\mathbf{M} = \operatorname{Part} \operatorname{Marking} \operatorname{Code}$

# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain Source Voltage			Vdss	20	V
Gate-Source Voltage			Vgss	±10	V
Continuous Drain Current (Note 5) $V_{GS} = 4.5V$	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	lo	240 190	mA
Continuous Drain Current (Note 5) V <sub>GS</sub> = 1.8V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	180 140	mA
Pulsed Drain Current - tP = 10µs			Ідм	805	mA

### Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5) @T <sub>A</sub> = +25°C	PD	350	mW
Thermal Resistance, Junction to Ambient (Note 5)	Reja	357	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Note: 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

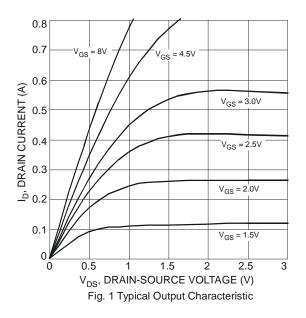


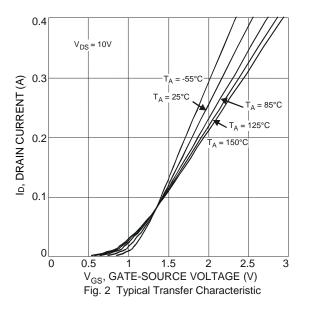
# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)				•	•		
Drain-Source Breakdown Voltage	BVDSS	20	_	_	V	$V_{GS} = 0V, I_{D} = 100 \mu A$	
Zero Gate Voltage Drain Current @ T <sub>C</sub> = +25°C	I <sub>DSS</sub>	_	_	500	nA	$V_{DS} = 20V, V_{GS} = 0V$	
Gate-Body Leakage	IGSS	_	_	±1 ±100	μA nA	$V_{GS} = \pm 10V, V_{DS} = 0V$ $V_{GS} = \pm 5V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	Vgs(th)	0.6		0.9	V	Vds = Vgs, Id = 250µA	
Static Drain-Source On-Resistance	Rds(on)	 	1.8 2.5 3.4 4.7	3.0 4.0 6.0 10.0	Ω	$\label{eq:VGS} \begin{array}{l} V_{GS} = 4.5 V, \ I_{D} = 100 m A \\ V_{GS} = 2.5 V, \ I_{D} = 50 m A \\ V_{GS} = 1.8 V, \ I_{D} = 20 m A \\ V_{GS} = 1.5 V, \ I_{D} = 10 m A \end{array}$	
Forward Transconductance	Y <sub>fs</sub>	180	242	_	mS	$V_{DS} = 10V, I_{D} = 0.1A$	
Source-Drain Diode Forward Voltage	Vsd	0.5	_	1.4	V	Vgs = 0V, Is = 115mA	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss	_	14.1	28.2	pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V f = 1.0MHz	
Output Capacitance	Coss	_	2.9	5.8	pF		
Reverse Transfer Capacitance	Crss	_	1.6	3.2	pF		
SWITCHING CHARACTERISTICS (Note 7)							
Turn-On Delay Time	tD(ON)	_	3.8	—		V <sub>GS</sub> = 4.5V, V <sub>DD</sub> = 10V	
Rise Time	t <sub>R</sub>	_	7.9	_	ns		
Turn-Off Delay Time	tD(OFF)	_	13.4	—	115	$I_D = 200 mA, R_G = 2.0 \Omega$	
Fall Time	tF	_	15.2	_			

Notes: 6. Short duration pulse test used to minimize self-heating effect.

7. Guaranteed by design. Not subject to product testing.



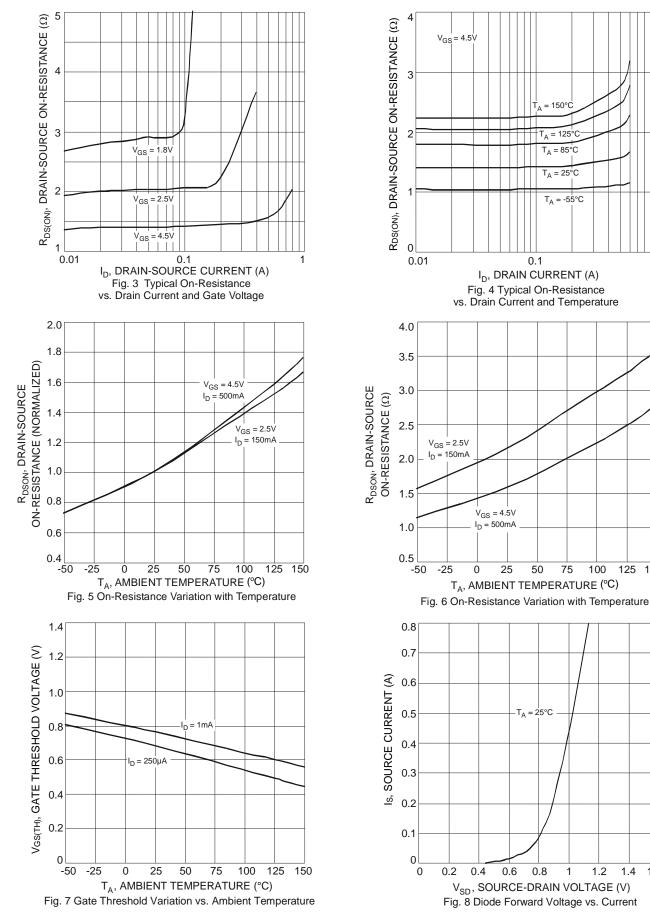




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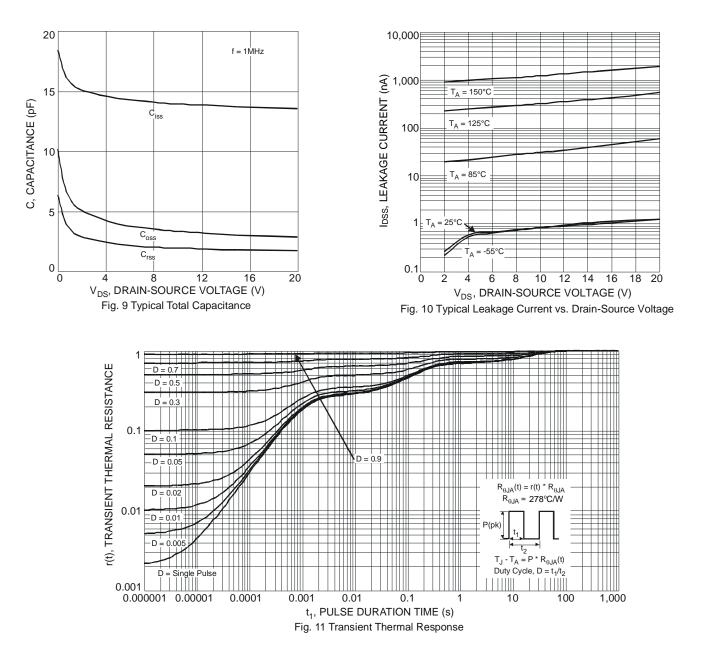
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125 150



1.4 1.6



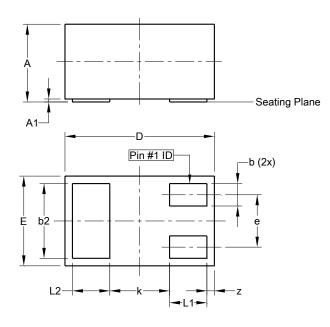




### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### X2-DFN1006-3

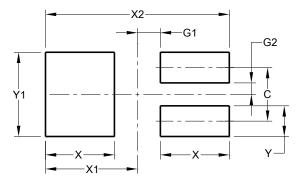


X2-DFN1006-3					
Dim	Min	Max	Тур		
Α		0.40	_		
A1	0.00	0.05	0.03		
b	0.10	0.20	0.15		
b2	0.45	0.55	0.50		
D	0.95	1.05	1.00		
ш	0.55	0.65	0.60		
e			0.35		
L1	0.20	0.30	0.25		
L2	0.20	0.30	0.25		
k			0.40		
Z	0.02	0.08	0.05		
All Dimensions in mm					

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### X2-DFN1006-3



Dimensions	Value (in mm)		
С	0.350		
G1	0.150		
G2	0.075		
Х	0.450		
X1	0.600		
X2	1.200		
Y	0.200		
Y1	0.550		



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