

Product Specification

16G Fibre Channel Short Wavelength SFP+ Transceiver

FTLF8529P5xyV

PRODUCT FEATURES

- Up to 14.025 Gb/s bi-directional data links
- Hot-pluggable SFP+ footprint
- Built-in digital diagnostic functions
- 850nm Oxide VCSEL laser transmitter
- Duplex LC connector
- Bail latch or pull tab
- RoHS compliant
- 35m on 50/125µm MMF
- 100m over M5E MMF (50/125um OM3)
- Metal enclosure, for lower EMI
- Single 3.3V power supply
- Operating temperature range:

C-temp: 0°C to 70°C

E-temp: 0°C to 85°C





APPLICATIONS

Tri-Rate 4.25/8.5/14.025 Gb/s Fibre Channel

Finisar® FTLF8529P5xyV SFP+ transceivers are designed for use in Fibre Channel links up to 14.025 Gb/s data rate over multimode fiber. They are compliant with FC-PI-5 Rev. 6.00a, SFF-8472 Rev 11.0c and SFF-8081h, and compatible with SFF-8432b and applicable portions of SFF-8431 Rev. 4.1d. The transceiver is RoHS compliant as described in Application Note AN-2038.

PRODUCT SELECTION

FTLF8529P5xyV

x=B: Bail latch x=P: Pull tab

y=C: C-temp (0° C to 70° C)

y=N: E-temp (0°C to 85°C)

I. Pin Descriptions

Pin	Symbol	Name/Description					
			f.				
1	V_{EET}	Transmitter Ground (Common with Receiver Ground)	1				
2	T_{FAULT}	Transmitter Fault.	2				
3	T_{DIS}	Transmitter Disable. Laser output disabled on high or open.	3				
4	SDA	2-wire Serial Interface Data Line (MOD-DEF2)	4				
5	SCA	2-wire Serial Interface Clock (MOD-DEF1)	4				
6	MOD_ABS	Module Absent, connected to V _{EET} or V _{EER}	4				
7	RS0	Rx Rate Select:	5				
		Open or Low = 8.5 or 4.25 Gb/s Fibre Channel (Low Bandwidth)					
		High = 14.025 Gb/s Fibre Channel (High Bandwidth)					
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation.	6				
9	RS1	Tx Rate Select:	5				
		Open or Low = 8.5 or 4.25 Gb/s Fibre Channel (Low Bandwidth)					
		High = 14.025 Gb/s Fibre Channel (High Bandwidth)					
10	V_{EER}	Receiver Ground (Common with Transmitter Ground)	1				
11	V_{EER}	Receiver Ground (Common with Transmitter Ground)	1				
12	RD-	Receiver Inverted DATA out. AC Coupled					
13	RD+	Receiver Non-inverted DATA out. AC Coupled					
14	$V_{\rm EER}$	Receiver Ground (Common with Transmitter Ground)	1				
15	V_{CCR}	Receiver Power Supply					
16	V_{CCT}	Transmitter Power Supply					
17	$V_{\rm EET}$	Transmitter Ground (Common with Receiver Ground)	1				
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.					
19	TD-	Transmitter Inverted DATA in. AC Coupled.					
20	V_{EET}	Transmitter Ground (Common with Receiver Ground)	1				

- 1. Circuit ground is internally isolated from chassis ground.
- 2. T_{FAULT} is an open collector/drain output, which should be pulled up with a 4.7k-10k Ohms resistor on the host board if intended for use. Pull up voltage should be between 2.0V to Vcc+0.3V. A high output indicates a transmitter fault caused by either the TX bias current or the TX output power exceeding the preset alarm thresholds. A low output indicates normal operation. In the low state, the output is pulled to <0.8V.
- 3. Laser output disabled on $T_{DIS} > 2.0V$ or open, enabled on $T_{DIS} < 0.8V$.
- 4. Should be pulled up with 4.7k 10kohms on host board to a voltage between 2.0V and 3.6V. MOD_ABS pulls line low to indicate module is plugged in.
- 5. Rate select can also be set through the 2-wire bus in accordance with SFF-8472 v. 12.1°. Rx Rate Select is set at Bit 3, Byte 110, Address A2h. Tx Rate Select is set at Bit 3, Byte 118, Address A2h. Note: writing a "1" selects maximum bandwidth operation. Rate select is the logic OR of the input state of Rate Select Pin and 2-wire bus.
- 6. LOS is open collector output. Should be pulled up with 4.7k 10kohms on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.

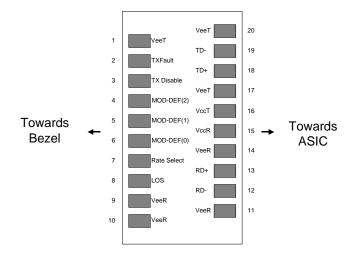


Diagram of Host Board Connector Block Pin Numbers and Names

II. Absolute Maximum Ratings

Parameter	Symbol	Min	Тур	Max	Unit	Ref.
Maximum Supply Voltage	Vcc	-0.5		4.0	V	
Storage Temperature	T_{S}	-40		85	°C	
Case Operating Temperature	$T_{\rm C}$				°C	
C-temp (FTLF8529P5xCV)		0		70		
E-temp (FTLF8529P5xNV)		0		85		
Relative Humidity (Non-condensing)	RH	0		85	%	1

Notes:

1. Non Condensing

III. Electrical Characteristics (T_A , $V_{CC} = 3.15$ to 3.46 Volts)

Parameter	Symbol	Min	Тур	Max	Unit	Ref.
Supply Voltage	Vcc	3.15		3.46	V	
Supply Current	Icc			290	mA	1
Transmitter						
Input differential impedance	Rin		100		Ω	2
Single-ended data input swing	Vin,pp	90		800	mV	
Transmit Disable Voltage	V_{D}	2		Vcc	V	3
Transmit Enable Voltage	V_{EN}	Vee		Vee+0.8	V	
Receiver						
Single ended data output swing	Vout,pp	185		425	mV	4
LOS Fault	V _{LOS fault}	2		Vcc _{HOST}	V	5
LOS Normal	V _{LOS norm}	Vee		Vee+0.8	V	5
Power Supply Rejection	PSR	100			mVpp	6
Deterministic Jitter	RX DJ				UI	
8.5 Gb/s				0.420		
14.025Gb/s				0.220		

- 1. With established link. The total power dissipation could exceed 1W when the module is trying to establish link at operating case temperature below 25°C
- 2. Connected directly to TX data input pins. AC coupling from pins into laser driver IC.
- 3. Or open circuit.
- 4. Into 100 ohms differential termination.
- 5. LOS is an open collector output. Should be pulled up with 4.7k 10kohms on the host board. Normal operation is logic 0; loss of signal is logic 1. Maximum pull-up voltage is 5.5V.
- 6. Receiver sensitivity is compliant with power supply sinusoidal modulation of 20 Hz to 1.5 MHz up to specified value applied through the recommended power supply filtering network.

IV. Optical Characteristics (T_A , $V_{CC} = 3.15$ to 3.46 Volts)

Parameter	Symbol	Min	Тур	Max	Unit	Ref.		
Transmitter								
Average Output Power: 50 or 62.5 MMF	P _{OUT}	-7.8			dBm	1		
Optical Wavelength	λ	840		860	nm			
Spectral Width (RMS), 28.05Gb/s	σ			0.59	nm			
Optical Modulation Amplitude 4.25Gb/s 8.5 Gb/s 14.025Gb/s	OMA	247 (-6.1) 302 (-5.2) 331 (-4.8)			μW(dBm)			
Optical Rise/Fall Time @ 4.25Gb/s	$t_{\rm r}/\ t_{\rm f}$	(,		90	ps	2		
Transmitter Waveform and Dispersion Penalty, 8.5 Gb/s	TWDP			4.3	dB	3		
Vertical Eye Closure Penalty 14.025 Gb/s	VECP			2.56	dB	4		
Relative Intensity Noise	RIN			-128	dB/Hz			
Receiver								
Unstressed Receiver OMA Sensitivity 4.25Gb/s 8.5 Gb/s	RxSENS			61(-12.1) 76(-11.2) 89(-10.5)	μW(dBm)	5		
14.025Gb/s Average Receiver Power	D _{vv}	0		09(-10.3)	dBm			
Optical Center Wavelength	Rx_{MAX} λ_{C}	770		860	nm			
Optical Return Loss	700	12		000	dB			
LOS De-Assert	LOS _D	12		-13	dBm			
LOS Assert	LOSA	-30			dBm			
LOS Hysteresis		0.5			dB			

- 1. Class 1 Laser Safety limit per FDA/CDRH, and EN (IEC) 60825 laser safety standards.
- 2. Unfiltered, 20-80%. Complies with FC 1x and 2x eye mask when filtered
- 3. TWDP is calculated with a 1,0 equalizer and a 9.84 GHz Gaussian filter for the fiber simulation. Jitter values at γ_T and γ_R are controlled by TWDP and stress receiver sensitivity.
- 4. For 16GFC, VECPQ is calculated with a 16.6 GHz Gaussian filter for fiber simulation
- 5. Unstressed Rx sensitivity

IV. General Specifications

Parameter	Symbol	Min	Тур	Max	Units	Ref.
Data Rate	BR		4.25		Gb/sec	1
			8.5			
			14.025			
Bit Error Rate	BER			10 ⁻¹²		2
Fiber Length on 50/125µm	L			150	m	3
(OM2/M5) MMF				50		4
				35		5
Fiber Length on 50/125µm high-	L			380	m	3
bandwidth (OM3/M5E) MMF				150		4
				100		5

Notes:

- 1. 8x Fibre Channel compatible, per FC-PI-4¹.
- 2. PRBS 2⁷-1 for 8GFC. PRBS 2³¹-1 for 16GFC
- 3. At 4.25 Gb/s Fibre Channel data rate
- 4. At 8.5 Gb/s Fibre Channel data rate.
- 5. At 14.025 Gb/s Fibre Channel data rate.

VI. Environmental Specifications

Parameter	Symbol	Min	Тур	Max	Units	Ref.
Case Operating Temperature	T_{op}				°C	
C-temp (FTLF8529P5xCV)		0		70		
E-temp (FTLF8529P5xNV)		0		85		
Storage Temperature	T_{sto}	-40		85	°C	

VII. Regulatory Compliance

The FTLF8529P5xyV transceivers are RoHS compliant. Copies of certificates are available from II-VI Incorporated upon request.

FTLF8529P5xyV transceiver modules are Class 1 laser eye safety compliant per IEC 60825-1.

CAUTION: Use of controls or adjustments or performance of procedures other than those specified herein may result in hazardous radiation exposure.

VIII. Digital Diagnostic Functions

 $Finisar^{\$}$ FTLF8529P5xyV SFP+ transceivers support the 2-wire serial communication protocol as defined in the SFP MSA $^{\rm f}$. It is very closely related to the E $^{\rm 2}$ PROM defined in the GBIC standard, with the same electrical specifications.

The standard SFP serial ID provides access to identification information that describes the transceiver's capabilities, standard interfaces, manufacturer, and other information.

Additionally, Finisar® SFP transceivers provide a enhanced digital diagnostic monitoring interface, which allows real-time access to device operating parameters such as transceiver temperature, laser bias current, transmitted optical power, received optical power and transceiver supply voltage. It also defines a sophisticated system of alarm and warning flags, which alerts end-users when particular operating parameters are outside of a factory set normal range.

The SFP MSA defines a 256-byte memory map in E²PROM that is accessible over a 2-wire serial interface at the 8 bit address 1010000X (A0h). The digital diagnostic monitoring interface makes use of the 8 bit address 1010001X (A2h), so the originally defined serial ID memory map remains unchanged. The interface is identical to, and is thus fully backward compatible with both the GBIC Specification and the SFP Multi Source Agreement. The complete interface is described in Finisar Application Note AN-2030: "Digital Diagnostics Monitoring Interface for SFP Optical Transceivers".

The operating and diagnostics information is monitored and reported by a Digital Diagnostics Transceiver Controller (DDTC) inside the transceiver, which is accessed through a 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL, Mod Def 1) is generated by the host. The positive edge clocks data into the SFP transceiver into those segments of the E²PROM that are not write-protected. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA, Mod Def 2) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

For more information, please see the SFP MSA documentation c,f and $Finisar^{\circledR}$ Application Note AN-2030.

IX. Digital Diagnostic Specifications

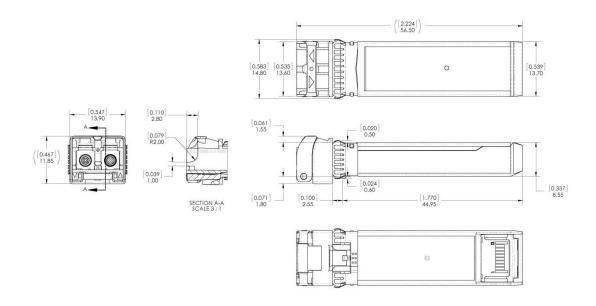
FTLF8529P5xyV transceivers can be used in host systems that require either internally or externally calibrated digital diagnostics.

Parameter	Symbol	Units	Min	Max	Accuracy	Ref.
Transceiver temperature C-temp (FTLF8529P5xCV) E-temp (FTLF8529P5xNV)	D_{DDTemp}	°C	0	+70 +85	±5°C	
Transceiver supply voltage	D _{DDVoltage}	V	3.14	3.45	±3%	
Transmitter bias current	D _{DDBias}	mA	0	20	±10%	1
Transmitter output power	D _{DDTx-Power}	dBm	-8	-1	±3dB	
Receiver average optical input power	D _{DDRx-Power}	dBm	-12	0	±3dB	

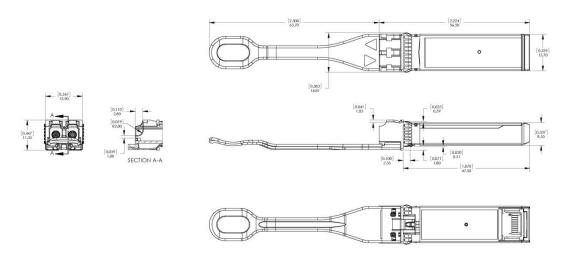
 $^{1.\,}$ Accuracy of Measured Tx Bias Current is 10% of the actual Bias Current from the laser driver to the laser.

X. Mechanical Specifications

Finisar® FTLF8529P5xyV SFP+ transceivers are compatible with the SFF-8432^b specification for improved pluggable form factor.



FTLF8529P5ByV (Bail latch)



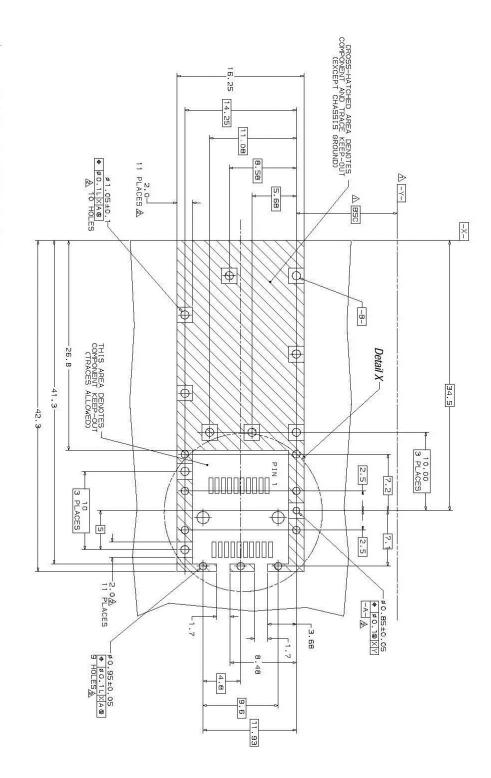
FTLF8529P5PyV (Pull tab)

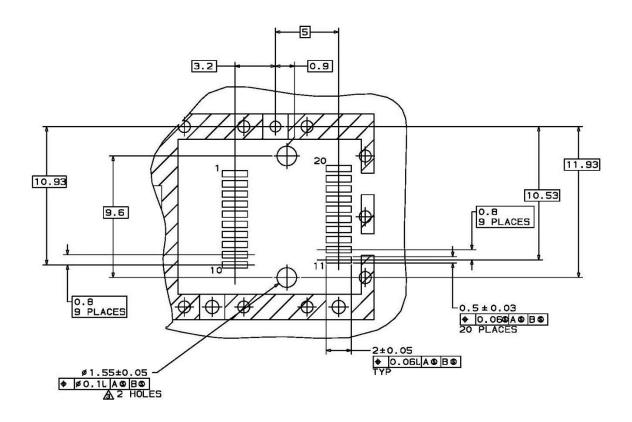
XI. PCB Layout and Bezel Recommendations

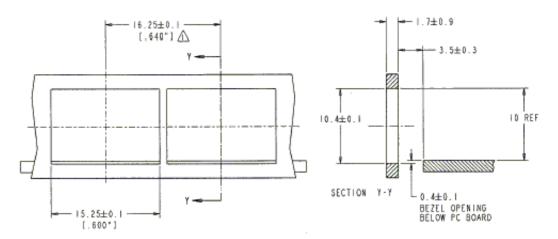
 Begtum and Basic Dimension Established by Customer

 ARads and Vias are Chassis Ground, 11 Places

 AThrough Holes are Unplated







NOTES:

ANINIMUM PITCH ILLUSTRATED, ENGLISH DIMENSIONS ARE FOR REFERENCE ONLY

2. NOT RECOMMENDED FOR PCI EXPANSION CARD APPLICATIONS

XII. Ordering Information

Part Number	Unlocking Mechanism	Operating Temperature Range
FTLF8529P5BCV	Bail latch	0°C to 70°C
FTLF8529P5BNV	Bail latch	0°C to 85°C
FTLF8529P5PCV	Pull tab	0°C to 70°C
FTLF8529P5PNV	Pull tab	0°C to 85°C

XIII. References

- a. "Fibre Channel Physical Interfaces -5, INCITS 479-2011 (*Defines 4GFC*, 8GFC and 16GFC)
- b. "Improved Pluggable Form Factor", SFF Document Number SFF-8432, Revision 5.0, July 16, 2007.
- c. "Digital Monitoring Interface for Optical Transceivers", SFF Document Number SFF-8472, Revision 12.1.
- d. "Enhanced Small Form Factor Pluggable Module 'SFP+ '", SFF Document Number SFF-8431, Revision 4.1, July 6, 2009; Addendum Jun 21, 2013.
- e. Directive 2011/65/EU of the European Council Parliament and of the Council, "on the restriction of the use of certain hazardous substances in electrical and electronic equipment". 08-June 2011, which supercedes the previous ROHS Directive 2002/95/EC.
- f. Small Form Factor Pluggable (SFP) Transceiver Multi-source Agreement (MSA), September 14, 2000 which is available as SFF Document Number INF-8074, Revision 1.0 May 12, 2001
- g. "0.8mm Card Edge Connector for 16Gb/s Applications", SFF Document Number SFF-8081, Revision 0.2, December 21, 2009.
- h. SFF-8402, Revision 0.2

XIV. For More Information

II-VI Incorporated 375 Saxonburg Boulevard Saxonburg, PA 16056 photonics.sales@ii-vi.com www.ii-vi.com