**HALOGEN** 

FREE



# Vishay General Semiconductor

# **Photovoltaic Solar Cell Protection Schottky Rectifier**

Ultra Low  $V_F = 0.29 \text{ V}$  at  $I_F = 5 \text{ A}$ 



PRIMARY CHARACTERISTICS			
I <sub>F(DC)</sub>	15 A		
$V_{RRM}$	45 V		
I <sub>FSM</sub>	200 A		
V <sub>F</sub> at I <sub>F</sub> = 15 A	0.41 V		
T <sub>OP</sub> max. (AC mode)	150 °C		
T <sub>J</sub> max. (DC forward current)	200 °C		
Package	P600		
Diode variation	Single die		

### **FEATURES**

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- High forward surge capability
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- T<sub>J</sub> 200 °C max. in solar by-pass mode application
- Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **TYPICAL APPLICATIONS**

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

### **MECHANICAL DATA**

Case: P600

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: Color band denotes cathode end

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	VSB15L45	UNIT	
Device marking code		V15L45		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	45	V	
Maximum average forward rectified current (fig. 1)	I <sub>F(AV)</sub> <sup>(1)</sup>	15		
	I <sub>F(AV)</sub> (2)	7.0	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	200		
Operating junction temperature range (AC mode)	T <sub>OP</sub>	-40 to +150		
Storage temperature range	T <sub>STG</sub>	-40 to +175	°C	
Junction temperature in DC forward current without reverse bias, $t \le 1$ h (fig. 2)	T <sub>J</sub> <sup>(3)</sup>	≤ 200		

### Notes

- (1) With heatsink
- (2) Without heatsink, free air
- (3) Meets the requirements of IEC 61215 ed. 2 bypass diode thermal test



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 25 °C	5 °C V <sub>F</sub> <sup>(1)</sup>	0.41	-	V	
	I <sub>F</sub> = 7.5 A			0.44	-		
	I <sub>F</sub> = 15 A			0.49	0.57		
	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 125 °C		0.29	-		
	I <sub>F</sub> = 7.5 A		T <sub>A</sub> = 125 °C		0.33	-	
	I <sub>F</sub> = 15 A			0.41	0.50	1	
Reverse current	V <sub>R</sub> = 45 V	T <sub>A</sub> = 25 °C	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	-	4.0	
	V <sub>R</sub> = 45 V	T <sub>A</sub> = 125 °C	IR (=)	17	35	- mA	
Typical junction capacitance	4.0 V, 1 MHz		CJ	1430	-	pF	

### Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: 40 ms pulse width

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	VSB15L45	UNIT	
Thermal resistance	R <sub>θJA</sub> <sup>(1)</sup>	50	°C/W	
	R <sub>0JL</sub> (1)	3.5	C/VV	
Typical thermal resistance	R <sub>0JL</sub> (2)	2.5	°C/W	

#### **Notes**

(1) Without heatsink, free air; units mounted on PCB with 2 mm x 2 mm copper pad areas at 9.5 mm lead length

<sup>(2)</sup> Leads clipped at 3 mm lead length from plastic body on 7.0 cm x 2.2 cm x 1.9 cm x 2 heatsink

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
VSB15L45-M3/54	1.88	54	800	13" diameter paper tape and reel	
VSB15L45-M3/73	1.88	73	300	Ammo pack packaging	

# RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

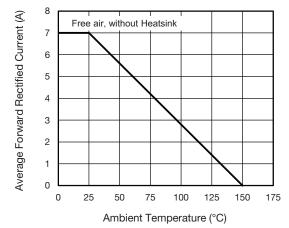


Fig. 1 - Forward Current Derating Curve

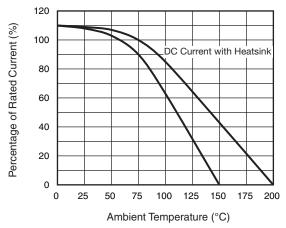


Fig. 2 - Rated Forward Current vs. Ambient Temperature



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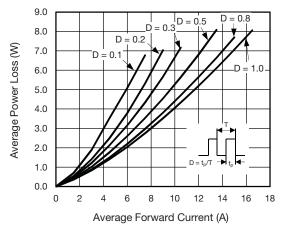


Fig. 3 - Forward Power Loss Characteristics

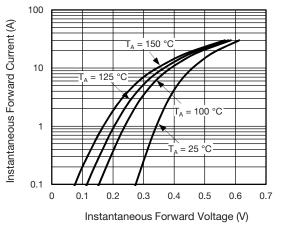


Fig. 4 - Typical Instantaneous Forward Characteristics

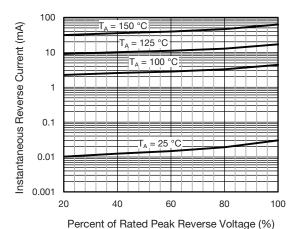


Fig. 5 - Typical Reverse Leakage Characteristics

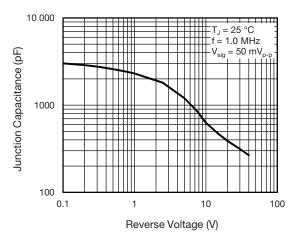
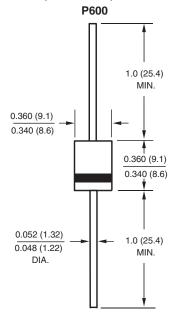


Fig. 6 - Typical Junction Capacitance

## PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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