Notification Numbers	20150910000		Notification Date:				10/09/2015			
Title: Datasheet update for PCM5100A										
Customer Contact:	ustomer Contact: PCN Manager				De	ot:	Quality Services			
Change Type:										
Assembly Site		Design				Wafei	fer Bump Site			
Assembly Process	$\overline{\Sigma}$		eet				fer Bump Material			
_			mber chang	je				Bump Process		
Mechanical Specification		Test Sit				Wafer Fab Site				
Packing/Shipping/	Labeling	Test Pro	ocess			Wafei	Wafer Fab Materials			
						Wafer Fab Process				
Notification Details										
Description of Change:										
Texas Instruments Incorporated is announcing an information only notification.										
The product datasheet(s) is updated as seen in the change revision history below:										
DOMESTON BOMESTOS D	21454004					•	U TE	XAS		
PCM5100A, PCM5101A, PCM5102A PCM5100A-Q1, PCM5101A-Q1, PCM5102A-Q1						STRUMENTS				
SLAS859C – MAY 2012 – REVISED MAY 2015 www.ti.com										
Changes from Revision B (Ja	anuary 2015) to Rev	ision C						Page		
Changed the device number from "PCM510x" to "PCM510xA" in the Simplified System Diagram										
Changed typical performance table to reflect part differences accurately										
 Changed "Storage temperatures, T_{stg}" to "Operating junction temperature range at –40°C to 130°C" 										
Changed "Storage temperature (Q1 devices) –40°C to 125°C" to "Storage temperatures, T _{stg} –65°C to 150°C"										
 Changed the stereo line output load resistance MIN value in the Recommended Operating Conditions from "2 kΩ" to "1 kΩ"										
Changed the operating junction temperature range in the Recommended Operating Conditions from "MIN = -25°C										
MAX = 85°C" to "MIN = -40°C MAX = 130°C"										
Added "Q1 Automotive grade devices" and "Consumer grade (non-Q1) devices" to the condition statement in the Electrical Characteristics										
Added "Q1 Automotive grade devices" and "Consumer grade (non-Q1) devices" to the condition statement in the Typical Characteristics graphs section										
Changed "MCK" to "SCK" a	at the PLL Clock in th	ne Functional	Block Diagram)				14		
Added label "Mute Circuit" and ground symbols to pins DEMP and FMT in Figure 33										
The datasheet number will be changing.								_		
Device Family		Change From:					ge To:			
			SLAS859B				SLAS859C			
These changes may be	reviewed at the	e datashe	et links pro	vided.						
http://www.ti.com/product/pcm5100a										
Reason for Change:										
To more accurately reflect device characteristics.										
Anticipated impact o				Reliah	ility	(nosi	itive /	/ negative):		
No anticipated impact.										
to the actual device.	iiiis is a speciii	ication cn	ange annot	inceme	iit Oi	iiy. III	ere ar	e no changes		
	dentification	resulting	from this	Notific	ratio	nn'				
Changes to product identification resulting from this Notification:										
None.										
Product Affected:	T							1		
PCM5100APW	PCM5100ATP	WRQ1	PCM5101	AQPWRO	Q1	PCM5102APWR				
PCM5100APWR	PCM5101APW	1	PCM5101	ATPWRC	21	PCM	5102AQPWRQ1			
PCM5100AOPWRO1	PCM5101APW	/R	PCM5102/							

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

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lanan	PCNJapanContact@list.ti.com				