

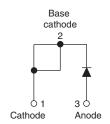
VS-30ETH06FP-F3, VS-30ETH06FP-N3

Vishay Semiconductors

Hyperfast Rectifier, 30 A FRED Pt®







PRIMARY CHARACTERISTICS				
Package	TO-220 FullPAK			
I _{F(AV)}	30 A			
V_{R}	600 V			
V _F at I _F	1.34 V			
t _{rr} (typ.)	23 ns			
T _J max.	175 °C			
Circuit configuration	Single			

FEATURES

- Reduced Q_{rr} and soft recovery
- 175 °C T_J maximum
- For PFC CRM/CCM operation
- Fully isolated package (V_{INS} = 2500 V_{RMS})
- UL E78996 approved



 Designed and qualified according JEDEC®-JESD 47

to **HALOGEN FREE**

· Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	V_{RRM}		600	V	
Average rectified forward current	I _{F(AV)}	T _C = 37 °C	30	А	
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	220	A	
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR} , V_{R}	I _R = 100 μA	600	-	-	
Forward valtage	Farmer de calle de la calle de	I _F = 30 A	-	2.00	2.60	V
Forward voltage V _F	I _F = 30 A, T _J = 150 °C	-	1.34	1.75		
Reverse leakage current I _R	V _R = V _R rated	-	0.3	50		
	$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	60	500	μΑ	
Junction capacitance	C _T	V _R = 600 V	-	33	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8	-	nH

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	28	35	
B	$I_F = 1 \text{ A, } dI_F/dt = 100 \text{ A/}\mu\text{s, } V_R = 30 \text{ V}$		-	23	30		
Reverse recovery time	t _{rr}	T _J = 25 °C		-	31	-	ns
		T _J = 125 °C		-	77	-	
Peak recovery current I _{RRM}	T _J = 25 °C	$I_F = 30 \text{ A}$	-	3.5	-	А	
	IRRM	T _J = 125 °C	dI _F /dt = 200 A/μs V _R = 200 V	-	7.7	-	A
Reverse recovery charge Q _{ri}	0	T _J = 25 °C		-	65	-	nC
	Q _{rr}	T _J = 125 °C		-	345	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance, junction-to-case per leg	R _{thJC}		-	-	2.85	
Thermal resistance, junction-to-ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W
Thermal resistance, case-to-heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.2	-	
Weight			-	2	-	g
vveignt			-	0.07	-	OZ.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220 FullPAK		30ETH	H06FP	

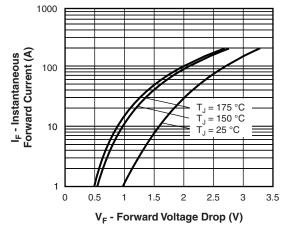


Fig. 1 - Typical Forward Voltage Drop Characteristics

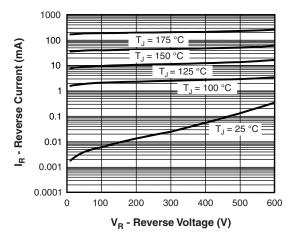


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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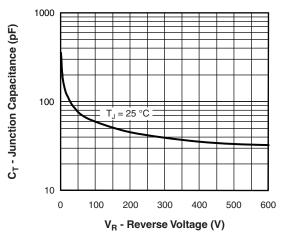


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

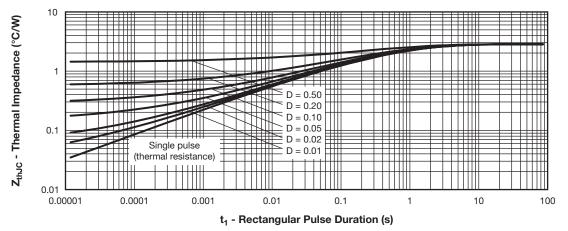


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

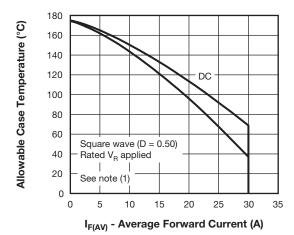


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

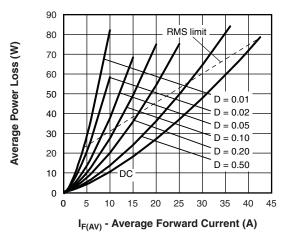


Fig. 6 - Forward Power Loss Characteristics

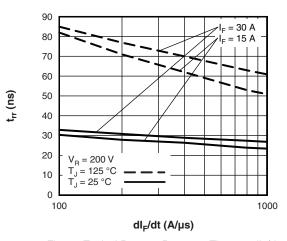


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

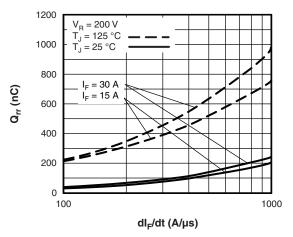
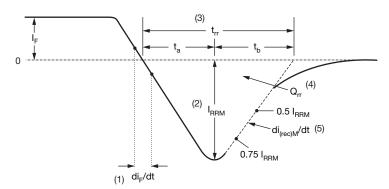


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

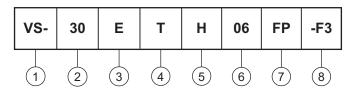
Fig. 9 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (30 A)

3 - E = single

- T = TO-220

- H = hyperfast recovery

6 - Voltage rating (06 = 600 V)

7 - TO-220 FullPAK

Environmental digit:

-F3 = RoHS-compliant and totally lead (Pb)-free

-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

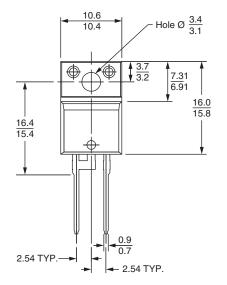
ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-30ETH06FP-F3	50	1000	Antistatic plastic tube		
VS-30ETH06FP-N3	50	1000	Antistatic plastic tube		

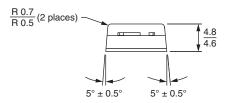
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95005</u>				
Part marking information	www.vishay.com/doc?95440			
SPICE model	www.vishay.com/doc?96440			

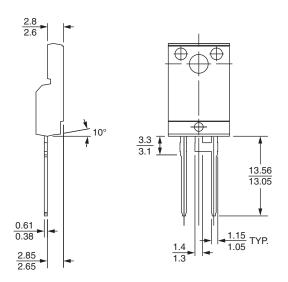


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DIMENSIONS in millimeters







Lead assignments

Diodes

1 + 2 - Cathode

3 - Anode

Conforms to JEDEC outline TO-220 FULL-PAK



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