AUIPS6044G





### INTELLIGENT POWER HIGH SIDE SWITCH

### **Features**

- Over temperature shutdown (with auto-restart)
- Short circuit protection (current limit)
- Reverse battery protection (turns On the MOSFET)
- Full diagnostic capability (short circuit to battery)
- Active clamp
- Open load detection in On and Off state
- Ground loss protection
- Logic ground isolated from power ground
- ESD protection

### Description

The AUIPS6044G is quad output Intelligent Power Switch (IPS) for use in a high side configuration. It features short circuit, over-temperature, ESD protection, inductive load capability and diagnostic feedback. The output current is limited to the Ilim value. The current limitation is activated until the thermal protection acts. The over-temperature protection turns off the device if the junction temperature exceeds the Tshutdown value. It will automatically restart after the junction has cooled 7°C below the Tshutdown value. The reverse battery protection turns On the MOSFET. A diagnostic pin provides different voltage levels for each fault condition. The double level shifter circuitry will allow large offsets between the logic and load

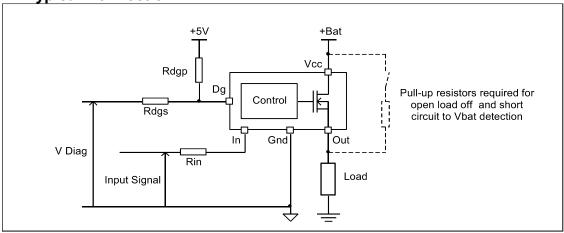
### **Product Summary**

130m $\Omega$  max. Rds(on) Vclamp 39V **I** Limit 7A Open load 3V / 0.22A

### **Package**



**Typical Connection** 



# AUIPS6044GPbF

### Qualification Information<sup>†</sup>

Quaiii	ication information					
Qualification Level			Automotive (per AEC-Q100 <sup>††</sup> )			
		Comments: This family of ICs has passed an Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.				
Moisture Sensitivity Level		SOIC-28L	MSL2, 260°C (per IPC/JEDEC J-STD-020)			
	Machine Model	Class M2 (-	Class M2 (+/-150V) ***			
	Macriffe Model	(per AEC-				
ESD	Lluman Dady Madal	Class H1C (	+/-1500V) <sup>†††</sup>			
ESD	Human Body Model	(per AEC-Q100-002)				
	Charged Device Model	Class C4 (-	-/-900V) <sup>†††</sup>			
	Charged Device Model	(per AEC-Q100-011)				
IC Latch-Up Test		Class II, Level A				
		(per AEC-Q100-004)				
RoHS C	ompliant	Yes				

<sup>†</sup> Qualification standards can be found at International Rectifier's web site <a href="http://www.irf.com/">http://www.irf.com/</a>
†† Exceptions to AEC-Q100 requirements are noted in the qualification report.
††† Passing voltage level



### **Absolute Maximum Ratings**

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are referenced to Ground lead. Tj= -40°C..150°C, Vcc=6..35V (unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
Vout	Maximum output voltage	Vcc-35	Vcc+0.3	
Voffset	Maximum logic ground to load ground offset	Vcc-35	Vcc+0.3	
Vin	Maximum input voltage	-0.3	5.5	V
Vcc max.	Maximum Vcc voltage	_ 36		
Vcc cont.	Maximum continuous Vcc voltage	_	28	
lin max.	Maximum IN current	-3	10	mA
ldg max.	Maximum diagnostic output current	-3	10	ША
Vdg	Maximum diagnostic output voltage	-0.3	5.5	V
Pd	Maximum power dissipation (internally limited by thermal protection)			W
Fu	Rth=130°C/W per channel	_	3.8	VV
Tj max.	Max. storage & operating temperature junction temperature	-40	150	°C

### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Units
Rth1	Thermal resistance junction to ambient 1" sqrt. Footprint / 1 channel On	50	_	
Rth2	Thermal resistance junction to ambient 1" sqrt. Footprint / 2 channels On	100	_	°C/W
Rth3	Thermal resistance junction to ambient 1" sqrt. Footprint / 4 channels On	130		

note: Tj=Power dissipated in one channel x Rth

### **Recommended Operating Conditions**

These values are given for a quick design. For operation outside these conditions, please consult the application notes.

Symbol	Parameter	Min.	Max.	Units
VIH	High level input voltage	4	5.5	
VIL	Low level input voltage	0	0.9	
lout	Continuous drain current, Rth=130°C/W, Tj=150°C, 4 channels On			
	Tambient=85°C / 1" sqrt. footprint	l —	1.5	A
	Tambient=105°C / 1" sqrt. footprint	_	1.2	
Rin	Recommended resistor in series with IN pin	4	10	
Rdgs	Recommended resistor in series with DG pin for reverse battery protection	4	20	$\mathbf{k}\Omega$
Rdgp	Recommended pull-up resistor for DG	4	20	K75
Rol	Recommended pull-up resistor for open load detection	5	100	
F max.	Max. switching frequency		3.5	kHz



### **Static Electrical Characteristics**

Tj=-40°C..150°C, Vcc=6..28V (unless otherwise specified), typical values are given for Vcc=14V and Tj=25°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Rds(on)	ON state resistance Tj=25°C	_	110	130		Vin=5V, lout=2.5A
	ON state resistance Tj=150°C(1)	_	190	230		Vin=5V, lout=2.5A
	ON state resistance Tj=25°C, Vcc=6V	_	125	155	mΩ	Vin=5V, lout=1.5A
	ON state resistance during reverse battery	_	140	180		Vcc-Gnd=-14V
	Tj=25°C					
Vcc op.	Operating voltage range	6	_	28		
V clamp 1	Vcc to Out clamp voltage 1	37	39	_	V	lout=20mA
V clamp 2	Vcc to Out clamp voltage 2	_	40	_		lout=2.5A (see Fig. 1)
Icc Off	Supply current when Off and Vout	_	4	9		Vin=0V, Vout=0V,
	connected to ground with R<4Ω				μA	Tj=25°C, Vcc=14V
Icc On	Supply current when On	_	2.2	5	mA	Vin=5V, Vcc=14V
Vih	Input high threshold voltage	_	2.5	3		
Vil	Input low threshold voltage	1.5	2	_	V	
In hyst.	Input hysteresis	0.2	0.5	1		
lin On	Input current when device is On	_	40	100		Vin=5V
ldg	Dg leakage current	_	0.1	10	μA	Vdg=5V
Vdg	Low level DG voltage	_	0.25	0.4	V	ldg=1.6mA

Switching Electrical Characteristics Vcc=14V, Resistive load= $6\Omega$ , Vin=5V, Vin=5

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Tdon	Turn-on delay time	_	5	15		
Tr1	Rise time to Vout=Vcc-5V	_	3	10	μs	
Tr2	Rise time to Vout=0.9 x Vcc	_	4	30	· ·	
dV/dt (On)	Turn On dV/dt	_	2.5	_	V/µs	]
EOn	Turn On energy	_	100	_	μJ	see Fig. 3
Tdoff	Turn-off delay time	_	10	20	0	]
Tf	Fall time to Vout=0.1 x Vcc	_	3	10	μs	
dV/dt (Off)	Turn Off dV/dt	_	6.5	_	V/µs	]
EOff	Turn Off energy	_	50	_	μJ	



### **Protection Characteristics**

Tj=-40°C..150°C, Vcc=6..28V (unless otherwise specified), typical values are given for Vcc=14V and Tj=25°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
llim	Internal current limit	4	7	10	Α	Vout=0V, Tj=25°C
Tsd+	Over temperature high threshold	150(1)	165	_	°C	See fig. 2
Tsd-	Over temperature low threshold	_	158	_	C	See lig. 2
Vsc	Short-circuit detection voltage(2)	2	3	4		
UV+	Under voltage protection Vcc going up	-	5	6.2	V	
UV-	Under voltage protection Vcc going down	-	4.5	5.8	V	
VOL Off	Open load detection threshold	2	3	4		
I OL On	Open load detection threshold	0.05	0.17	0.27	Α	Tj=-4025°C
102011		0.05	0.15	0.22		Tj=25150°C

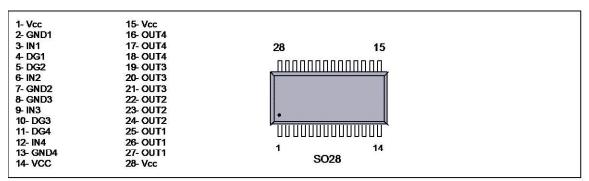
<sup>(1)</sup> Guaranteed by design (2) Reference to Vcc

### **True Table**

Operating Conditions	IN	OUT	DG
Normal	Н	Н	Н
Normal	L	L	Н
Open Load	Н	Н	L
Open Load (3)	L	Н	L
Short circuit to Gnd	Н	L	L
Short circuit to Gnd	L	L	Н
Short circuit to Vcc	Н	Н	L (4)
Short circuit to Vcc (5)	L	Н	L
Over-temperature	Н	Ĺ	Ĺ
Over-temperature	Ĺ	Ĺ	Н

<sup>(3)</sup> With a pull-up resistor connected between the output and Vcc.

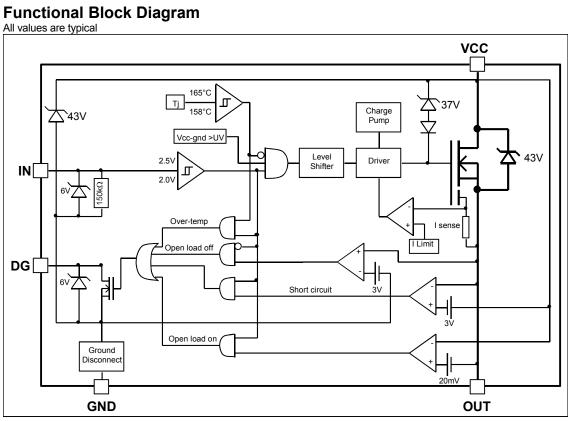
## **Lead Assignments**



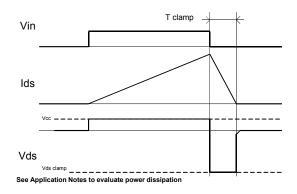
<sup>(4)</sup> Vds lower than 10mV.

<sup>(5)</sup> Without a pull-up resistor connected between the output and Vcc.





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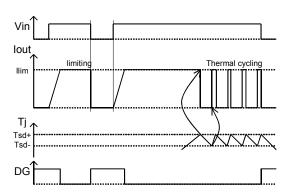
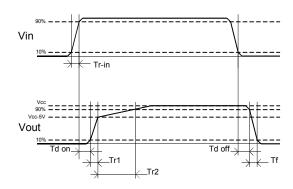


Figure 1 - Active clamp waveforms

Figure 2 – Protection timing diagram



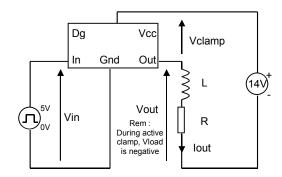


Figure 3 - Switching times definitions

Figure 4 - Active clamp test circuit

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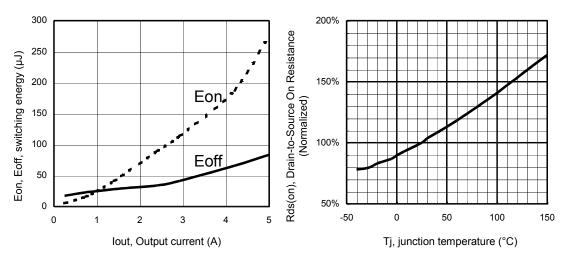


Figure 5 – Switching energy (μJ) Vs Output current (A)

Figure 6 - Normalized Rds(on) (%) Vs Tj (°C)

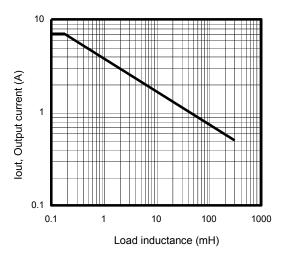


Figure 7 – Max. Output current (A) Vs Load inductance (mH)

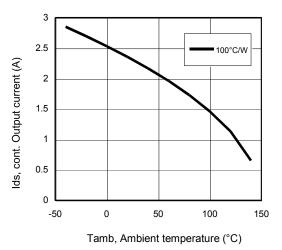
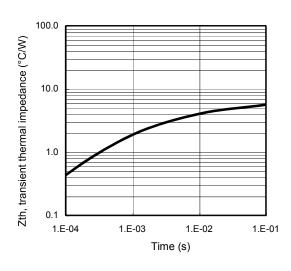


Figure 8 – Max. ouput current (A) Vs Ambient temperature (°C)

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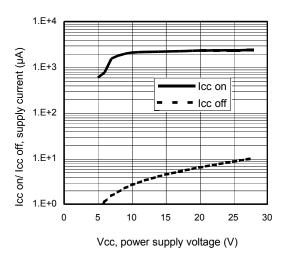


8
6
2
4
2
0
-50
0
50
100

Tj, junction temperature (°C)

Figure 9 – Transient thermal impedance (°C/W)
Vs time (s)

Figure 10 –I limit (A)
Vs junction temperature (°C)



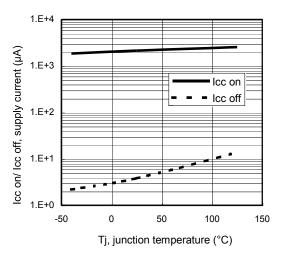


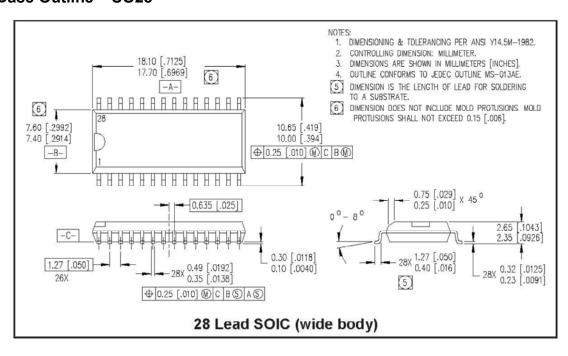
Figure 11 – Icc on/ Icc off (μA) Vs Vcc (V)\*

Figure 12 - Icc on/ Icc off (µA) Vs Tj (°C)\*

\*Vout connected to ground with R<4 $\Omega$ 

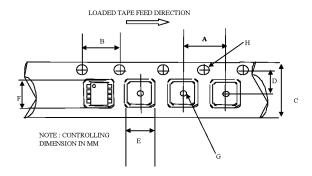
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### Case Outline - SO28



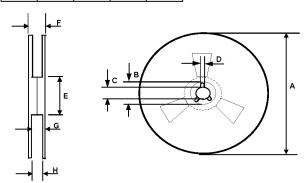


# Tape & Reel - SO28



CARRIER TAPE DIMENSION FOR 28SOICW

	Metric		Imp	erial	
Code	Min	Max	Min	Max	
Α	11.90	12.10	0.468	0.476	
В	3.90	4.10	0.153	0.161	
С	23.70	24.30	0.933	0.956	
D	11.40	11.60	0.448	0.456	
E	10.80	11.00	0.425	0.433	
F	18.20	18.40	0.716	0.724	
G	1.50	n/a	0.059	n/a	
H	1.50	1.60	0.059	0.062	

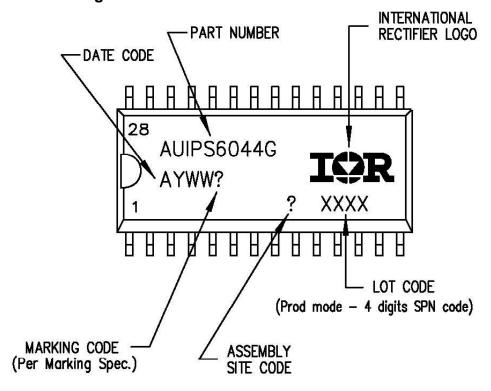


REEL DIMENSIONS FOR 28SOICW

	Metric		Imp	erial
Code	Min	Max	Min	Max
Α	329.60	330.25	12.976	13.001
В	20.95	21.45	0.824	0.844
С	12.80	13.20	0.503	0.519
D	1.95	2.45	0.767	0.096
E	98.00	102.00	3.858	4.015
F	n/a	30.40	n/a	1.196
G	26.50	29.10	1.04	1.145
Н	24.40	26.40	0.96	1.039



# **Part Marking Information**



# **Ordering Information**

Base Part Number		Standard Pack	Occupated Boot Newsham	
Base Fait Number	Package Type	Form	Quantity	Complete Part Number
ALUDS6044C	SOIC 29	Tube	30	AUIPS6044G
AUIPS6044G SOIC-28		Tape and reel	1000	AUIPS6044GTR

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#### **WORLD HEADQUARTERS:**

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**Revision History** 

Revision	Date	Notes/Changes
B2	September, 12th 2011	AU release
B3	December, 10 <sup>th</sup> 2011	Update qualification page
С	May 15, 2012	Add the test condition for the ICC (off) parameters