

#### **Integrated Current Sensor HMSR-SMS/SP3 series** $I_{PN} = 20 ... 30 A$

HMSR 20-SMS/SP3, HMSR 30-SMS/SP3 Ref:

#### **Definition**

The HMSR is a new generation of high insulated Integrated Current Sensor from LEM.

These product family provides a robust, compact and very accurate solution for measuring DC and AC currents in all highly demanding, switching power applications for commercial, industrial. HMSR is a micro core based open loop sensor with a reinforced insulation and overcurrent detections (user configurable and factory set). These features make the device suitable for high voltage applications requiring high precision and strong immunity against external field.

The primary conductor (pins 1 and 8) has very low electrical resistance and dedicated pads designed to withstand against high surge currents such as lightning strikes.

HMSR is measuring the magnetic field generated by the current flowing in the copper primary path of the device. By using a micro magnetic core, HMSR is immune to the external fields, making the device well suited for power electronic applications with high levels of disturbance.

The reinforced isolation between primary (pins 1 and 8) and secondary (pins 9 to 15) offers the HSMR, a small, cost effective solution to measure high side current compared to resistive isolated solutions.





#### **Features**

- · Open loop multi-range current transducer
- Voltage output
- Double overcurrent detection
- Galvanic separation between primary and secondary
- Low power consumption
- Compact design for surface mount PCB mounting
- Factory calibrated
- · High bandwidth, very low loss magnetic core.

#### **Special feature**

• Single power supply +3.3 V.

## Advantages

- Low profile: h = 6 mm
- Small foot-print
- Low electrical resistance 0.76 mΩ
- Reinforced insulation capability
- High resolution
- High immunity to external interference
- Lightning impulse current ≤ 20 kA.

# **Applications**

- Small drives
- **HVAC Inverters**
- **Appliances**
- Solar Inverters.

#### **Standards**

- IEC 61800-5-1: 2007
- IEC 62109-1: 2010
- IEC 60950-1: 2005
- UL 1577: 2014 (pending).

#### **Application Domains**

- Industrial
- Automotive (pending).



### Safety



#### Caution

If the device is used in a way that is not specified by the manufacturer, the protection provided by the device may be compromised. Always inspect the electronics unit and connecting cable before using this product and do not use it if damaged. Mounting assembly shall guarantee the maximum primary conductor temperature, fulfill clearance and creepage distance, minimize electric and magnetic coupling, and unless otherwise specified can be mounted in any orientation.



Caution, risk of electrical shock

This transducer must be used in limited-energy secondary circuits SELV according to IEC 61010-1, in electric/electronic equipment with respect to applicable standards and safety requirements in accordance with the manufacturer's operating specifications.

Use caution during installation and use of this product; certain parts of the module can carry hazardous voltages and high currents (e.g. power supply, primary conductor).

Ignoring this warning can lead to injury and or/or cause serious damage.

De-energize all circuits and hazardous live parts before installing the product.

All installations, maintenance, servicing operations and use must be carried out by trained and qualified personnel practicing applicable safety precautions.

This transducer is a build-in device, whose hazardous live parts must be inaccessible after installation.

This transducer must be mounted in a suitable end-enclosure.

Main supply must be able to be disconnected.

This transducer is a built-in device, not intended to be cleaned with any product. Nevertheless if the user must implement cleaning or washing process, validation of the cleaning program has to be done by the user in accordance with semi-conductor practices.



ESD susceptibility

The product is susceptible to be damaged from an ESD event and the personnel and work space should be grounded when handling it.

Do not dispose of this product as unsorted municipal waste. Contact a qualified recycler for disposal.

Although LEM applies utmost care to facilitate compliance of end products with applicable regulations during LEM product design, use of this part may need additional measures on the application side for compliance with regulations regarding EMC and protection against electric shock. Therefore LEM cannot be held liable for any potential hazards, damages, injuries or loss of life resulting from the use of this product.





# **Absolute maximum ratings**

Parameter	Symbol	Unit	Value
Maximum supply voltage	$U_{_{ m Cmax}}$	V	8
Maximum supply voltage (working) (-40 125 °C)	$U_{_{ m Cmax}}$	V	6.5
Electrostatic discharge voltage (HBM - Human Body Model)	$U_{_{ESDHBM}}$	kV	2
Electrostatic discharge voltage (CDM - Charged Device Model)	$U_{_{ESDCDM}}$	V	500
Maximum output current source	I out max	mA	25
Maximum input current sink	$I_{ m in\ max}$	mA	50
Maximum junction temperature	$T_{_{ m Jmax}}$	°C	150

Absolute maximum ratings apply at 25 °C unless otherwise noted. Stresses above these ratings may cause permanent damage.

Exposure to absolute maximum ratings for extended periods may degrade reliability.





# **Environmental and mechanical characteristics**

Parameter	Symbol	Unit	Min	Тур	Max	Comment
Ambient operating temperature	$T_{A}$	°C	-40		125	
Ambient storage temperature	$T_{_{_{ m Ast}}}$	°C	-55		165	
Resistance of the primary @ $T_A = 25 ^{\circ}\text{C}$	$R_{_{_{\mathrm{P}}}}$	mΩ		0.76		
Thermal resistance junction to case 1)	$R_{ m th\ JC}$	K/W		18		
Thermal resistance junction to ambient 1)	R th JA	K/W		19		
Mass	m	g		1.4		

# **Insulation coordination**

Parameter	Symbol	Unit	≤ Value	Comment
RMS voltage for AC insulation test, 50 Hz, 1 min	$U_{d}$	kV	4.95	According to IEC 60950-1
Impulse withstand voltage 1.2/50 μs	$U_{_{ m Ni}}$	kV	8	According to IEC 62109-1, IEC 61800-5-1
Partial discharge RMS test voltage ( $q_m$ < 5 pC)	<i>U</i> t	٧	1650	According to IEC 62109-1, IEC 61800-5-1
Clearance (pri sec.)	d	mm	8	Shortest distance through air
Creepage distance (pri sec.)	$d_{Cp}$	mm	8	Shortest path along device body
Case material	-	,	V0	According to UL 94, flamability
Comparative tracking index	CTI		600	grade requirements mas compand
Application example System voltage RMS		V	1000	Basic insulation according to IEC 61800-5-1, IEC 62109-1, IEC 60950-1 CAT III, PD2
Application example System voltage RMS		V	600	Reinforced insulation according to IEC 61800-5-1, IEC 62109-1, IEC 60950-1 CAT III, PD2
Application example System voltage DC		٧	1500	Basic insulation according to IEC 62109-1 CAT III, PD2
Application example System voltage DC		V	800	Reinforced insulation according to IEC 62109-1 CAT III, PD2

Note: 1) Done on LEM evaluation board PCB 6030: https://www.lem.com/en/hmsr-miniature-current-sensors.



# **Electrical data HMSR 20-SMS/SP3**

At  $T_A = 25$  °C,  $U_C = +3.3$  V,  $R_L = 10$  k $\Omega$ , unless otherwise noted (see Min, Max, typ, definition paragraph in page 7).

Parameter	Symbol	Unit	Min	Тур	Max	Comment
Primary nominal current	<i>I</i> P N	Α		20		
Primary current, measuring range	<i>I</i> P M	Α	-50		50	
Internal reference voltage @ I <sub>P</sub> = 0 A	$U_{ ext{out}}$ – $U_{ ext{I ref}}$	V	1.63	1.65	1.67	
Output voltage range @ I <sub>P M</sub>	$U_{ m out}$ - $U_{ m Iref}$	V	-1.15		1.15	
Output Internal resistance of $U_{\text{out}}$	$R_{out}$	Ω		2	5	
Output Internal resistance of $U_{\text{ref}}$	$R_{ref}$	Ω	120	200	333	
Load capacitance on $U_{\text{out}}$	$C_L$	nF	0	4.7	6	
Load capacitance on $U_{\text{ref}}$	$C_{L}$	nF	0	47	100	
DC supply voltage	$U_{C}$	V	3.135	3.3	3.465	
DC current consumption	$I_{\mathbb{C}}$	mA		19	25	
Nominal sensitivity	$S_{N}$	mV/A		25		500 mV @ I <sub>P N</sub>
Sensitivity error	$arepsilon_{ extsf{s}}$	%	-0.75		0.75	Factory adjustment
Sum of sensitivity and linearity error @ $T_A$ = 25 °C	€ SL25	% of $I_{\sf PN}$	-1.25		1.25	
Electrical offset voltage referred to primary	Uoe	mV	-5		5	$U_{\text{out}}$ - $U_{\text{I ref}}$ @ $U_{\text{I ref}}$ =1.65 V
Electrical offset current referred to primary	IOE	mA	-200		200	
Temperature coefficient of S	TCS	ppm/K	-250		250	−40 °C 125 °C
Temperature coefficient of $U_{\text{l ref}}$	$TCU_{I}$ ref	ppm/K	-150		150	−40 °C 125 °C
Temperature coefficient of UoE	$TCU_{OE}$	mV/K	-0.075		0.075	−40 °C 125 °C
Temperature coefficient of $I_{OE}$ referred to primary	TCI <sub>O E</sub>	mA/K	-3		3	−40 °C 125 °C
Magnetic offset current after overload ( $I_P > 500 \text{ A}$ ) referred to primary	I <sub>O M</sub>	А		0.18		
Noise voltage spectral density referred to primary	$u_{no}$	μV/Hz <sub>1/2</sub>		TBD		100 Hz 100 kHz
Internal OCD detection threshold	I I OCD Th	A		3.05 x <i>I</i> P N		
Internal OCD threshold error	E I OCD Th	%		±10		Referred to IPN
Internal OCD output on resistance	R on I OCD	Ω	70	95	100	Open drain output, active low
Linearity error 0 ±I <sub>PN</sub>	εL	%	-0.5		0.5	Referred to IPN
Linearity error 0 ±I <sub>PM</sub>	εL	%	-0.5		0.5	Referred to IPM
Delay time @ 10 % of the final output value I <sub>PN</sub> step	t D 10	μs			1.5	
Delay time @ 90 % of the final output value I <sub>PN</sub> step	<i>t</i> D 90	μs			2	
Internal OCD delay time	t DIOCD	μs	1.4	1.7	2.1	
Frequency bandwidth (-3 dB)	BW	kHz		300		
Internal OCD output hold time	t hold I OCD	μs	7	10	14	



# **Electrical data HMSR 30-SMS/SP3**

At  $T_A = 25$  °C,  $U_C = +3.3$  V,  $R_L = 10$  k $\Omega$ , unless otherwise noted (see Min, Max, typ, definition paragraph in page 7).

Parameter	Symbol	Unit	Min	Тур	Max	Comment
Primary nominal current	<i>I</i> P N	Α		30		
Primary current, measuring range	$I_{PM}$	Α	-75		75	<i>T</i> <sub>A</sub> < 75 °C
Internal reference voltage @ $I_P = 0$ A	$U_{ m out}$ – $U_{ m Iref}$	V	1.63	1.65	1.67	
Output voltage range @ I <sub>P M</sub>	$U_{ ext{out}}$ - $U_{ ext{I ref}}$	V	-1.15		1.15	
Output Internal resistance of $U_{\text{out}}$	$R_{out}$	Ω		2	5	
Output Internal resistance of $U_{\text{ref}}$	$R_{ref}$	Ω	120	200	333	
Load capacitance on $U_{\text{out}}$	$C_{L}$	nF	0	4.7	6	
Load capacitance on $U_{\text{ref}}$	$C_L$	nF	0	47	100	
DC supply voltage	$U_{C}$	V	3.135	3.3	3.465	
DC current consumption	$I_{\mathbb{C}}$	mA		19	25	
Nominal sensitivity	Sn	mV/A		16.67		500 mV @ <i>I</i> P N
Sensitivity error	<i>€</i> s	%	-0.75		0.75	Factory adjustment
Sum of sensitivity and linearity error @ $T_A$ = 25 °C	€ SL25	% of $I_{\rm PN}$	-1.25		1.25	
Electrical offset voltage referred to primary	$U_{OE}$	mV	-5		5	$U_{ m out}$ - $U_{ m Iref}$ @ $U_{ m Iref}$ =1.65 V
Electrical offset current referred to primary	IOE	mA	-300		300	
Temperature coefficient of S	TCS	ppm/K	-250		250	−40 °C 125 °C
Temperature coefficient of Ulref	$TCU_{I}$ ref	ppm/K	-150		150	−40 °C 125 °C
Temperature coefficient of UoE	TCU <sub>OE</sub>	mV/K	-0.075		0.075	−40 °C 125 °C
Temperature coefficient of $I_{OE}$ referred to primary	TCI <sub>OE</sub>	mA/K	-4.5		4.5	−40 °C 125 °C
Magnetic offset current after overload ( $I_P > 500 \text{ A}$ ) referred to primary	I <sub>O M</sub>	А		0.18		
Noise voltage spectral density referred to primary	$u_{no}$	µV/Hz1/2		TBD		100 Hz 100 kHz
Internal OCD detection threshold	$I_{ ext{I OCD Th}}$	А		3.05 x I <sub>P N</sub>		
Internal OCD threshold error	€ I OCD Th	%		±10		Referred to IPN
Internal OCD output on resistance	R	Ω	70	95	100	Open drain output, active low
Linearity error 0 ±I <sub>PN</sub>	€L	%	-0.5		0.5	Referred to IPN
Linearity error 0 ±I <sub>PM</sub>	εL	%	-0.5		0.5	Referred to IP M
Delay time @ 10 % of the final output value $I_{PN}$ step	<i>t</i> D 10	μs			1.5	
Delay time @ 90 % of the final output value I <sub>PN</sub> step	t D 90	μs			2	
Internal OCD delay time	t DIOCD	μs	1.4	1.7	2.1	
Frequency bandwidth (-3 dB)	BW	kHz		300		
Internal OCD output hold time	t hold I OCD	μs	7	10	14	

Note: 1) See figure 2.



## Sum of sensitivity and linearity error

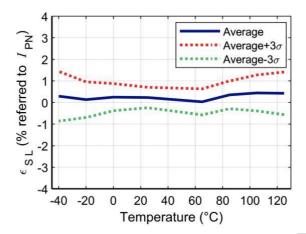
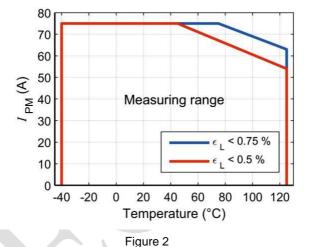


Figure 1: Typical value on HMSR 20-SMS/SP3

# Measuring range vs ambient temperature



# Definition of typical, minimum and maximum values

Minimum and maximum values for specified limiting and safety conditions have to be understood as such as well as values shown in "typical" graphs.

On the other hand, measured values are part of a statistical distribution that can be specified by an interval with upper and lower limits and a probability for measured values to lie within this interval.

Unless otherwise stated (e.g. "100 % tested"), the LEM definition for such intervals designated with "min" and "max" is that the probability for values of samples to lie in this interval is 99.73 %.

For a normal (Gaussian) distribution, this corresponds to an interval between -3 sigma and +3 sigma. If "typical" values are not obviously mean or average values, those values are defined to delimit intervals with a probability of 68.27 %, corresponding to an interval between -sigma and +sigma for a normal distribution.

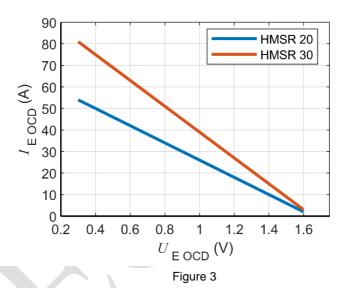
Typical, maximal and minimal values are determined during the initial characterization of the product.



## **External overcurrent detection**

	Comple of	Unit		Specification	O and distance	
Parameter	Parameter Symbol		Min	Typical	Max	Conditions
External OCD voltage	U E OCD	V	0.3		2.5	
External OCD output on resistance to	R					Open drain output,
ground	on E OCD	Ω	35	200	300	active low
	t					To be added to the
External OCD delay time	D E OCD	μs	7	10	13	sensor delay time
External OCD output hold time	t hold E OCD	μs	7	10	14	
External OCD threshold error	EOCD	%		±6		Referred to IP N

# $I_{E \ OCD}$ : External overcurrent detection (OCD threshold) at $U_{ref}$ = 1.65 V

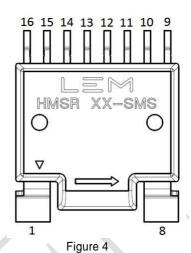


$$I_{\text{E OCD}} = \underbrace{U_{\text{ref}} - U_{\text{E OCD}}}_{\text{F OCD}} \qquad U_{\text{E OCD}} = \underbrace{R_{\text{E}}}_{\text{E}} \times U_{\text{ref}}$$
 with  $0.3 \le U_{\text{E OCD}} \le U_{\text{ref}}$ 



## **Pins definition**

Pins number	Name	Description		
1	I <sub>P</sub> +	Input of the primary current		
8	I <sub>P</sub> -	Output of the primary current		
9	OCD INT	Internal OCD		
10	<i>U</i> c	Supply voltage		
11	$U_{ m ref}$	Reference voltage		
12	$U_{_{ m out}}$	Output voltage		
13	NC	No internal connection		
14	OCD EXT	External OCD terminal		
15	GND	Ground terminal		
16	$U_{_{EOCD}}$	External OCD threshold voltage terminal		



# **Block diagram**

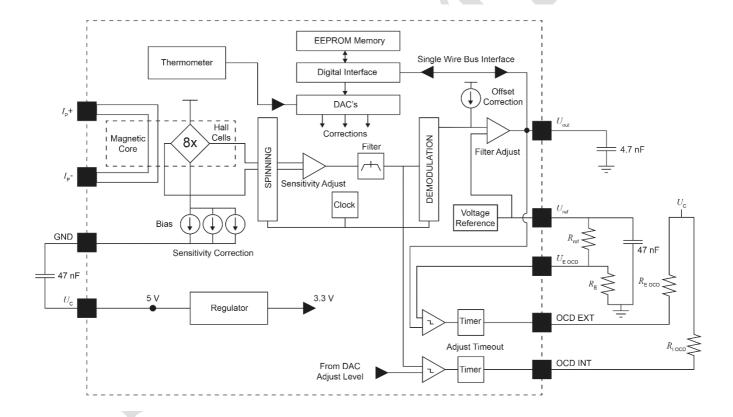


Figure 5



# **Terms and definitions**

#### **HMSR-SMS/SP3** series

## Total error referred to primary

The total error  $\varepsilon_{\rm tot}$  is the error at  $\pm I_{\rm P \ N}$ , relative to the rated value  $I_{\rm P \ N}$ .

It includes all errors mentioned above

- the electrical offset Io E
- the magnetic offset Io M
- the sensitivity error εs

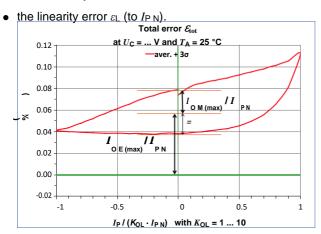


Figure 6: Total error  $\varepsilon_{tot}$ 

## **Electrical offset referred to primary**

Using the current cycle shown in figure 7, the electrical offset current  $I_{0E}$  is the residual output referred to primary when the input current is zero.

# **Magnetic offset referred to primary**

$$I = \frac{I + I}{P(3) P(5)}$$
oe
2

The magnetic offset current  $I_{OM}$  is the consequence of a current on the primary side ("memory effect" of the transducer's ferromagnetic core). It is measured using the following primary current cycle.  $I_{OM}$  depends on the current value  $I_P \ge I_{PN}$ .

Kol: Overload factor

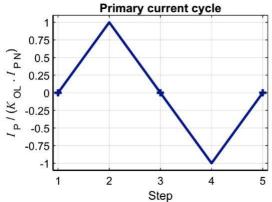


Figure 7: Current cycle used to measure magnetic and electrical offset (transducer supplied)

$$I_{\text{OM}} = \frac{I_{\text{P(3)}} - I_{\text{P(5)}}}{2}$$

### Sensitivity and linearity

To measure sensitivity and linearity, the primary current (DC) is cycled from 0 to  $I_P$ , then to  $-I_P$  and back to 0 (equally spaced  $I_P$  N/10 steps). The sensitivity S is defined as the slope of the linear regression line for a cycle between  $\pm I_P$  N.

The linearity error  $\varepsilon_L$  is the maximum positive or negative difference between the measured points and the linear regression line, expressed in % of  $I_{PN}$ .

## **Delay times**

The delay time  $t_{\rm D~10}$  @ 10 % and the delay time  $t_{\rm D~90}$  @ 90 % with respect to the primary are shown in the next figure. Both slightly depend on the primary current di/dt.

They are measured at nominal current.

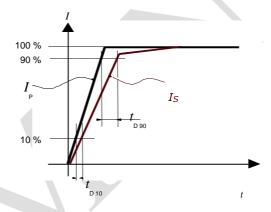


Figure 8: tD 10 (delay time @ 10 %) and

t <sup>D 90</sup> (delay time @ 90 %).

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# PCB footprint (in mm)

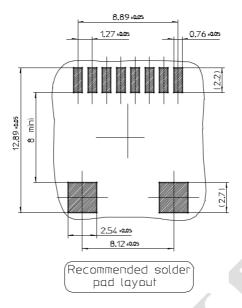
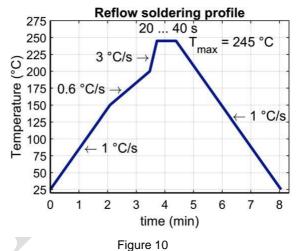


Figure 9

# **Soldering on PCB**

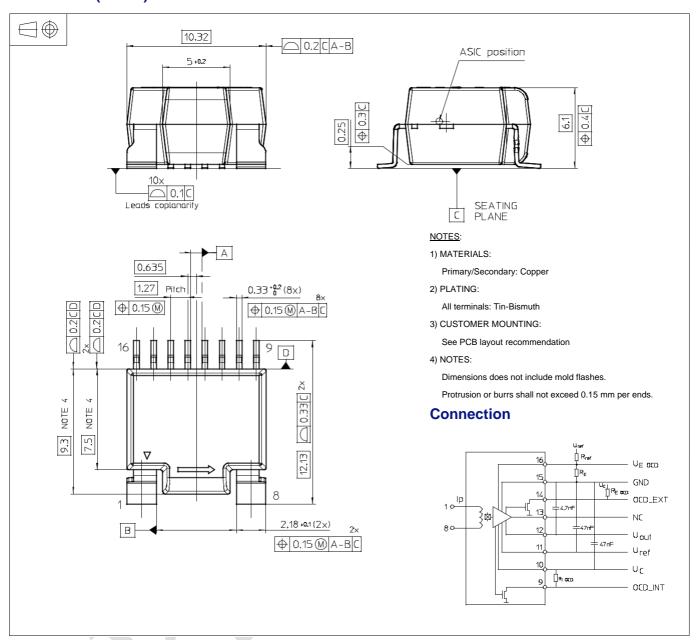


- HMSR is qualified MSL1 for storage and mounting purposes.
- Per JEDEC J-STD-020E for packages more than 2.5 mm thick per table 4.2 (Pb-Free Process) of the specification.
- Best practice is to use 7 zones or greater conventional reflow system, limiting the time at reflow temperature as indicated
  in profile above.
- Rework not recommended.

**Soldering remarks:** 



# **Dimensions (in mm)**



## **Mechanical characteristics**

• General tolerance ±0.15 mm

#### Remark

 U<sub>out</sub> – U<sub>ref</sub> is positive when I<sub>P</sub> flows in the direction of arrow (pin 1 to pin 8).



# Tape and Reel (in mm)

